

RVT70HSMFWCA0

IPS MIPI 7.0" LCD TFT DATASHEET

Rev.1.0 2021-08-02

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally Black/IPS	/
Size	7.0	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	181.60 x 100.60 x 8.68	mm
Active Area (W x H)	154.21 x 85.92	mm
Pixel Pitch (W x H)	0.1506 x 0.1432	mm
Resolution	1024 (RGB) x 600	/
Brightness	800	cd/m ²
LCD Interface Type	MIPI	/
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
LCD Driver	EK79007AD3+EK73217BCGA	/
With/Without Touch	With Projected Capacitive Touch Panel	/
CTP Driver	ILI2132A	/
Touch Interface	USB /I2C/Optional UART	/
Weight	246	g

Note 1: RoHS3 compliant

Note 2: LCM weight tolerance: ± 5%.



1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2021-08-02	Initial Release	



2. CONTENTS

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3. MODULE CLASSIFICATION INFORMATION

RV	т	70	н	S	М	F	W	С	A0
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	70 – 7.0"
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	S – 1024 x 600 px
6.	INTERFACE	M – MIPI
7.	FRAME	F – With Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – With Capacitive Touch Panel, aTouch
10.	VERSION	A0 – aTouch



4. ASSEMBLY GUIDE

4.1 Mounting frame

For dimensions 3.5", 4.3", 5.0", 7.0" and 10.1" the product with mounting frame version is available. Thanks to the four catches attached to the side, frame provides strong assembly to the surface by mounting element (like the screw, see Figure 1). The frames are specially designed to fit Riverdi products perfectly. The diameter of the mounting hole is 3.5mm.

Figure 1. Mounting frame







6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Power for Circuit Driving	VDD	-0.3	+4.6	V
Operating Temperature	T _{OP}	-20	70	°C
Storage Temperature	T _{ST}	-30	80	°C
Storage Humidity (@ 25 ± 5°C)	H _{st}	10	-	% RH
Operating Ambient Humidity (@ 25 ± 5°C)	Hop	10	-	% RH

Operating Ambient Humidity (@ $25 \pm 5^{\circ}$ C)H_{OP}10-% RHNote. The above are maximum values. If exceeded, they may cause permanent damage to
the unit.

7. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage for Module	VDD	2.6	3.3	3.6	V
Digital Power Current	IDD	-	30	45	mA
Logic Input Signal Voltago	VIH	0.7VDD	-	VDD	V
Logic Input Signal Voltage	VIL	0	-	0.3VDD	V

8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Backlight Driving Voltage	VF	9.0	9.6	10.2	V
Backlight Driving Current	IF	-	270	315	mA
Backlight Power			2592		m\\/
Consumption	VVDL	-	2392	-	11177
Lifetime	-	-	50,000	-	

Note. Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.



9. ELECTRO-OPTICAL CHARACTERISTICS

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf		-	35	-	ms	FIG 2.	4
Contrast Ratio	Cr	0-0°	-	800	-		FIG 3.	1
Luminance Uniformity	δ WHITE	θ=0° ø=0° Ta=25 °C	-	75	-	%	FIG 3.	3
Surface Luminance	Lv		-	800	-	cd/m2	FIG 3.	2
Viewing Angle Range	θ	ø = 90°	-	85	-	deg	FIG 4.	
		ø = 270°	-	85	-	deg	FIG 4.	6
		ø = 0°	-	85	-	deg	FIG 4.	
		ø = 180°	-	85	-	deg	FIG 4.	
	Rx		0.578	0.618	0.658	-		
	Ry		0.489	0.329	0.369	-	_	
	Gx	0-00	0.376	0.416	0.456	-		
CIE (x, y)	Gy	Ø=0°	0.493	0.533	0.573	-		E
Chromaticity	Bx	= 0 Ta=25 °C	0.071	0.111	0.151	-	FIU J.	5
	Ву	1a-23 C	0.108	0.148	0.188	-		
	Wx		0.270	0.310	0.350	-		
	Wy		0.290	0.330	0.370	-		

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 3.

Contrast Ratio = $\frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 3.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

$$\delta \text{ WHITE } = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.



Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see figure 4.

.Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.





Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity









10. INTERFACE DESCRIPTION

10.1 TFT assignment

1 NC - No connection 2 VDD P Power supply 33V 3 VDD P Power supply 33V 4 NC - No connection 5 RESET 1 Global reset pin. Active low to enter reset state. NOTE1 6 STBYB 1 STBVB=1, normal operation. STBVB=0, timing control, source driver will turn off 7 GND P Cround - 8 DON I/O Positive MIPI differential data input - 9 DOP I/O Positive MIPI differential data input - 11 DIN I/O Negative MIPI differential data input - 12 DIP I/O Negative MIPI differential data input - 13 GND P Ground - 14 D2N I/O Negative MIPI differential data input - 15 D2P I/O Positive MIPI differential clock input - 16 GND P Ground - - 17 DCLKN I/	PIN NO.	SYMBOL	I/O	DESCRIPTION	NOTE
2 VDD P Power supply 3.3V 3 VDD P Power supply 3.3V 4 NC - No connection 5 RESET I Global reset pin. Active low to enter reset state. NOTE 1 6 STBYB I STBYB=1, normal operation. STBYB=0, timing control, source driver will turn off 7 GND P Ground	1	NC	-	No connection	
3 VDD P Power supply 3.3V 4 NC - No connection 5 RESET I Global reset pin. Active low to enter reset state. NOTE 1 6 STBYB I STBYB-1, normal operation. STBYB-1, ormal operation. STBYB-1, ormal operation. 7 GND P Ground - - 8 DON I/O Negative MIPI differential data input - 9 DOP I/O Positive MIPI differential data input - 10 GND P Ground - 11 DIN I/O Negative MIPI differential data input - 12 DIP I/O Positive MIPI differential data input - 13 GND P Ground - - 14 D2N I/O Negative MIPI differential data input - 15 D2P I/O Positive MIPI differential clock input - 16 GND P Ground - - 17 DCLKN I/O Negative MIPI differentia	2	VDD	Р	Power supply 3.3V	
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5 RESET I Global reset pin. Active low to enter reset state. NOTE 1 6 STBYB I Internally pull-up, Internally pull-up, 6 STBYB I STBYB-0, timing control, source driver will turn off 7 GND P Ground Image: control, source driver will turn off 8 DON I/O Negative MIPI differential data input Image: control, source driver will turn off 9 DOP I/O Negative MIPI differential data input Image: control, source driver will turn off 10 GND P Ground Image: control, source driver will turn off 11 DIN I/O Negative MIPI differential data input Image: control, source driver will turn off 12 DIP I/O Negative MIPI differential data input Image: control, source driver will turn off 13 GND P Ground Image: control, source driver will turn off Image: control, source driver will turn off 14 D2N I/O Negative MIPI differential data input Image: control, source driver will turn off 15 D2P I/O Positive MIPI differential clock input Imag	4	NC	-	No connection	
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29NC-No connection30GNDPGround31LED-PLED Cathode32LED-PLED Cathode33L/RILeft/Right display control, internally pull-upNote 234U/DIUp/Down display control, internally pull downNote 235NC-No connection36NC-No connection37NC-No connection38NC-No connection39LED+PLED Anode40LED+PLED Anode	28	NC	-	No connection	
30GNDPGroundImage: Constraint of the system31LED-PLED CathodeImage: Constraint of the systemImage: Constraint of the system32LED-PLED CathodeImage: Constraint of the systemImage: Constraint of the system33L/RILeft/Right display control, internally pull-upImage: Constraint of the systemImage: Constraint of the system34U/DIUp/Down display control, internally pull downImage: Constraint of the systemImage: Constraint of the system35NC-No connectionImage: Constraint of the systemImage: Constraint of the system36NC-No connectionImage: Constraint of the systemImage: Constraint of the system37NC-No connectionImage: Constraint of the systemImage: Constraint of the system38NC-No connectionImage: Constraint of the systemImage: Constraint of the system39LED+PLED AnodeImage: Constraint of the systemImage: Constraint of the system40LED+PLED AnodeImage: Constraint of the systemImage: Constraint of the system	29	NC	-	No connection	
31LED-PLED CathodeImage: Second constraints of the second constraints	30	GND	Р	Ground	
32LED-PLED CathodeI33L/RILeft/Right display control, internally pull-upNote 234U/DIUp/Down display control, internally pull downNote 235NC-No connectionI36NC-No connectionI37NC-No connectionIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	31	LED-	Р	LED Cathode	
33L/RILeft/Right display control, internally pull-upNote 234U/DIUp/Down display control, internally pull downNote 235NC-No connection36NC-No connection37NC-No connection38NC-No connection39LED+PLED Anode40LED+PLED Anode	32	LED-	Р	LED Cathode	
34U/DIUp/Down display control, internally pull downNote 235NC-No connection36NC-No connection37NC-No connection38NC-No connection39LED+PLED Anode40LED+PLED Anode	33	L/R	I	Left/Right display control, internally pull-up	
35NC-No connection36NC-No connection37NC-No connection38NC-No connection39LED+PLED Anode40LED+PLED Anode	34	U/D	I	Up/Down display control, internally pull down	Note 2
36NC-No connection37NC-No connection38NC-No connection39LED+PLED Anode40LED+PLED Anode	35	NC	-	No connection	
37NC-No connection38NC-No connection39LED+PLED Anode40LED+PLED Anode	36	NC	-	No connection	
38NC-No connection39LED+PLED Anode40LED+PLED Anode	37	NC	-	No connection	
39 LED+ P LED Anode 40 LED+ P LED Anode	38	NC	-	No connection	
40 LED+ P LED Anode	39	LED+	Р	LED Anode	
	40	LED+	Р	LED Anode	

l: input, O: output, P: Power



Note 1: Recommended Reset circuit:



Note 2: U/D (Pin 34) is internally pull-down, and R/L (Pin 33) is internally pull-up. The default scanning direction is up to down, left to right.

SETTING OF SCAN CONTROL	SCANNING DIRECTION	
U/D	L/R	
GND	VDD	Up to down, left to right
VDD	GND	Down to up, right to left
GND	GND	Up to down, right to left
VDD	VDD	Down to up, left to right

Definition of scanning direction, refer to the figure as below:



10.2 Touch panel assignment

PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	USB_GND	USB_Ground	
2	USB_VDD	USB Power for CTP, 5.0V	
3	USB_D-	USB _Data Signal –	
4	USB_D+	USB _Data Signal +	
5	I2C_GND	I2C _ Ground	
6	I2C_VDD	I2C _Power for CTP, 3.3 V	
7	I2C_RST	I2C _Reset Pin, Active low	
8	I2C_SCL	I2C _Clock Input	Note 1
9	I2C_INT	I2C _Interrupt Signal from CTP, Active low	
10	I2C_SDA	I2C _Data Signal	

Note 1. External pull-up resistors are required.



10.3 CON1 assignment

PIN NO.	SYMBOL	DESCRIPTION
1	USB_VDD	USB_Power for CTP, DC 5.0V
2	USB_D-	USB _Data Signal -
3	USB_D+	USB _Data Signal +
4	USB_GND	USB_Ground

11. TIMING CHARACTERISTICS

11.1 Low power transmitter AC characteristic

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT
15%~85% rising time	e and falling time	T _{rlp} /T _{flp}	-	-	25	
30%~85% rising tim	e and falling time	T _{reot}	-	-	35	
Pulse width of LP exclusive -OR	First LP EXOR clock pulse after STOP state or LAST pulse before stop state	T _{LP-PULSE-TX}	40	-	-	ns
clock	All other pulses		20	-	-	
Period of the LP EXOR clock		T _{LP-PER-TX}	90	-	-	
Slew Rate @CLOAD =0pF		- δV /δt _{sr}	30	-	500	mV/ns
Slew Rate @CLOAD =5pF			30	-	200	
Slew Rate @CLOAD =20pF			30	-	150	
Slew Rate @CLOAD =70pF			30	-	100	
Load Capacitance		T _{RLP}	-	-	70	рF





11.2 High speed transmission

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
UI instantaneous	UI _{INST}	2	-	12.5	ns
Data to Clock Skew (measured at transmitter)	T _{skew(tx)}	-0.15	-	0.15	
Data to Clock Setup time	T _{SETUP(RX)}	0.15	-	-	UI _{INST}
(Measured at receiver)					
Data to Clock Hold time	T _{hold(RX)} T _r ,T _f	0.15	-		
(Measured at receiver)					
20%~80% rising time and falling time		150	-	-	ps
		-	-	0.3	Ul _{inst}





11.3 High speed clock transmission

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Time that the transmitter shall continue sending HS clock after the last associated Data Lane has transitioned to LP mode	T _{CLK-POST}	60+52UI	-	-	
Detection time that the clock has stopped toggling	T _{CLK-MISS}	-	-	60	
Time to drive LP-00 to prepare for HS clock transmission	T _{clk} -prepare	38	-	95	ns
Minimum lead HS-0 drive period before starting clock	T _{clk-prepare+} T _{clk-zero}	300	-	-	
Time to enable Clock Lane receiver line termination measured from when Dn cross $V_{\text{IL,MAX}}$	T _{HS-TERM-EN}	-	-	38	
Minimum time that the HS clock must be prior to any associated data lane beginning the transmission from LP to HS mode	T _{CLK-PRE}	8	-	-	UI
Time to drive HS differential state after last payload clock bit of a HS transmission burst	T _{CLK-TRAIL}	60	-	-	ns





11.4 High speed data transmission in bursts



12.CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

12.1 Mechanical characteristics

DESCRIPTION	SPECIFICATION	REMARK			
Touch Panel Size	7.0 inch				
Outline Dimension of CTP	164.70 mm x 99.80 mm				
Product Thickness	2.45 mm				
Glass Thickness	1.1 mm	aTouch			
CTP View Area	155.01 mm x 86.72 mm				
Sensor Active Area	156.68 mm x 88.52 mm				
Structure type	Glass + Glass				
Surface Hardness	7H				

12.2 Electrical characteristics

PARAMETER		SPECIFICATION	REMARK		
Operating Voltage		DC 5.0 V (USB)			
Operating voltage		DC 3.3 V (I2C)			
Power	Active Mode	90 mA			
Consumption (IDD) Sleep Mode		10 mA	aTouch		
Interface	·	USB / I2C /Optional UART			
Linearity		+/-1.5mm			
Controller		ILI2132A			
I2C address		0x82			
Resolution		1024 x 600			



13. INITIALIZATION CODE

DCS_Short_Write_NP(0x01); //Software Reset Delay (120); Generic_Short_Write_1P(0x87,0x5A); //Other commands will not work if this command is nor written Generic_Short_Write_1P(0xB2,0x70); //Set Channels 2LANE:0x50; 3LANE:0x60; 4LANE:0x70 Generic_Short_Write_1P(0x80,0x4B); //Set Gamma voltage Generic_Short_Write_1P(0x81,0xFF); //Set Gamma voltage Generic_Short_Write_1P(0x82,0x1A); //Set Gamma voltage Generic_Short_Write_1P(0x83,0x88); //Set Gamma voltage Generic_Short_Write_1P(0x84,0x8F); //Set Gamma voltage Generic_Short_Write_1P(0x85,0x35); //Set Gamma voltage Generic_Short_Write_1P(0x86,0xB0); //Set Gamma voltage DCS_Short_Write_NP(0x11); //Exit Sleep Mode Delay (120); DCS_Short_Write_NP(0x29); //Display on; 0x28 is display off Delay (20);



14. INSPECTION

Standard acceptance/rejection criteria for TFT module

14.1 Inspection condition

Ambient conditions:

- Temperature: 25 ± 2°C
- Humidity: (60 ± 10) %RH
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance: 35 ± 5cm between inspector bare eye and LCD.

Viewing Angle: U/D: 45°/45°, L/R: 45°/45°





14.2 Inspection standard

ITEM		CRITE	RIO	٧			
		Size = 7"					
Plack spots	× ×	Average Diameter			Qu	Qualified Qty	
white spots		D ≤ 0.2 mm			lgn	Ignored	
light leakage,		0.2 mm < D	≤ 0.3	mm	N≤	N≤3	
(round Type)		0.5mm < D			Not	Not allowed	
	D=(x+y)/2 Spot's density: 10 mm	0.5mm < D					
	Width	Size = 7"					
		Length W		Wi	dth	Qualified Qty	
LCD black spots, white spots,		-		W ≤	0.05	Ignored	
light leakage (line Type)	Length	L ≤ 5.0		0.05 <	W ≤ 0.1	3	
(5.0 < L		0.1 < W		Not allowed	
		Size	= 7"				
Dui subst/Dauly	ltem		Qualified Qty				
Bright/Dark	Bright dots		N≤2				
DOIS	Dark dots		N≤3				
	Total bright and dark dots			N≤4			
	Size = 7"						
	Average Diameter			Qualified Qty			
Clear spots	D < 0.2 mm				Ignore	d	
	0.2 mm < D < 0.3 mm		4				
	0.3 mm < D < 0.5 mm		2				
	0.5 mm < D		0				
	Size = 7"						
Polarizer	Average Diameter			Qualified Qty			
bubbles	D ≤ 0.2 mm		Ignored				
	0.2 mm < D ≤ 0.5 mm		4				
	0.5 mm < D	<u> </u>	> F "		0		
	Size 2		20				
Touch panel	Average Diameter						
spot	D < 0.25 mm		m /				
	0.23 mm < D						
	0.5 11111 4 D	Size	> 5"		0		
Touch panel	Length Width		Qualified Otv				
White line	- W(<0.03						
Scratch	L < 5.0	0.03 < W < 0	0.05 2				
	-	0.05 < W			0		



15.RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION
1	High Temperature Storage	80°C/120 hours
2	Low Temperature Storage	-30°C/120 hours
3	High Temperature Operating	70 °C /120 hours
4	Low Temperature Operating	-20°C/120 hours
5	High Temperature and High Humidity	Humidity 40°C, 90%RH, 120Hrs
		-20°C for 30min, 70°C for 30 min.
6	Thermal Cycling Test (No operation)	100 cycles. Then test at room
		temperature after 1 hour
		Frequency: 10 ÷ 55 Hz.
7		Stroke: 1.5 mm.
	Vibration Test	Sweep: 10Hz ÷ 55Hz ÷ 10 Hz.
		2 hours for each direction of X, Y, Z
		(Total 6 hours)
0	Daelyage Drep Test	Height: 60 cm
0	Package Drop Test	1 corner, 3 edges, 6 surfaces
		Air: ±2 kV,
9	ESD Test	Human Body Mode,
		100 pF /1500 Ω

Note 1. Sample quantity for each test item is 5 ÷ 10 pcs.

Note 2. Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



16.LEGAL INFORMATION

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