ShinDengen

P70F5EN

Power MOSFETs
50V, 70A, N-channel

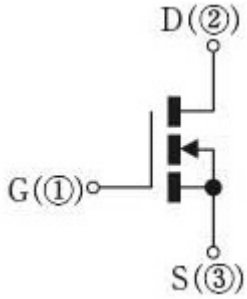
Feature

- N-channel
- Isolated Package
- Low Ron
- 10V Gate Drive
- Low Capacitance
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FTO-220AG
Package (JEITA Code): SC-91

Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25℃)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 150	℃
Channel tempertature	Tch		150	℃
Drain-source voltage	V_{DSS}		50	V
Gate-source voltage	V_{GSS}		±20	V
Continuous drain current(DC)	I_D		70	A
Continuous drain current(Peak)	I_{DP}	Pulse width 10μs, duty=1/100	280	A
Total power dissipation	P_T		53	W
Single avalanche current	I_{AS}	Starting Tch=25℃ Tch≦150℃	70	A
Single avalanche energy	E_{AS}	Starting Tch=25℃ Tch≦150℃	245	mJ
Dielectric strenght	Vdis	Terminals to case, AC1min	2	kV
Mounting torque	TOR	(Recommended torque：0.3N･m)	0.5	N･m

※ ：See the original Specifications

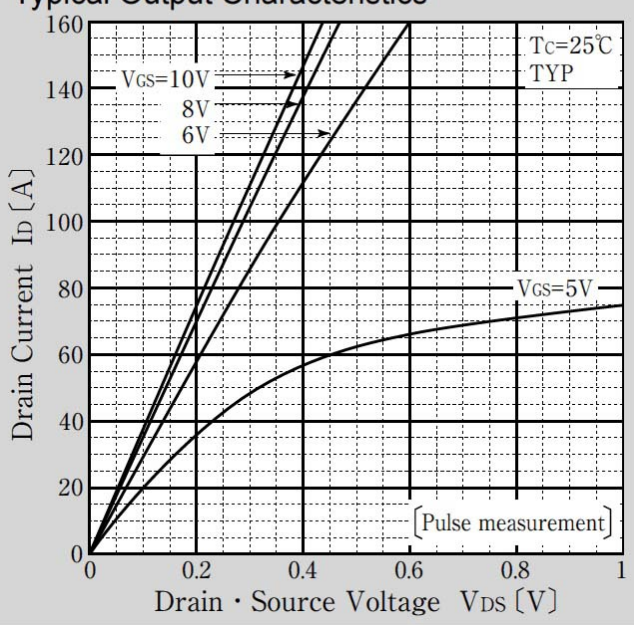
Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	50			V
Zero gate voltage drain current	I_{DSS}	VDS=50V, VGS=0V			1	μA
Gate-source leakage current	I_{GSS}	VGS=±20V, VDS=0V			±0.1	μA
Forward transconductance	g_{fs}	ID=35A, VDS=10V	25			S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=35A, VGS=10V		0.0027	0.0032	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	2	3	4	V
Source-drain diode forward voltage	V_{SD}	IS=70A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case			2.35	°C/W
Total gate charge	Qg	VDD=40V, VGS=10V, ID=70A		100		nC
Gate to source charge	Qgs	VDD=40V, VGS=10V, ID=70A		26		nC
Gate to drain charge	Qgd	VDD=40V, VGS=10V, ID=70A		38		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		5500		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		440		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		955		pF
Turn-on delay time	td(on)	ID=35A, RL=0.71Ω, VDD=25V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		13		ns
Rise time	tr	ID=35A, RL=0.71Ω, VDD=25V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		45		ns
Turn-off delay time	td(off)	ID=35A, RL=0.71Ω, VDD=25V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		53		ns
Fall time	tf	ID=35A, RL=0.71Ω, VDD=25V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		44		ns
Diode reverse recovery time	trr	IF=70A, VGS=0V, di/dt=100A/μs		52		ns
Diode reverse recovery charge	Qrr	IF=70A, VGS=0V, di/dt=100A/μs		86		nC

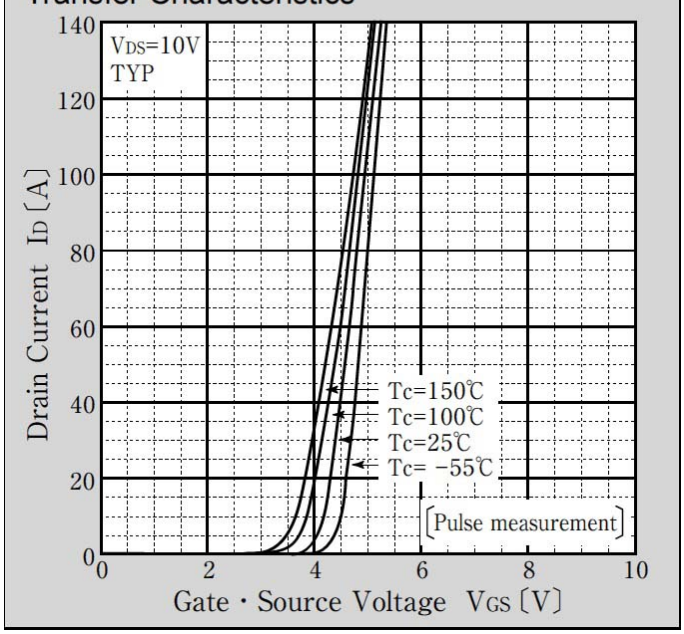
※ ：See the original Specifications

CHARACTERISTIC DIAGRAMS

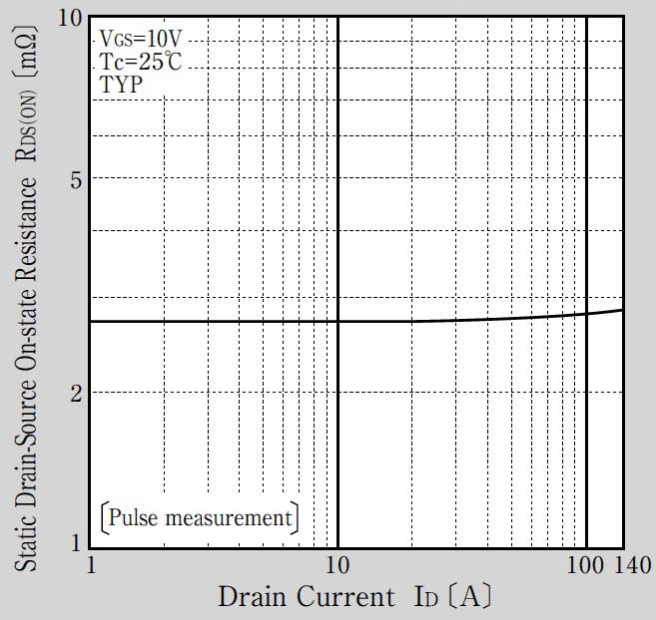
Typical Output Characteristics



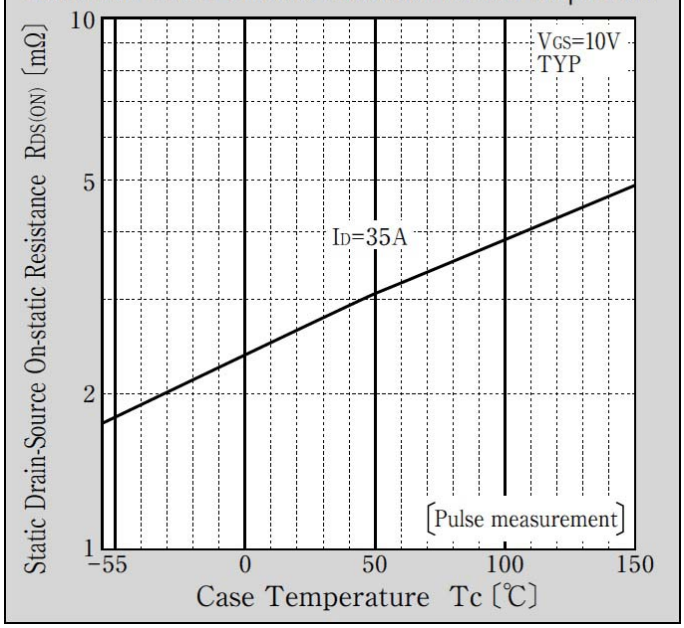
Transfer Characteristics



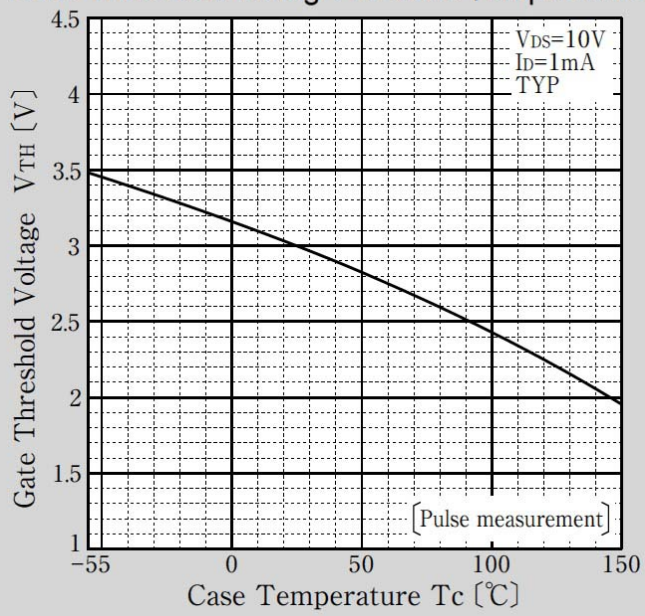
Static Drain-Source On-state Resistance vs Drain Current



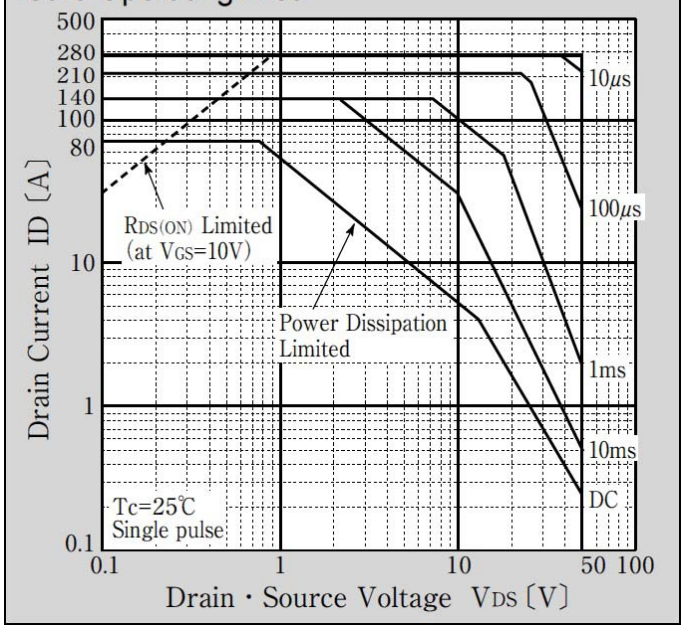
Static Drain-Source On-static Resistance vs Case Temperature



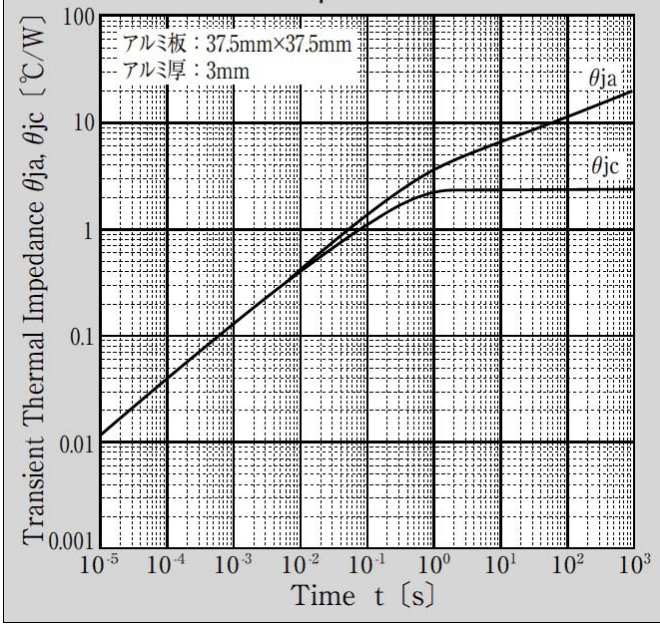
Gate Threshold Voltage vs Case Temperature



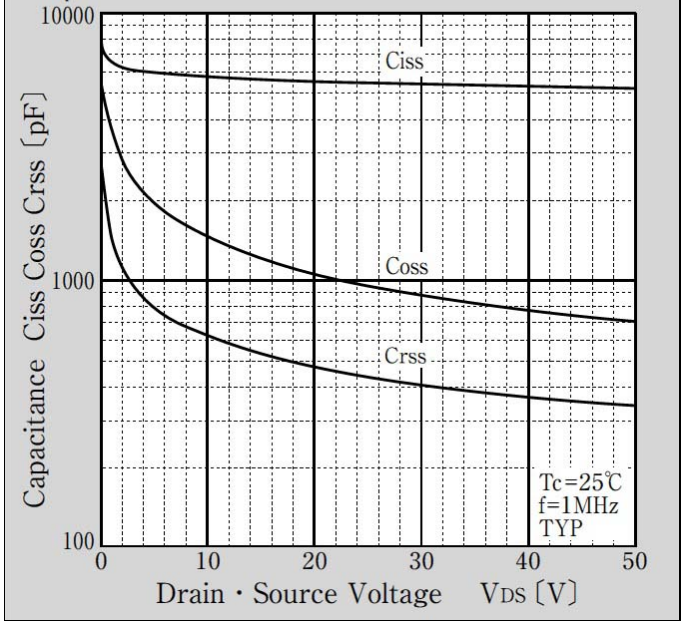
Safe Operating Area

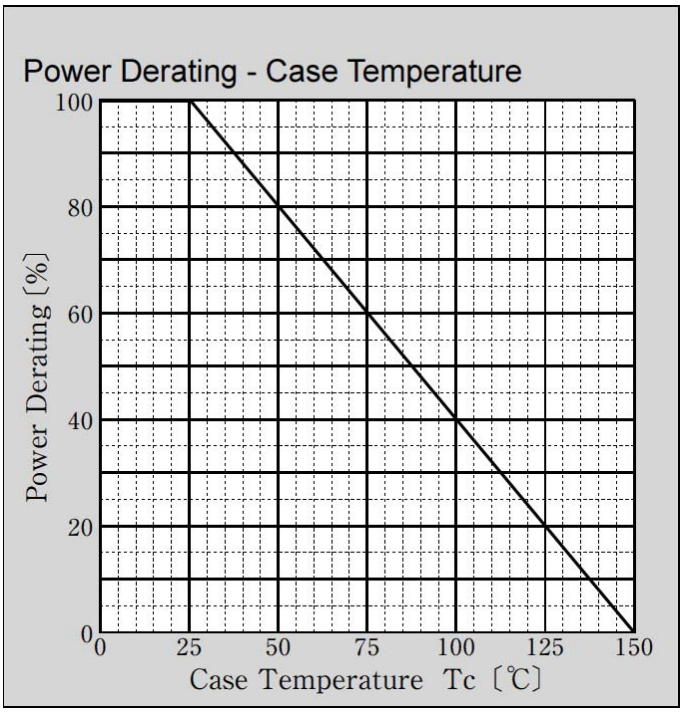


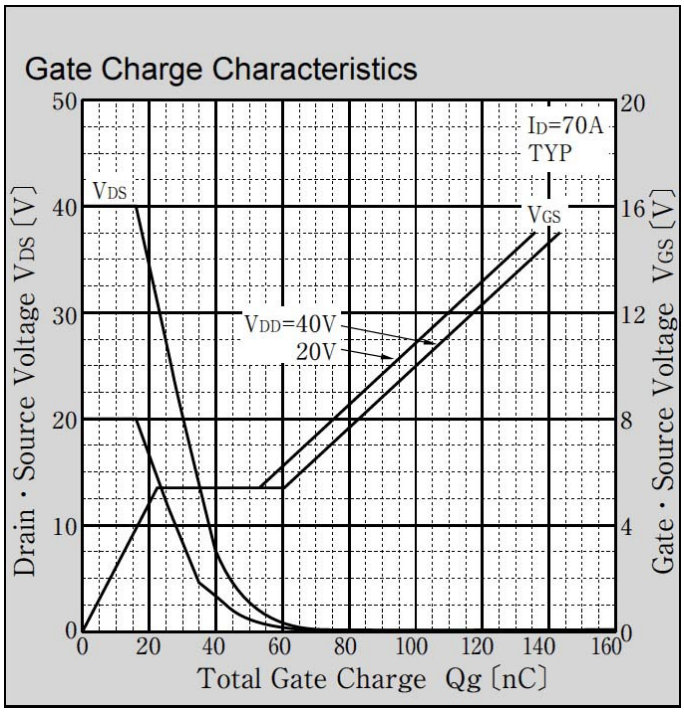
Transient Thermal Impedance

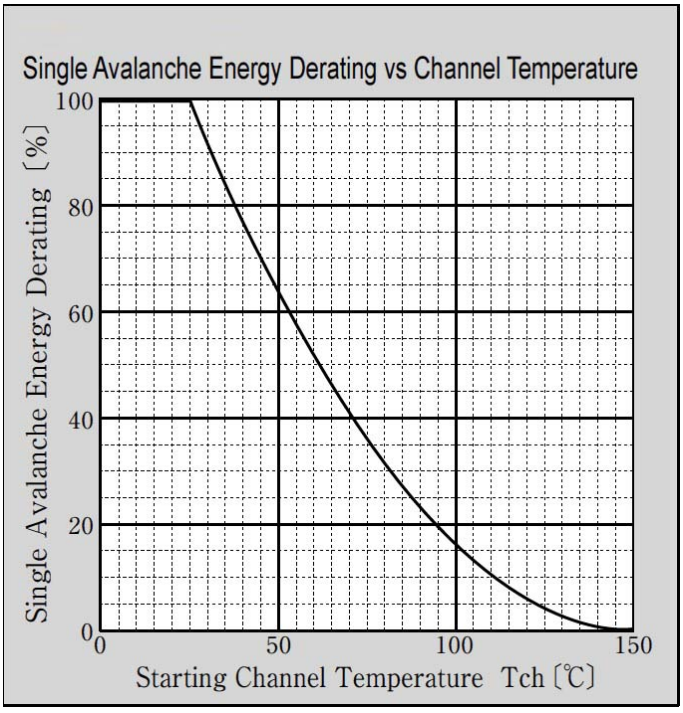


Capacitance Characteristics




Power Derating - Case Temperature
Power Derating〔%〕
100
80
60
40
20
0
0
25
50
75
100
125
150
Case Temperature Tc〔℃〕


Gate Charge Characteristics
Drain・Source Voltage VDS〔V〕
Gate・Source Voltage VGS〔V〕
ID=70A
TYP
VDS
VGS
VDD=40V
20V
50
40
30
20
10
0
20
16
12
8
4
0
0
20
40
60
80
100
120
140
160
Total Gate Charge Qg〔nC〕


Single Avalanche Energy Derating vs Channel Temperature
Single Avalanche Energy Derating〔%〕
100
80
60
40
20
0
0
50
100
150
Starting Channel Temperature Tch〔℃〕

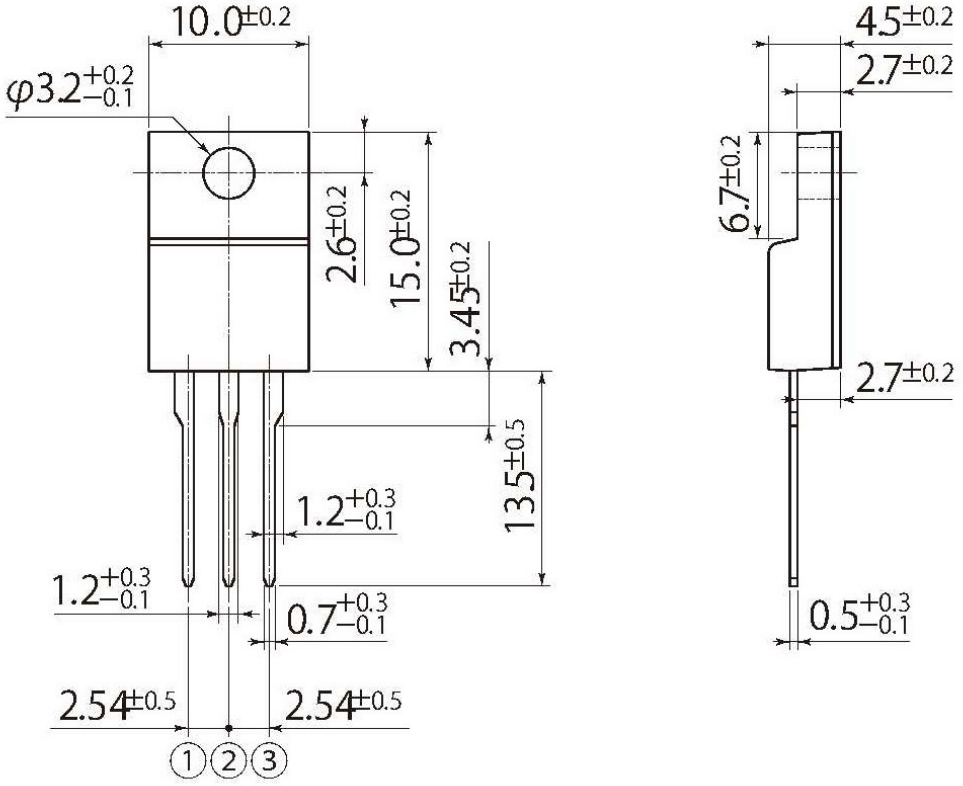
Outline Dimensions

unit:mm

scale: 2/1

J8

JEDEC Code	–
JEITA Code	SC-91
House Name	FTO-220AG(3pin)



Notes

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