B-LCDAD-HDMI1

DSI to HDMI adapter board for STM32 Development Tools

Data brief

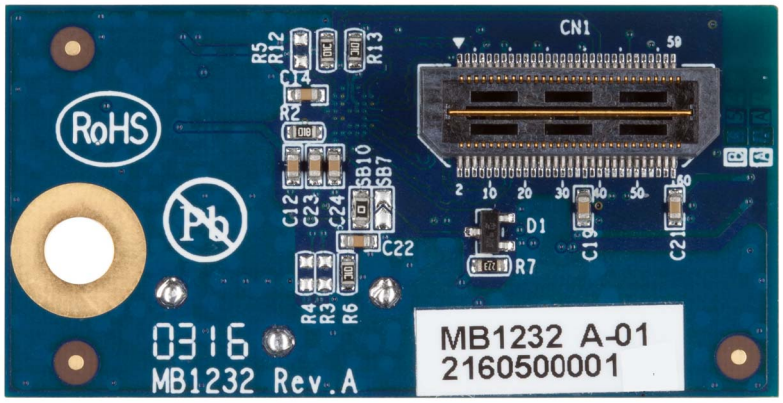
Features

- Up to four lanes of MIPI/DSI data, each running up to 800 Mbps (video mode only: Non-Burst mode with synchronization pulse)
- 2-channels I2S transmits stereo up to 192 kHz sampling rate
- S/PDIF carries stereo LPCM audio or compressed audio
- HDMI output connector:
  - Support of video resolutions with pixel clocks up to 80 MHz
  - Support of CEC signals, 5 V-tolerant I2C and HPD I/Os
  - Common-mode filters with ESD protection

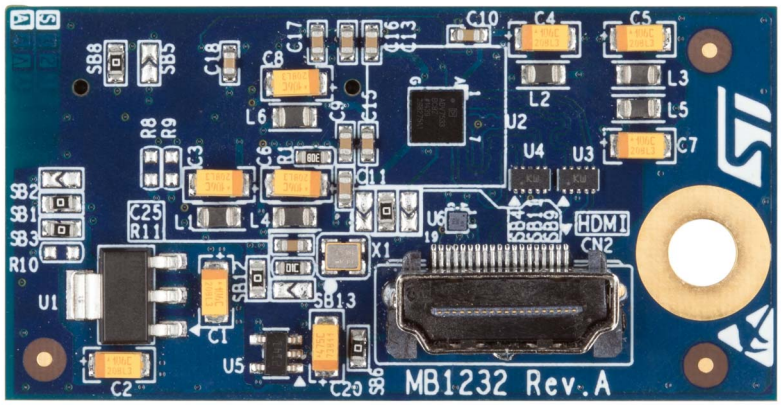
Description

The DSI to HDMI adapter board (order code B-LCDAD-HDMI1) provides DSI input port and HDMI output port. It can be used on STM32 evaluation boards or discovery boards, to demonstrate video solutions based on STM32 MCUs.

Thanks to the bridge chip ADV7533, the DSI to HDMI adapter board can support 2-, 3- or 4-lanes DSI video input data, S/PDIF, 2-channels I2S audio input data and HDMI v1.3 output port.



Back side



Front side

Pictures not contractual

1 Revision history

Table 1. Document revision history

Date	Revision	Changes
08-Jun-2016	1	Initial release.

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