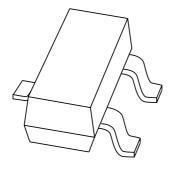
### **DISCRETE SEMICONDUCTORS**

# DATA SHEET



PBSS8110T 100 V, 1 A NPN low V<sub>CEsat</sub> (BISS) transistor

Product data sheet Supersedes data of 2003 Jul 28 2003 Dec 22



## 100 V, 1 A NPN low V<sub>CEsat</sub> (BISS) transistor

#### **PBSS8110T**

#### **FEATURES**

- SOT23 package
- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability: I<sub>C</sub> and I<sub>CM</sub>
- · Higher efficiency leading to less heat generation
- Reduced printed-circuit board requirements.

#### **APPLICATIONS**

- Major application segments
  - Automotive 42 V power
  - Telecom infrastructure
  - Industrial
- · Power management
  - DC/DC converters
  - Supply line switching
  - Battery charger
  - LCD backlighting.
- · Peripheral drivers
  - Driver in low supply voltage applications (e.g. lamps and LEDs).
  - Inductive load driver (e.g. relays, buzzers and motors).

#### **DESCRIPTION**

NPN low  $V_{CEsat}$  transistor in a SOT23 plastic package. PNP complement: PBSS9110T.

#### **MARKING**

TYPE NUMBER	MARKING CODE <sup>(1)</sup>
PBSS8110T	*U8

#### Note

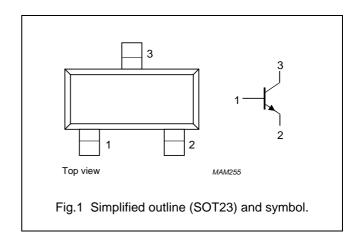
\* = p : Made in Hong Kong.
 \* = t : Made in Malaysia.
 \* = W : Made in China.

#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	UNIT
V <sub>CEO</sub>	collector-emitter voltage	100	V
I <sub>C</sub>	collector current (DC)	1	Α
I <sub>CM</sub>	repetitive peak collector current	3	Α
R <sub>CEsat</sub>	equivalent on-resistance	200	mΩ

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE NUMBER	PACKAGE			
ITPE NUMBER	NAME DESCRIPTION VERSION			
PBSS8110T	_	plastic surface mounted package; 3 leads		

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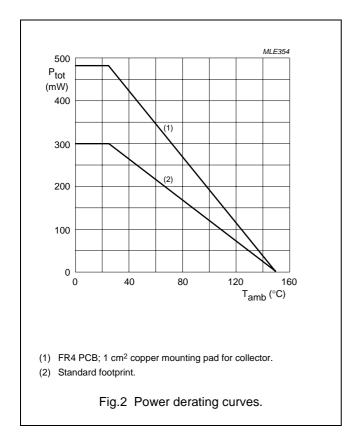
#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	120	V
$V_{CEO}$	collector-emitter voltage	open base	-	100	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	1	Α
I <sub>CM</sub>	peak collector current	limited by T <sub>j max</sub>	-	3	Α
I <sub>B</sub>	base current (DC)		_	300	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	-	300	mW
		T <sub>amb</sub> ≤ 25 °C; note 2	-	480	mW
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

#### **Notes**

- 1. Device mounted on a printed-circuit board, single sided copper, tinplated, standard footprint.
- 2. Device mounted on a printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>.



## 100 V, 1 A NPN low V<sub>CEsat</sub> (BISS) transistor

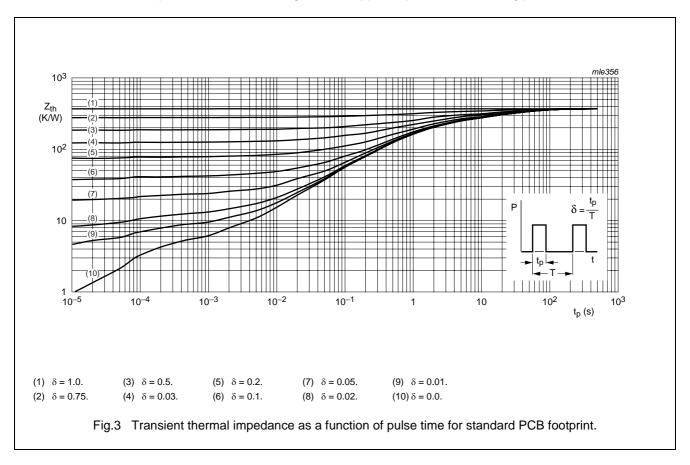
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#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to	in free air; note 1	417	K/W
	ambient	in free air; note 2	260	K/W

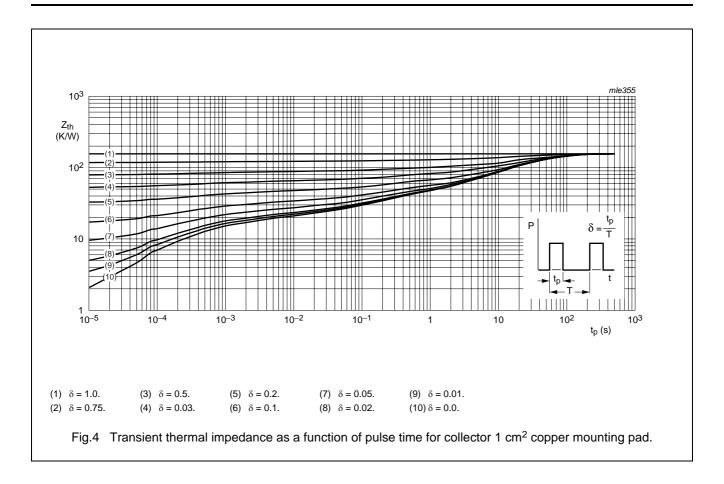
#### **Notes**

- 1. Device mounted on a printed-circuit board, single sided copper, tinplated and standard footprint.
- 2. Device mounted on a printed-circuit board, single sided copper, tinplated and mounting pad for collector 1 cm<sup>2</sup>.



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#### **CHARACTERISTICS**

 $T_j$  = 25 °C unless otherwise specified.

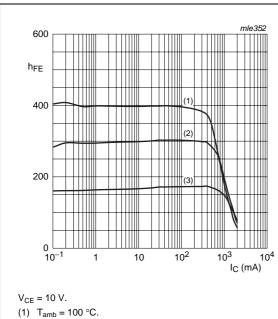
SYMBOL	PARAMETER	CONDITIONS		PARAMETER CONDITIONS MIN.		TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 80 V; I <sub>E</sub> = 0	_	_	100	nA		
		V <sub>CB</sub> = 80 V; I <sub>E</sub> = 0; T <sub>j</sub> = 150 °C	_	_	50	μΑ		
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = 80 V; V <sub>BE</sub> = 0	_	_	100	nA		
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0	_	-	100	nA		
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 1 mA	150	_	_			
		$V_{CE} = 10 \text{ V}; I_{C} = 250 \text{ mA}$	150	-	500			
		$V_{CE} = 10 \text{ V}; I_{C} = 500 \text{ mA}; \text{ note 1}$	100	-	-			
		V <sub>CE</sub> = 10 V; I <sub>C</sub> = 1 A; note 1	80	_	_			
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 10 mA	_	-	40	mV		
		I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	_	-	120	mV		
		I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; note 1	_	_	200	mV		
R <sub>CEsat</sub>	equivalent on-resistance	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; note 1	_	165	200	mΩ		
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA	_	-	1.05	V		
V <sub>BEon</sub>	base-emitter turn-on voltage	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 1 A	_	_	0.9	V		
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	100	_	-	MHz		
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = I_e = 0; f = 1 \text{ MHz}$	_	_	7.5	pF		

#### Note

1. Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.02.$ 

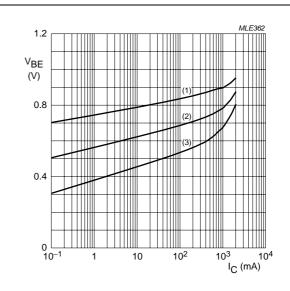
## 100 V, 1 A NPN low V<sub>CEsat</sub> (BISS) transistor

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- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = -55 \, ^{\circ}C$ .

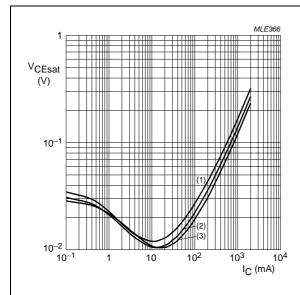
Fig.5 DC current gain as a function of collector current; typical values.



V<sub>CE</sub> = 10 V.

- (1)  $T_{amb} = -55 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = 100 \, ^{\circ}C$ .

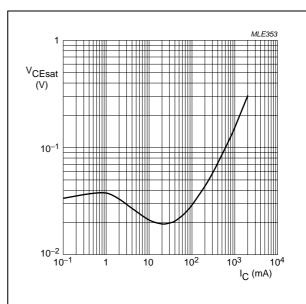
Fig.6 Base-emitter voltage as a function of collector current; typical values.



 $I_{\rm C}/I_{\rm B} = 10.$ 

- (1)  $T_{amb} = 100 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = -55 \, ^{\circ}C$ .

Fig.7 Collector-emitter saturation voltage as a function of collector current; typical values.



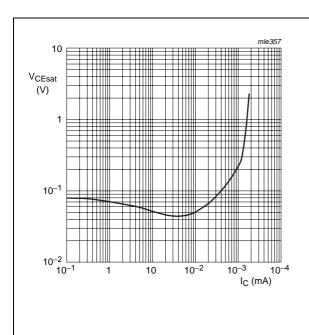
 $I_{\rm C}/I_{\rm B} = 20.$  $T_{amb} = 25 \, ^{\circ}C.$ 

Fig.8 Collector-emitter saturation voltage as a function of collector current; typical values.

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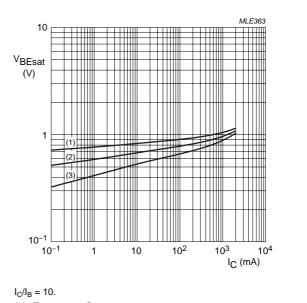
## 100 V, 1 A NPN low V<sub>CEsat</sub> (BISS) transistor

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 $I_C/I_B = 50$ .  $T_{amb} = 25 \, ^{\circ}C$ .

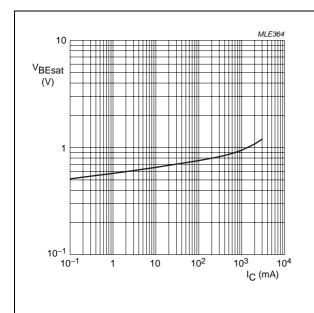
Fig.9 Collector-emitter saturation voltage as a function of collector current; typical values.



(1)  $T_{amb} = -55 \text{ °C}.$ (2)  $T_{amb} = 25 \text{ °C}.$ 

(3)  $T_{amb} = 100 \, ^{\circ}C$ .

Fig.10 Base-emitter saturation voltage as a function of collector current; typical values.



 $I_C/I_B = 20.$  $T_{amb} = 25 \, ^{\circ}C.$ 

Fig.11 Base-emitter saturation voltage as a function of collector current; typical values.

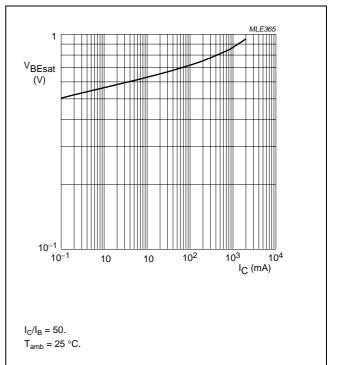


Fig.12 Base-emitter saturation voltage as a function of collector current; typical values.

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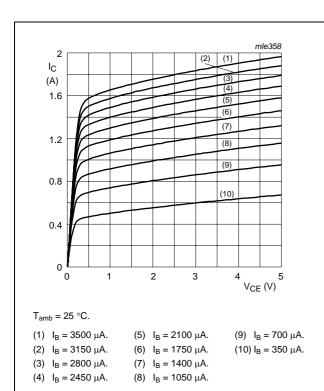


Fig.13 Collector current as a function of collector-emitter voltage; typical values.

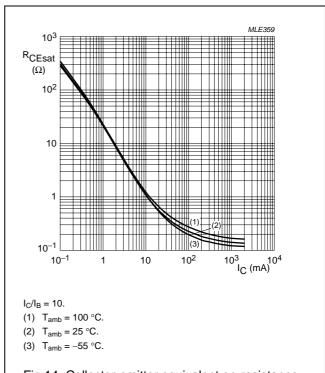


Fig.14 Collector-emitter equivalent on-resistance as a function of collector current; typical values

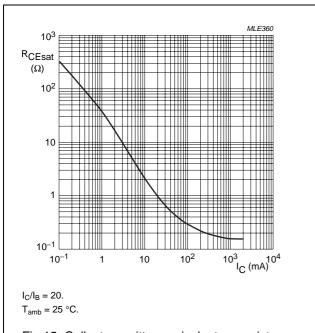
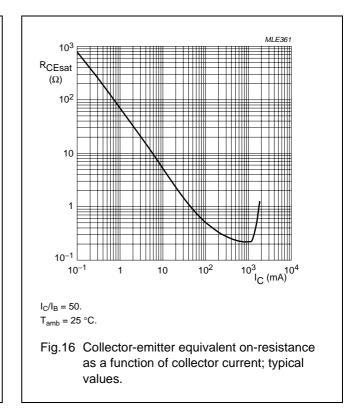


Fig.15 Collector-emitter equivalent on-resistance as a function of collector current; typical values.



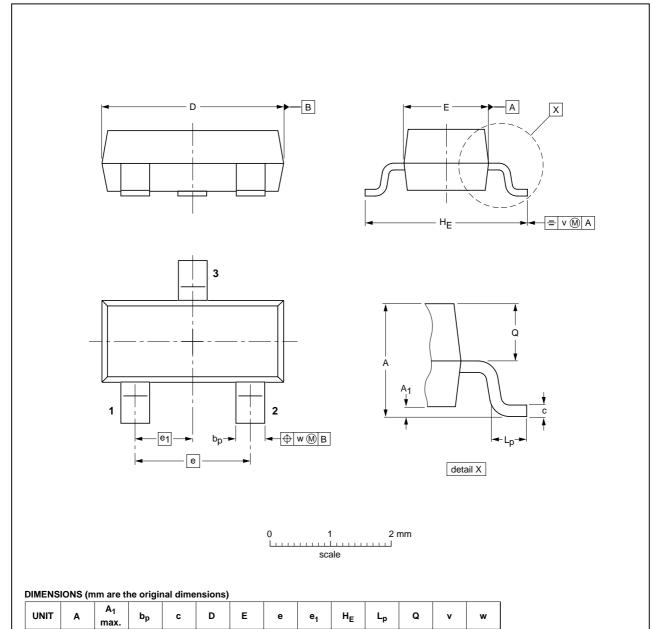
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#### **PACKAGE OUTLINE**



SOT23



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				<del>-04-11-04-</del> 06-03-16

1.9

0.45

0.55

0.1

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0.48

0.38

0.9

## 100 V, 1 A NPN low V<sub>CEsat</sub> (BISS) transistor

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#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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