

DATASHEET

4 PIN SOP HIGH VOLTAGE PHOTODARLINGTON PHOTOCOUPLER EL452-G Series



Features:

- •Halogens free(Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm)
- High collect-Emitter voltage (V_{CEO} = 350V)
- Current transfer ratio

(CTR: Min. 1000% at $I_F = 1mA$, $V_{CE} = 2V$)

- High isolation voltage between input and output (Viso=3750 V rms)
- Compact 4 Pin SOP with a 2.0 mm profile
- Pb free and RoHS compliant.
- Compliance with EU REACH.
- UL & CUL approved
- VDE approved
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

Description

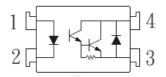
The EL452-G contains an infrared emitting diode, optically coupled to a high voltage darlington phototransistor.

It is packaged in a 4-pin small outline SMD package.

Applications

- Telephone set, telephone exchangers
- Sequence controllers
- System appliances, measuring instruments
- Signal transmission between circuits of different potentials and impedance

Schematic



Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Emitter
- 4. Collector



Absolute Maximum Ratings (Ta=25°C)

	Parameter	Symbol	Rating	Unit
Input	Forward current	I _F	60	mA
	Peak forward current (t = 10µs)	I _{FM}	1	А
	Power dissipation	P _D	100	mW
Output	Power dissipation	Pc	150	mW
	Collector current	Ic	150	mA
	Collector-Emitter voltage	V _{CEO}	350	V
	Emitter-Collector voltage	V_{ECO}	0.1	V
Total power dissipation		P _{TOT}	170	mW
Isolation vo	oltage *1	V _{ISO}	3750	V rms
Operating temperature		T_{OPR}	-55~+110	°C
Storage temperature		T _{STG}	-55~+125	°C
Soldering 7	Femperature*2	T _{SOL}	260	°C

Notes:

^{*1} AC for 1 minute, R.H.= $40 \sim 60\%$ R.H. In this test, pins 1 & 2 are shorted together, and pins 3 & 4 are shorted together.

^{*2} For 10 seconds



Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input

Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Forward Voltage	V_{F}	-	1.2	1.4	V	$I_F = 10mA$
Reverse Current	I _R	-	-	10	μA	$V_R = 4V$
Input capacitance	C _{in}	-	50	-	pF	V = 0, f = 1KHz

Note: Reverse Voltage(VR) Condition is applied to IR test only The device is not designed for reverse operation Output

Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Collector-Emitter dark current	I _{CEO}	-	-	100	nA	V _{CE} = 200V, I _F =0mA
Collector-Emitter breakdown voltage	BV _{CEO}	350	-	-	V	I _c =0.1mA
Emitter-Collector breakdown voltage	BV _{ECO}	0.1	-	-	V	I _E =0.01mA

Transfer Characteristics

Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Current Transfer ratio	CTR	1000	2000	-	%	I _F = 1mA ,V _{CE} = 2V
Collector-emitter saturation voltage	V _{CE(sat)}	-	1.0	1.2	V	I _F = 20mA , I _c = 100mA
Isolation resistance	R _{IO}	5×10 ¹⁰	10 ¹¹	-	Ω	V _{IO} = 500Vdc, 40~60%R.H
Cut-off frequency	fc	-	7	-	KHz	$V_{CE}=2V$, $I_{C}=2mA$, $R_{L}=100\Omega$, $-3db$
Floating capacitance	C_{IO}	-	0.6	-	pF	$V_{IO} = 0$, $f = 1MHz$
Rise time	t _r	-	80	250	μs	V _{CE} =2V,
Fall time	t_f	-	10	100	μs	- I _C =20mA, R _L =100Ω

^{*} Typical values at T_a = 25°C



Typical Electro-Optical Characteristics Curves

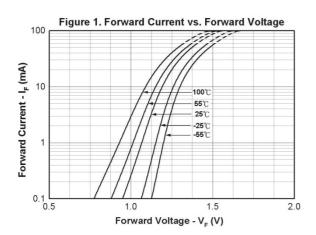


Figure 3. Collector Emitter Saturation Voltage vs.

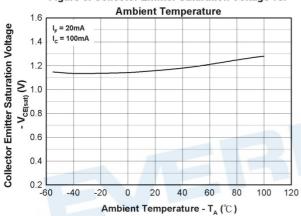


Figure 5. Current Transfer Ratio vs. Forward Current v_{ce}^{-2V}

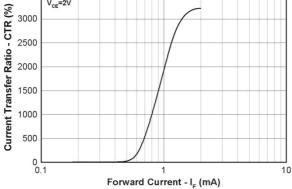


Figure 2. Collector Current vs.

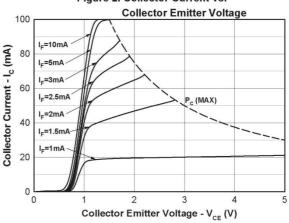


Figure 4. Collector-Emitted Saturation Voltage vs.

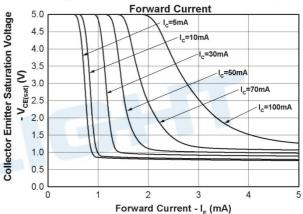
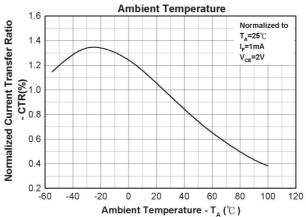
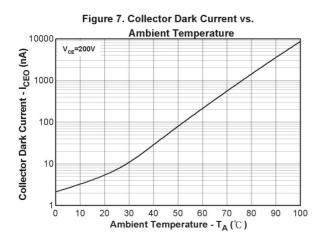
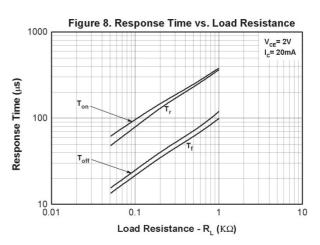


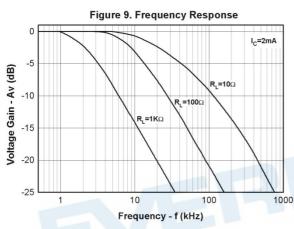
Figure 6. Normalized Current Transfer Ratio vs.











Note: The graphs shown in this datasheet are representing typical data only and do not show guaranteed values



Order Information Part Number

EL452(Y)-VG

Note

Y = Tape and reel option (TA, TB, or none).

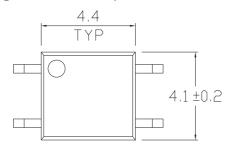
V = VDE safety (optional)

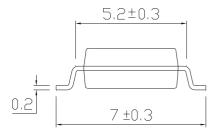
G = Halogens free

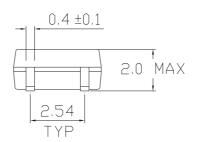
Option	Description	Packing quantity
None	Standard SMD option	100 units per tube
-V	Standard SMD option + VDE	100 units per tube
(TA)	TA Tape & reel option	3500 units per reel
(TB)	TB Tape & reel option	3500 units per reel
(TA)-V	TA Tape & reel option + VDE	3500 units per reel
(TB)-V	TB Tape & reel option + VDE	3500 units per reel
	VERLIGH	



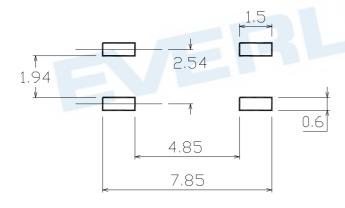
Package Dimension (Dimensions in mm)







Recommended pad layout for surface mount leadform





Device Marking



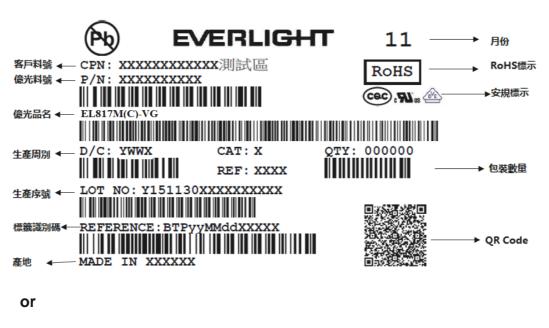
Notes

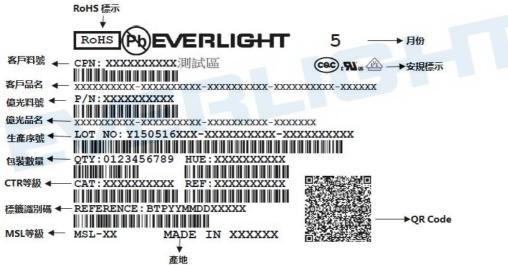
EL denotes Everlight
452 denotes Part Number
Y denotes 1 digit Year code
WW denotes 2 digit Week code
V denotes VDE approved (optional)



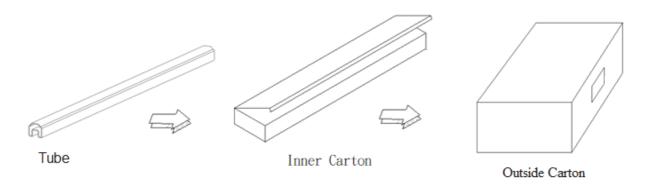


Label form



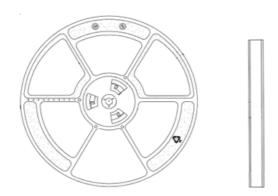


TUBE Dimension

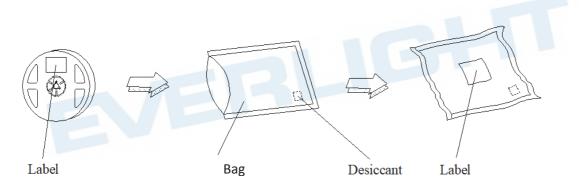


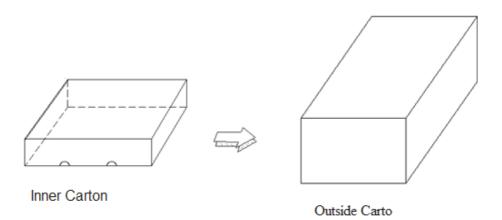


Reel Dimension



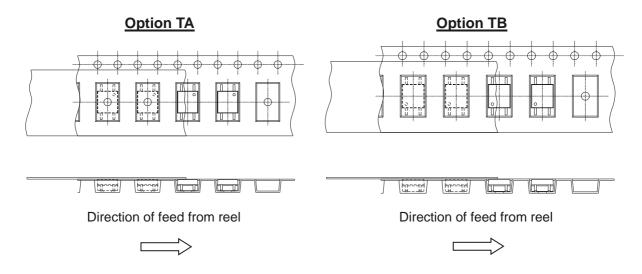
Moisture Resistant Packaging



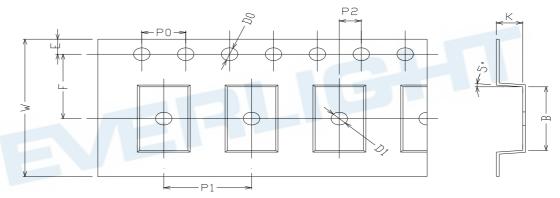




Tape & Reel Packing Specifications



Tape dimensions





Dimension No.	Α	В	Do	D1	E	F
Dimension(mm)	4.4 ± 0.1	7.4 ± 0.1	1.5 + 0.1/-0	1.5 ± 0.1	1.7 5± 0.1	7.5 ± 0.1
Dimension No.	Ро	P1	P2	t	W	К
Dimension(mm)	4.0 ± 0.15	8.0 ± 0.1	2.0 ± 0.1	0.25 ± 0.03	16.0 ± 0.2	2.4 ± 0.1

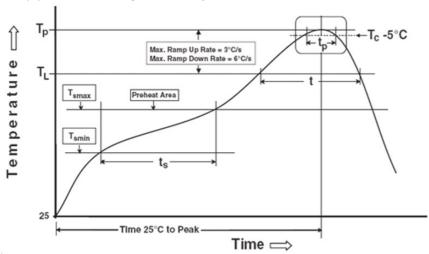


Reference: IPC/JEDEC J-STD-020D

Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

Preheat

150 °C Temperature min (T_{smin}) 200°C Temperature max (T_{smax})

Time $(T_{smin} \text{ to } T_{smax})$ (t_s) 60-120 seconds Average ramp-up rate (T_{smax} to T_p) 3 °C/second max

Other

217 °C Liquidus Temperature (T_L)

Time above Liquidus Temperature (t L) 60-100 sec

Peak Temperature (T_P) 260°C Time within 5 °C of Actual Peak Temperature: TP - 5°C 30 s

Ramp- Down Rate from Peak Temperature 6°C /second max.

Time 25°C to peak temperature 8 minutes max.

Reflow times 3 times



Precautions for General Storage

- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5°C to 35°C and 20 % to 60 %,respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- When restoring devices after removal from their packing, use anti-static containers.
- Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.





DISCLAIMER

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