

SPECIFICATIONS

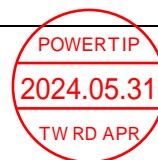
CUSTOMER	:	-
SAMPLE CODE	:	SH102600T021-ZFC
MASS PRODUCTION CODE	:	PH102600T021-ZFC
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	001
DRAWING NO. (Ver.)	:	LMD-PH102600T021-ZFC (Ver.001)
PACKAGING NO. (Ver.)	:	-

Customer Approved

Date:

Approved	Checked	Designer
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- ☒ Preliminary specification for design input
☐ Specification for sample approval



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NO.PT-A-005-8

History of Version

<u>Date</u> (mm / dd / yyyy)	<u>Ver.</u>	<u>Edi.</u>	<u>Description</u>	<u>Page</u>	<u>Design</u> <u>by</u>
05/31/2024	01	001	Preliminary.	-	Grace

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1. SPECIFICATIONS

1.1 Features

<u>Item</u>	<u>Standard Value</u>
Display Resolution	1024 *3 (RGB) * 600 Dots
LCD Type	Full Viewing Angle , Normally Black , Transmissive type
Screen size(inch)	7 inch
Color configuration	RGB Vertical Stripe
Backlight Type	White LED B/L
Interface	LVDS Interface
Driver IC	JD9165 (Or Compatible IC)
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website: http://www.powertip.com.tw/news_detail.php?Key=1&clD=1

1.2 Mechanical Specifications

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Outline Dimension	192.96 (W) * 115.76 (L) * 4.84 (H)	mm

LCD panel

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Active Area	154.21(W) * 85.92 (L)	mm

Note: For detailed information please refer to LCM drawing.

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Supply power voltage	VDD	-	-0.3	4.0	V
	AVDD	-	-0.3	12.0	V
	VGH	-	-0.3	VGL+32V	V
	VGL	-	VGH-32V	-0.3	V
Operating Temperature	T _{OP} (Ts)	Note 1	-20	70	°C
Storage Temperature	T _{ST} (Ta)	Note 2	-30	80	°C
Storage Humidity	H _D	Ta ≤ 60 °C	-	90	%RH

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

Module

GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply power voltage	VDD	-	3.1	3.3	3.6	V
	AVDD	-	-	-	-	V
	VGH	-	-	(20)	-	V
	VGL	-	-	(-7)	-	V
Input signal Voltage	VCOM	-	-	(3.7)	-	V
Input Signal Voltage	V _{IH}	GND=0	0.8VDD	-	VDD	V
	V _{IL}	GND=0	0	-	0.2VDD	
Supply Current	I _{DD}	VDD = 3.3 V Pattern= Red	-	(15)	-	mA
	I _{ADD}	AVDD= - V Pattern= Red	-	(30)	-	mA
	I _{GH}	VGH= 20.0V Pattern= Red	-	(0.5)	-	mA
	I _{GL}	VGL=-7.0V Pattern= Red	-	(0.5)	-	mA

1.5 Optical Characteristics

3V3=3.3V, Ta=25°C

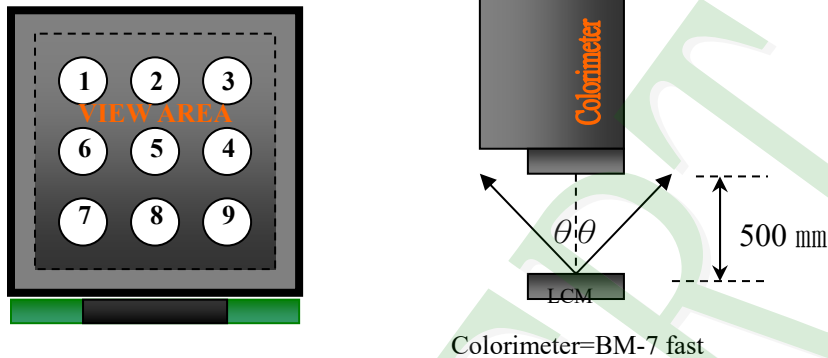
Item	Symbol		Condition	Min.	Typ.	Max.	unit	
Response time	Tr+Tf		Ta = 25°C θX, θY = 0°	-	(30)	(40)	ms	Note 2
Viewing angle	Top	θY+	CR ≥ 10	-	80	-	Deg.	Note 4
	Bottom	θY-		-	80	-		
	Left	θX-		-	80	-		
	Right	θX+		-	80	-		
Contrast ratio		CR	Ta = 25°C θX, θY = 0°	(650)	(800)	-	-	Note 3
Color of CIE Coordinate	White	X		-	(0.30)	-		Note1
		Y		-	(0.34)	-		
	Red	X		-	(0.62)	-		
		Y		-	(0.33)	-		
	Green	X		-	(0.29)	-		
		Y		-	(0.55)	-		
	Blue	X		-	(0.14)	-		
		Y		-	(0.17)	-		
Average Brightness Pattern=white display (With LCD & CTP)*2	IV		IF=200mA	(400)	(500)	-	cd/m ²	Note1
Uniformity (With LCD & CTP)*1	ΔB		IF=200mA	80	-	-	%	Note1

Note 1:

*1: $\Delta B = B(\min) / B(\max) * 100\%$

*2: Measurement Condition for Optical Characteristics:

- a: Environment: $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ / $60 \pm 20\% \text{R.H.}$, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency
- b: Measurement Distance: $500 \pm 50 \text{ mm}$, ($\theta = 0^{\circ}$)
- c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation
- d: The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note 2: Definition of response time:

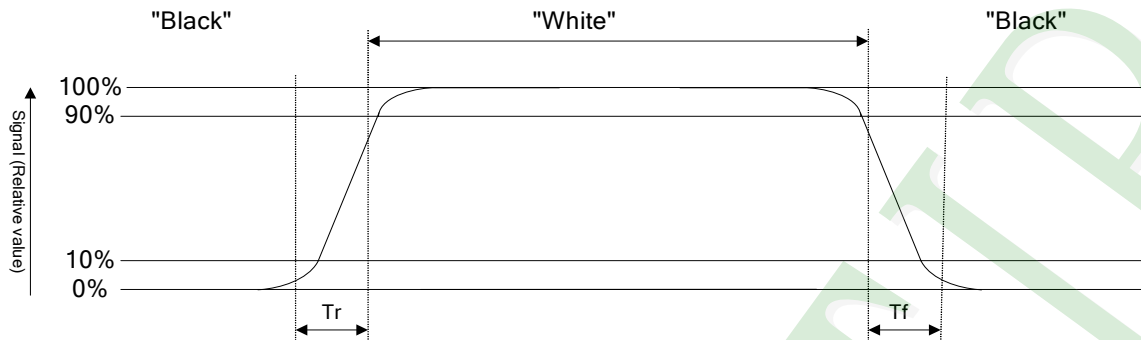
The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White



Normally Black



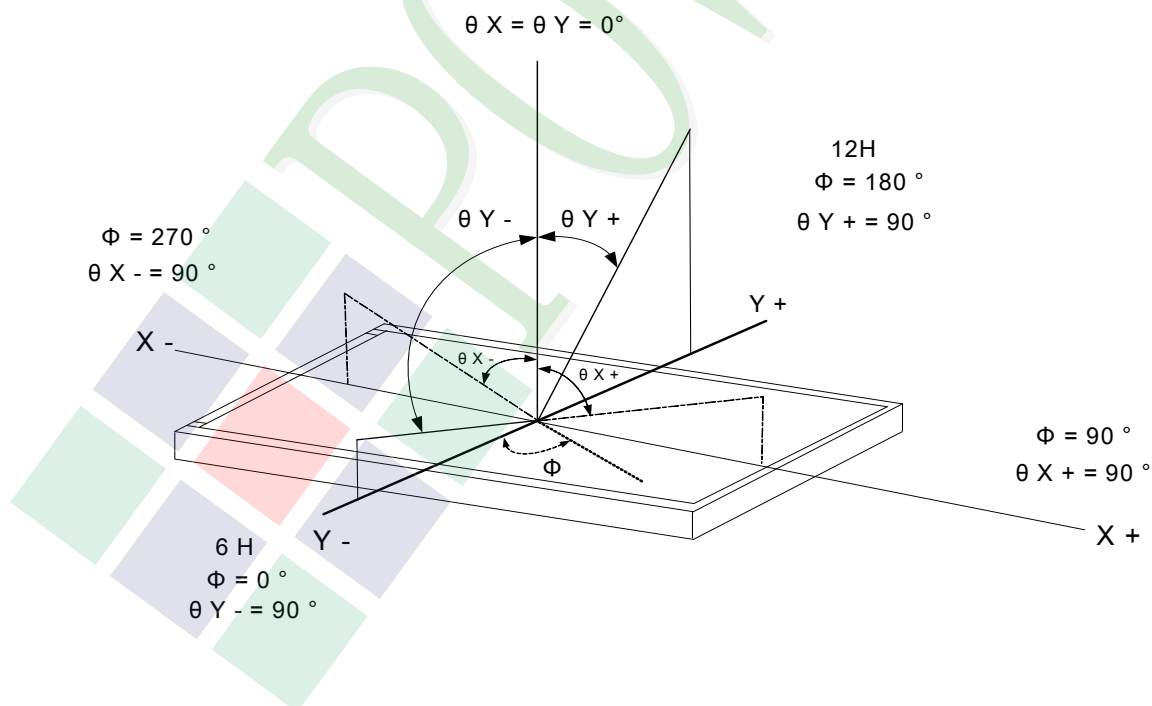
Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note 4: Definition of viewing angle:

Refer to figure as below:



1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
LED Forward Current	I_F	-	300	mA	-
LED Reverse Voltage	V_R	-	5	V	
Power Dissipation	PD	-	3.06	W	

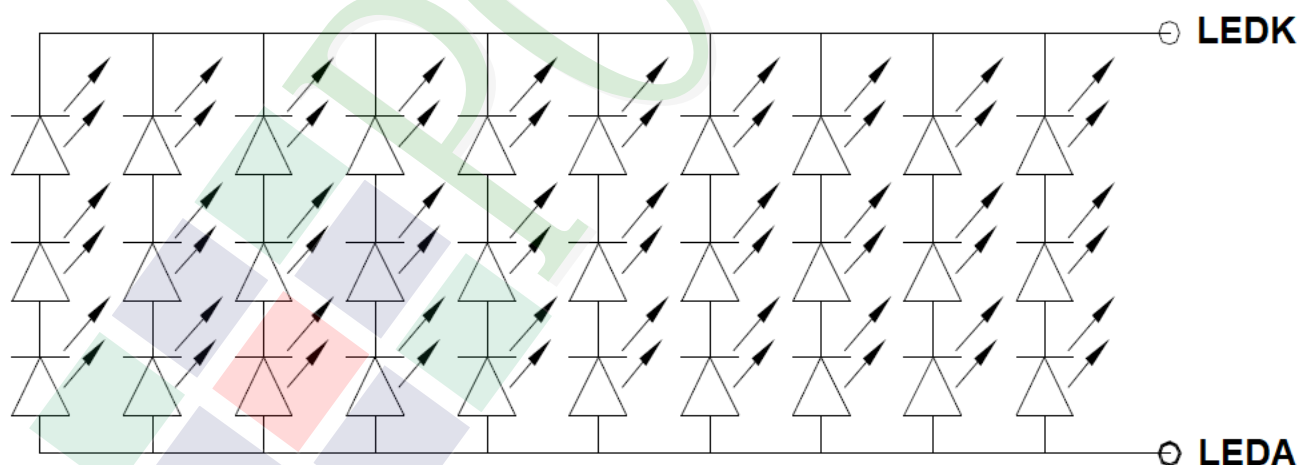
Electrical / Optical Characteristics

Electrical / Optical Characteristics						
<u>Item</u>	<u>Symbol</u>	<u>Conditions</u>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>Unit</u>
Forward Voltage	V _F	I _F =200 mA	8.4	9.0	10.2	V
Average Brightness (without LCD)	I _V		10000	12000	14000	cd/m ²
Color of CIE Coordinate*1 (Without LCD)	X		0.24	0.27	0.30	-
	Y		0.22	0.25	0.28	
Uniformity	△B		80	-	-	*2
Color	White					

*1 : This value will be changed while mass production.

*2 : $\Delta B = B(\text{min}) / B(\text{max}) \times 100\%$

B/L Internal Circuit Diagram



Other Description

Item	Conditions	Description
Life Time	$T_a = 25^\circ\text{C}$ $I_F = 200 \text{ mA}$	50000 hrs

1.7 Touch Panel Characteristics

Features

<u>Item</u>	<u>Standard Value</u>
Touch Panel Size	7"
Touch type	Projective Capacitive Touch Panel
Input Method	Finger / 5 Points touch
Output Interface	I ² C
IC	FT5426

I2C Address

<u>Bit 7</u>	<u>Bit 6</u>	<u>Bit 5</u>	<u>Bit 4</u>	<u>Bit 3</u>	<u>Bit 2</u>	<u>Bit 1</u>	<u>Bit 0</u>
-	-	-	-	-	-	-	R/W

Bit 0: 0 for Write / 1 for Read

Mechanical Specifications

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Viewing Area	154.88 (W) x 86.72 (H)	mm

Absolute Maximum Ratings

<u>Item</u>	<u>Symbol</u>	<u>Condition</u>	<u>Min.</u>	<u>Max.</u>	<u>Unit</u>
Supply voltage	TPVDD	-	-0.3	+6.0	V
Operating Temperature	T _{OP}	-	-20	+70	°C
Storage Temperature	T _{ST}	-	-30	+80	°C

DC Electrical Characteristics

<u>Item</u>	<u>Symbol</u>	<u>Condition</u>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>Unit</u>
Power Supply Voltage	TPVDD	-	2.8	3.3	3.6	V

Optical Characteristics

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Total light transmittance	(85%) or more	-
Hardness	≥(7H)	-

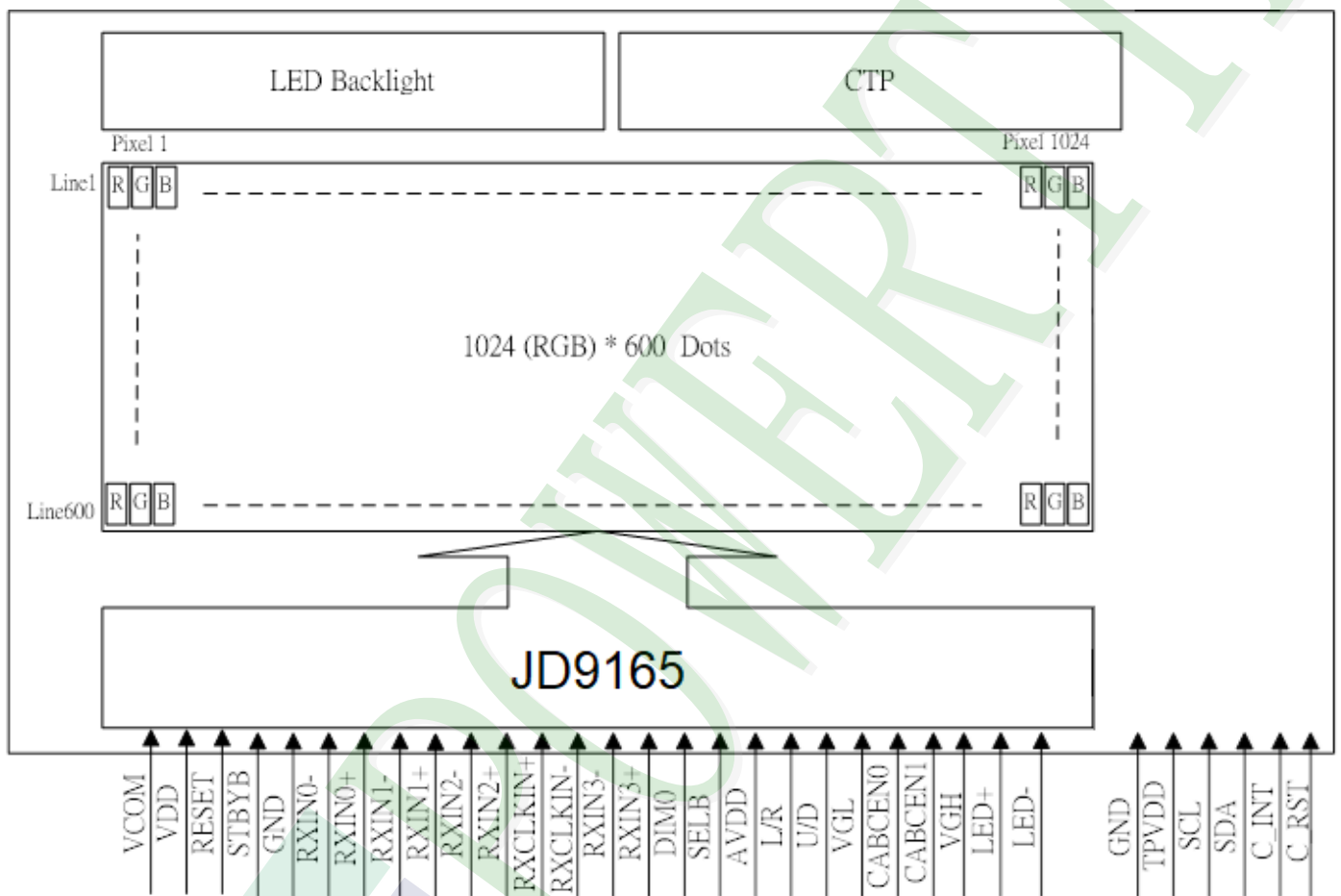
2. Module Structure

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



2.2 Interface Pin Description

TFT LCM Interface

Pin No.	Symbol	Function
1	VCOM	Common Voltage
2	VDD	Power Voltage for digital circuit
3	VDD	Power Voltage for digital circuit
4	NC	No connection
5	Reset	Global reset pin
6	STBYB	Standby mode, Normally pulled high STBYB = "1" , normal operation STBYB = "0" , timing controller, source driver will turn off, all output are High-Z
7	GND	Ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	+ LVDS differential data input
10	GND	Ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	+ LVDS differential data input
13	GND	Ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	+ LVDS differential data input
16	GND	Ground
17	RXCLKIN-	- LVDS differential clock input
18	RXCLKIN+	+ LVDS differential clock input
19	GND	Ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	+ LVDS differential data input
22	GND	Ground
23	NC	No Connection
24	NC	No Connection
25	GND	Ground
26	NC	No Connection

Pin No.	Symbol	Function
27	DIM0	Backlight CABC controller signal output DIM0=L Turn off external backlight controller DIM0=H Logical control signal to turn on external backlight controller
28	SELB	6bit/8bit mode select If LVDS input data is 6 bits ,SELB must be set to High; If LVDS input data is 8 bits ,SELB must be set to Low.
29	AVDD	Power for Analog Circuit
30	GND	Ground
31	LED-	LED Cathode
32	LED-	LED Cathode
33	L/R	Green Data.
34	U/D	Horizontal inversion When L/R="0", set right to left scan direction. When L/R="1", set left to right scan direction.
35	VGL	Vertical inversion When U/D="0", set top to bottom scan direction. When U/D="1", set bottom to top scan direction.
36	CABCEN1	CABC H/W enable Note:1
37	CABCEN0	CABC H/W enable Note:1
38	VGH	Gate ON Voltage
39	LED+	LED Anode
40	LED+	LED Anode

Note1:

<u>CABCEN1</u>	<u>CABCEN0</u>	<u>DESCRIPTION</u>
L	L	CABC OFF
L	H	User interface Image
H	L	Still Picture
H	H	Moving Image

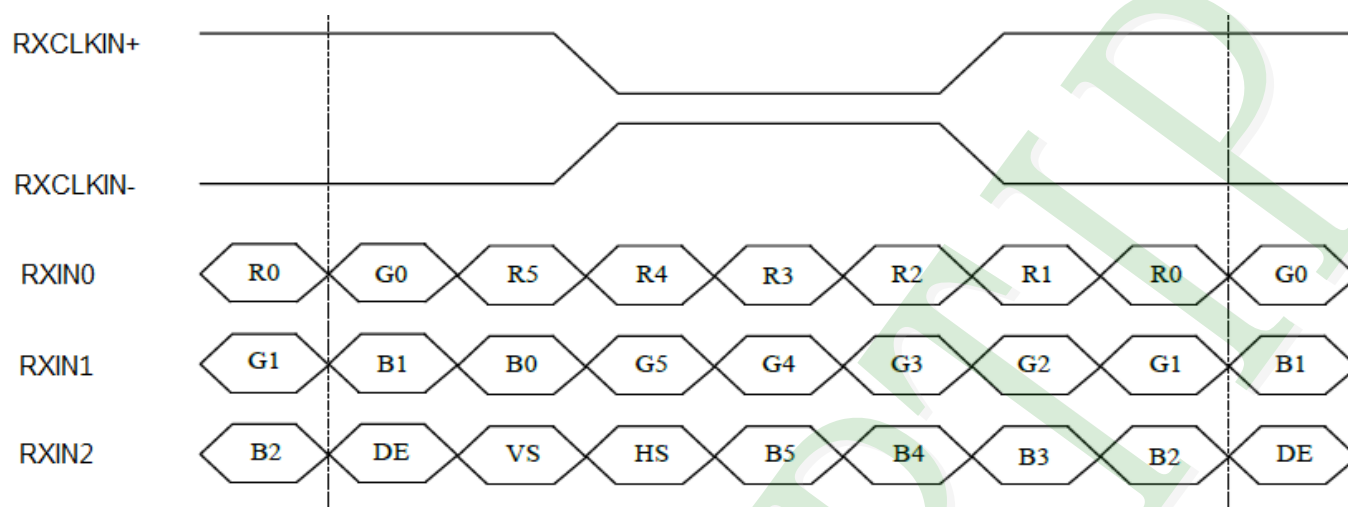
Capacitive Touch Panel (CTP) Interface

<u>Pin</u>	<u>Symbol</u>	<u>Function</u>
1	GND	Ground. (T/P)
2	TPVDD	Power.(T/P)
3	SCL	I ² C Clock. (T/P)
4	SDA	I ² C Data.(T/P)
5	C_INT	Active Low. (T/P)
6	C_RST	RESET. (T/P)

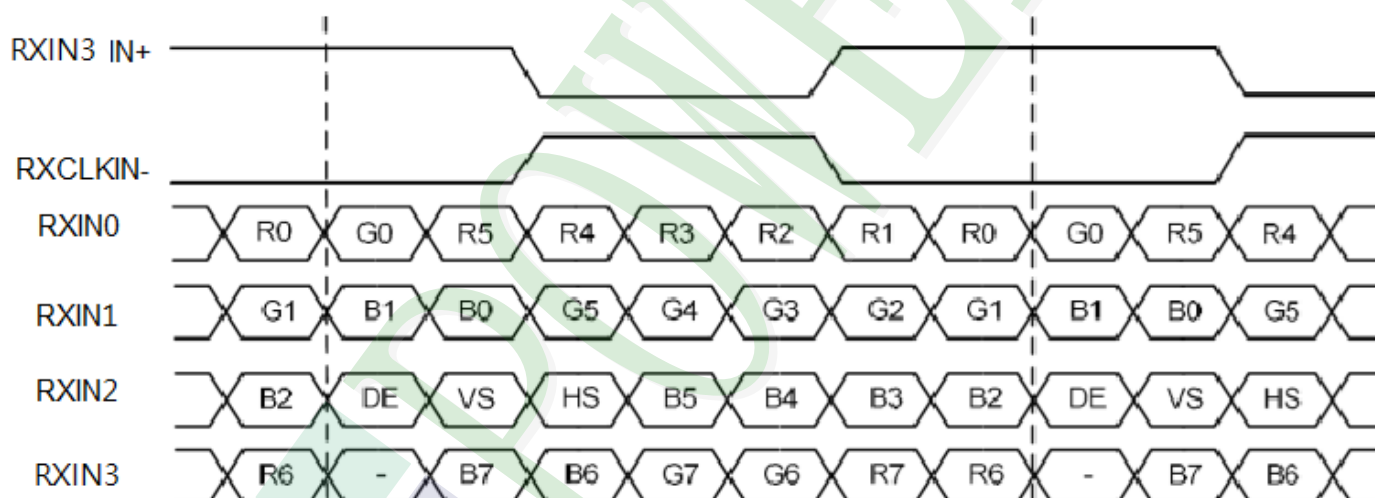
2.3 Timing Characteristics

Data Input Format For LVDS

6-bit RGB LVDS input timing



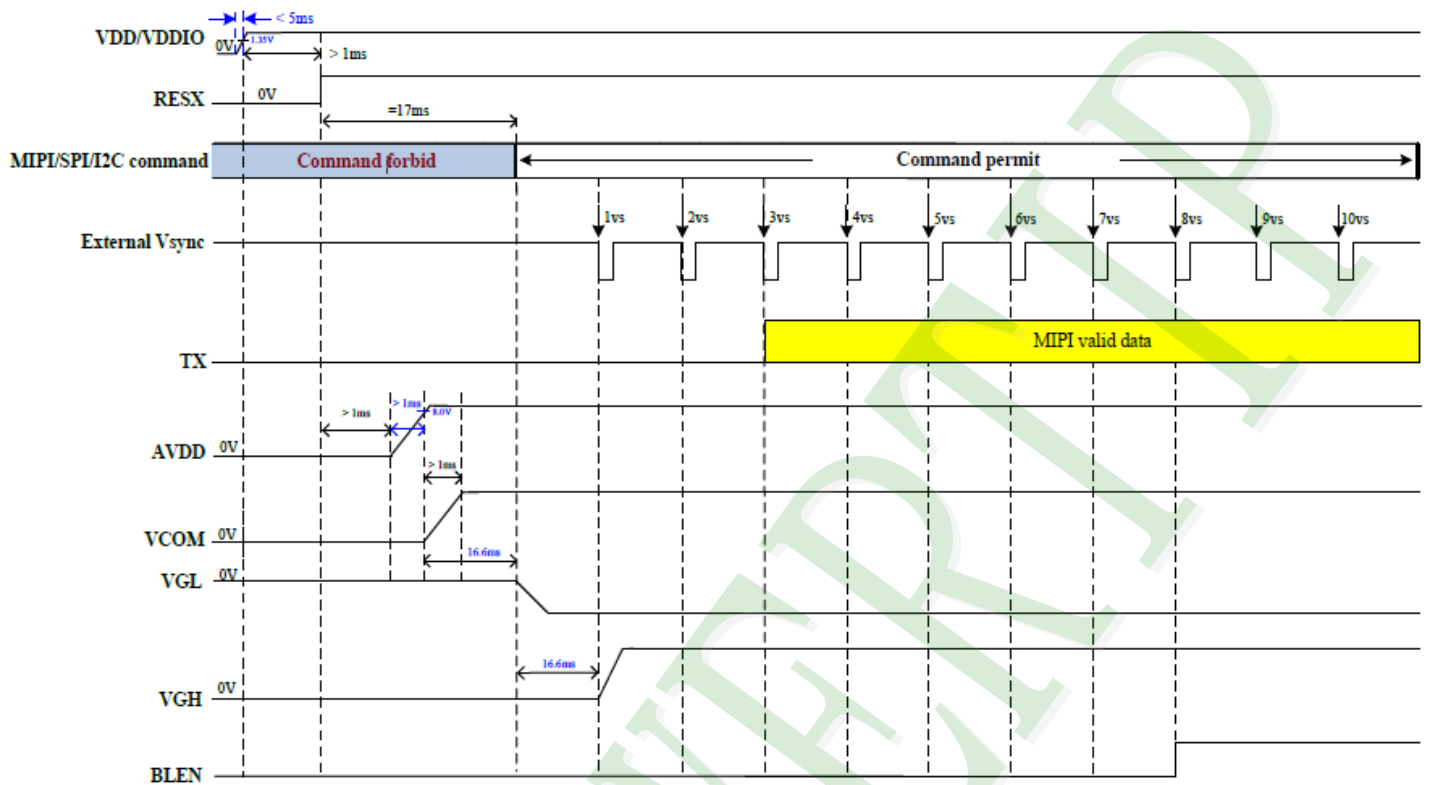
8-bit RGB LVDS VESA input timing



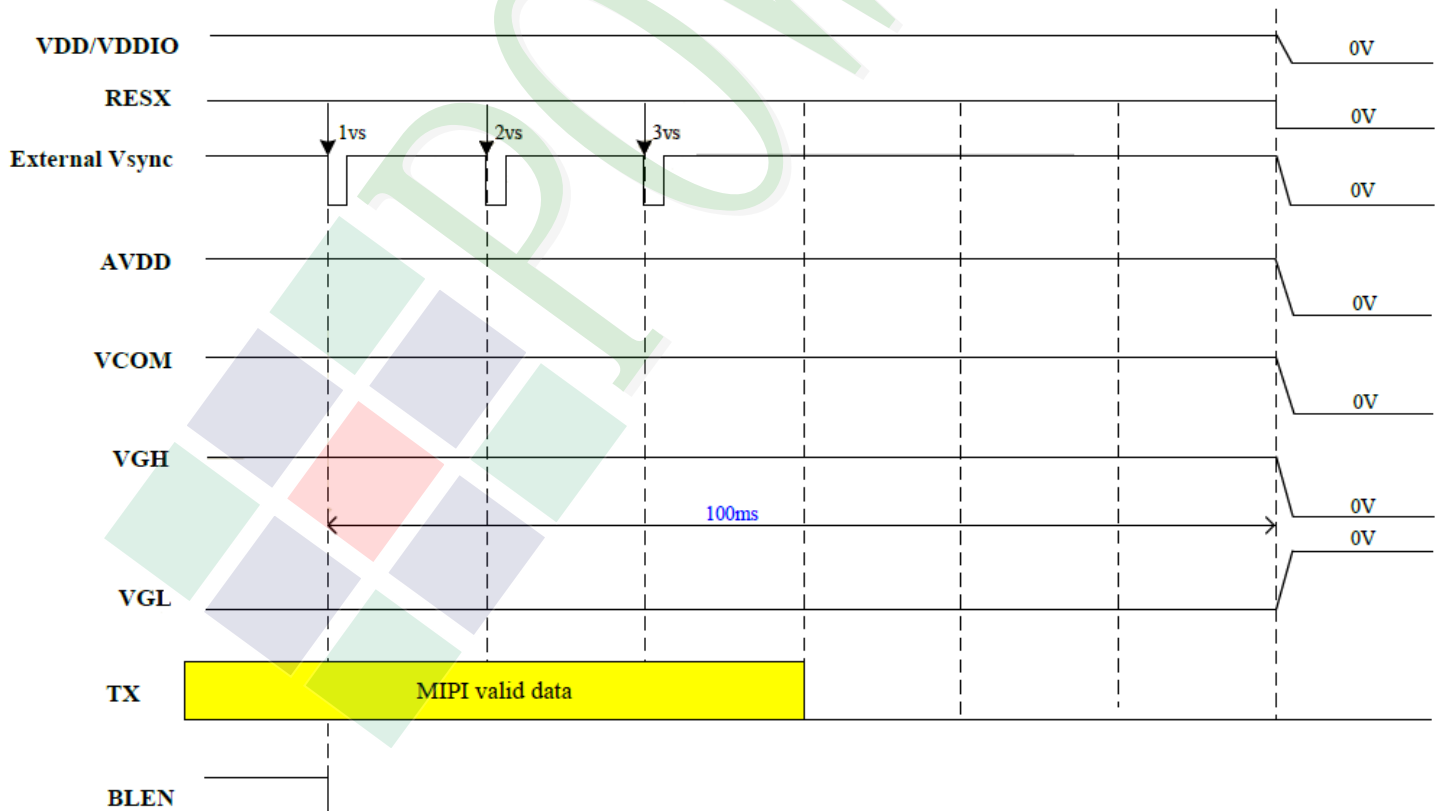
<u>Signal</u>	<u>Symbol</u>	<u>Min</u>	<u>Tpy</u>	<u>Max</u>	<u>Unit</u>
RXCLK Frequency	-	41.4	51.2	67.2	MHZ
Horizontal Total	t _{ht}	1114	1344	1400	DCLK
Hsync Pulse width	t _{hs}	1	24	HBP-1	DCLK
Horizontal Back Porch	t _{hb}	60	160	160	DCLK
Horizontal Valid Data	t _{hd}	1024			DCLK
Horizontal Front Porch	t _{hfp}	30	160	216	DCLK
Vertical Total	t _{vt}	620	635	800	THT
Vsync Pulse Width	t _{vs}	1	2	VBP-1	THT
Vertical Back Porch	t _{vb}	8	23	100	THT
Vertical Valid Data	t _{vd}	600			THT
Vertical Front Porch	t _{vfp}	12	12	100	THT

2.4 Power Sequence

Power ON Sequence

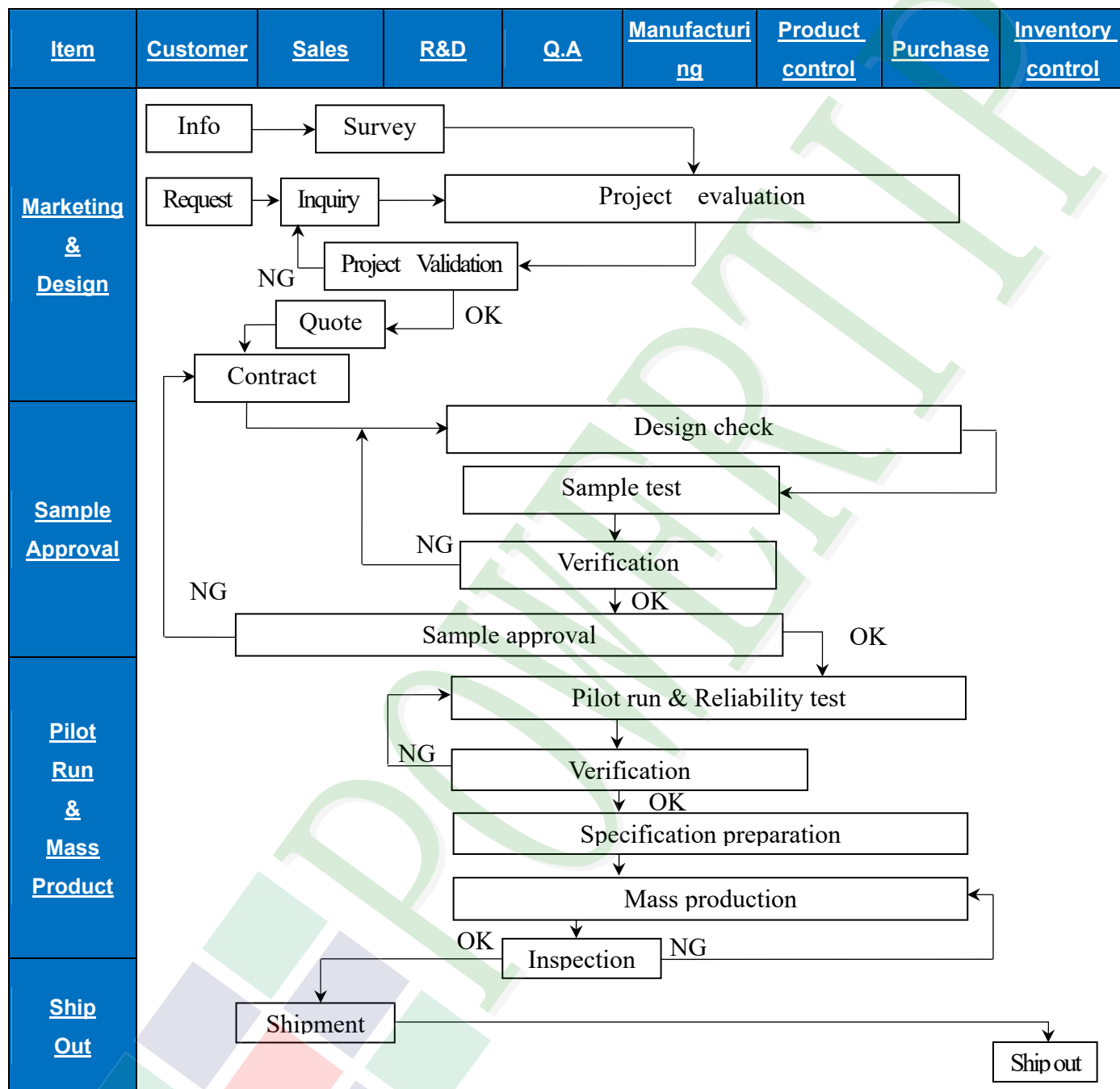


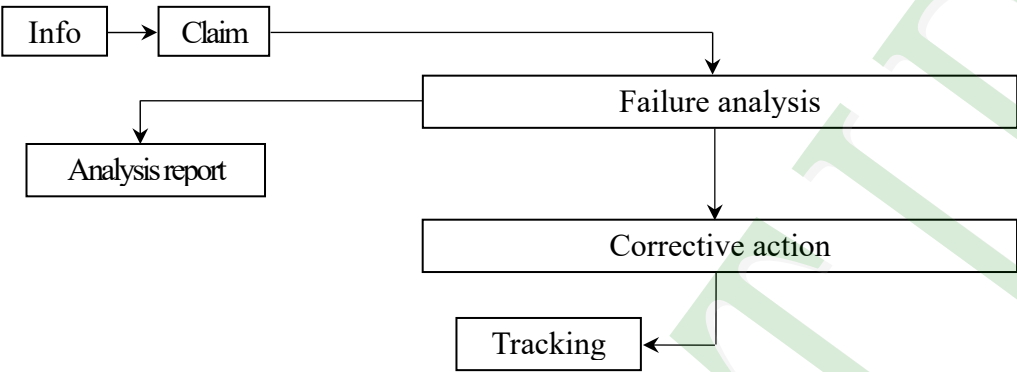
Power OFF Sequence



3. Quality Assurance System

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD Info[Info] --> Claim[Claim] Claim --> Failure[Failure analysis] Claim --> Report[Analysis report] Failure --> Action[Corrective action] Action --> Tracking[Tracking] </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

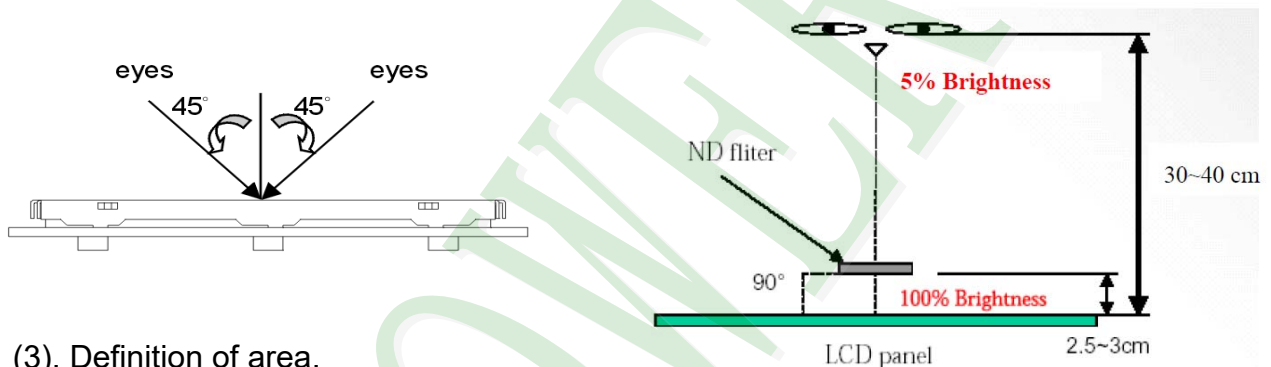
3.2 Inspection Specification

- ◆Scope: The document shall be applied to TFT-LCD Module for 3.5"-15" (Ver.B01).
- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.
- ◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample
- ◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5
- ◆OUT Going Defect Level: Sampling
- ◆Standard of the product appearance test:

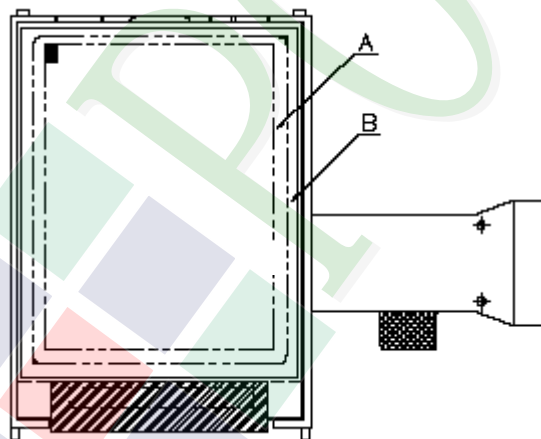
a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)
and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection : (Unit : mm)

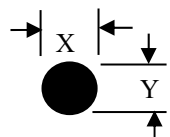
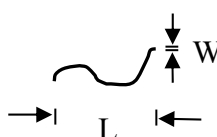
◆Specification For TFT-LCD Module 3.5"~15":

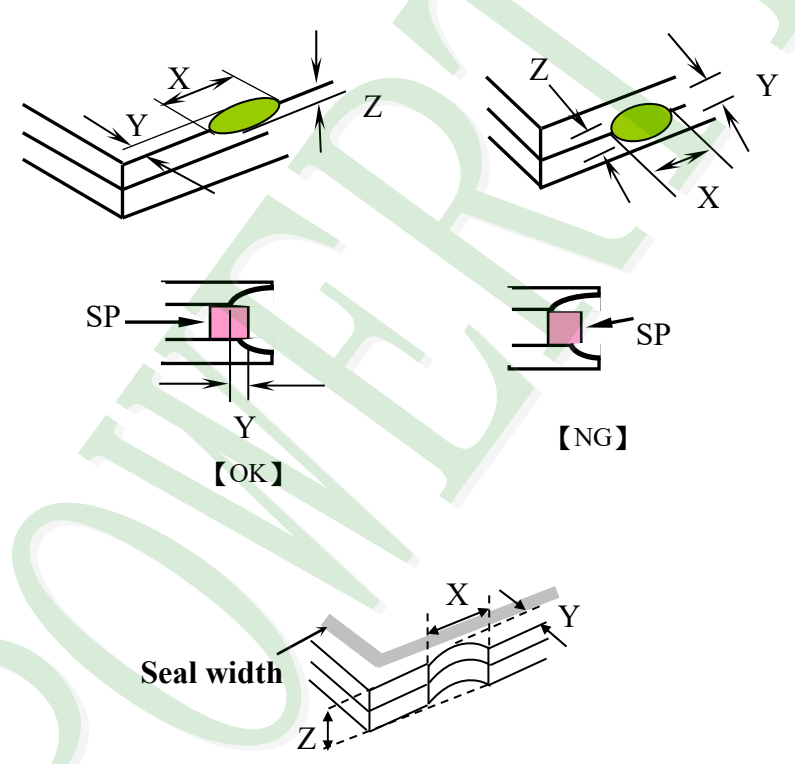
(Ver.B01)

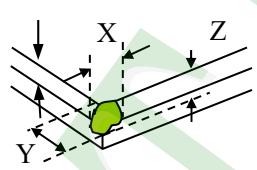
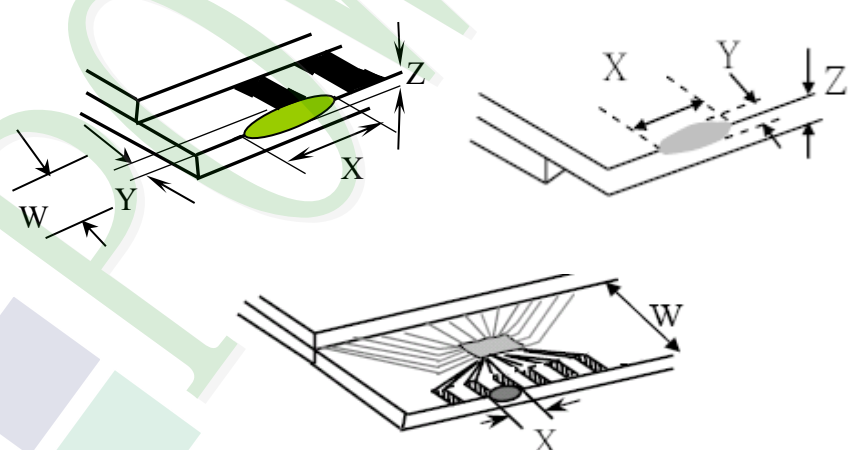
NO	Item	Criterion	Level										
01	Product condition	1.1 The part number is inconsistent with work order of production.	Major										
		1.2 Mixed product types.	Major										
		1.3 Assembled in inverse direction.	Major										
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major										
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major										
04	Electrical Testing	4.1 Missing line character and icon.	Major										
		4.2 No function or no display.	Major										
		4.3 Display malfunction.	Major										
		4.4 LCD viewing angle defect.	Major										
		4.5 Current consumption exceeds product specifications.	Major										
		4.6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.	Minor										
05	Dot defect (Bright dot, Dark dot) On -display	<table><tr><th>Item</th><th>Acceptance (Q'ty)</th></tr><tr><td>Bright Dot</td><td>≤ 4</td></tr><tr><td>Dark Dot</td><td>≤ 5</td></tr><tr><td>Joint Dot</td><td>≤ 3</td></tr><tr><td>Total</td><td>≤ 7</td></tr></table>	Item	Acceptance (Q'ty)	Bright Dot	≤ 4	Dark Dot	≤ 5	Joint Dot	≤ 3	Total	≤ 7	Minor
		Item	Acceptance (Q'ty)										
Bright Dot	≤ 4												
Dark Dot	≤ 5												
Joint Dot	≤ 3												
Total	≤ 7												
5.1 Inspection pattern: full white, full black, Red, Green and blue screens. 5.2 It is defined as dot defect if defect area > 1/2 dot. 5.3 The distance between two dot defect ≥ 5 mm. 5.4 Bright dot : Dots appear bright and unchanged in visible with 5% ND filter is defined. 5.5 Tiny bright dot: bright dot area ≤ 1/2 dot. a. Dots appear bright and unchanged in visible with 5% ND filter is defined defect and is judged in accordance with 6.1 b. Dots invisible with 5% ND Filter is Ignored													

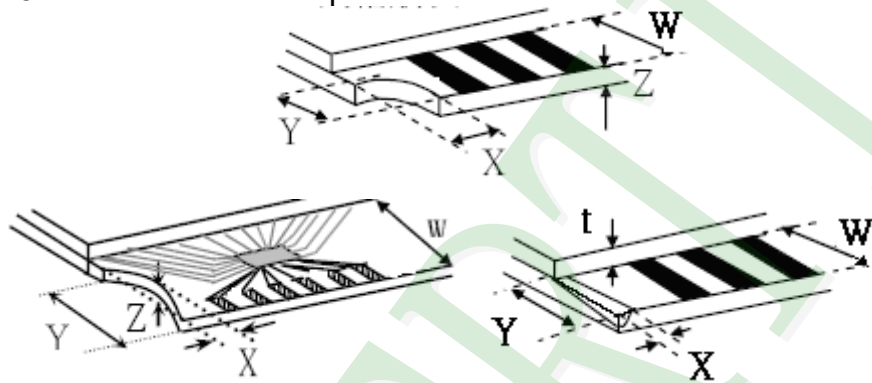
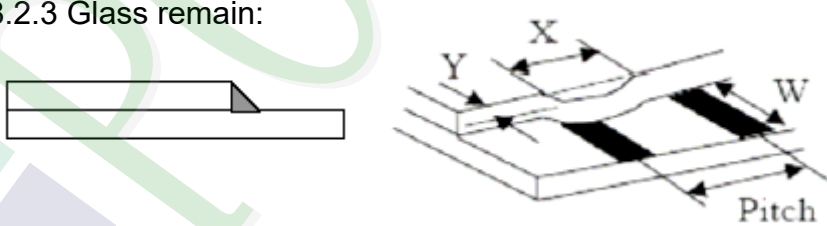

◆ Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level																																																				
06	<p>Black or white Dot, scratch, contamination</p> <p>Round type</p>  <p>$\Phi = (x + y) / 2$</p> <p>Line type</p> 	<p>6.1 Round type (Non-display or display):</p> <table><thead><tr><th rowspan="2">Dimension (diameter: Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr></thead><tbody><tr><td>$\Phi \leq 0.25$</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>$0.25 < \Phi \leq 0.50$</td><td>5</td></tr><tr><td>$\Phi > 0.50$</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></tbody></table> <p>6.2 Line type(Non-display or display):</p> <table><thead><tr><th rowspan="2">module size</th><th rowspan="2">Length (L)</th><th rowspan="2">Width (W)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr></thead><tbody><tr><td rowspan="5">3.5" to less 9"</td><td>---</td><td>$W \leq 0.03$</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>$L \leq 10.0$</td><td>$0.03 < W \leq 0.05$</td><td>4</td></tr><tr><td>$L \leq 5.0$</td><td>$0.05 < W \leq 0.10$</td><td>2</td></tr><tr><td>---</td><td>$W > 0.10$</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr><tr><td rowspan="4">9" to 15"</td><td>---</td><td>$W \leq 0.05$</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>$L \leq 10.0$</td><td>$0.05 < W \leq 0.10$</td><td>5</td></tr><tr><td>---</td><td>$W > 0.10$</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr></tbody></table>	Dimension (diameter: Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.25$	Ignore	Ignore	$0.25 < \Phi \leq 0.50$	5	$\Phi > 0.50$	0	Total	5	module size	Length (L)	Width (W)	Acceptance (Q'ty)		A area	B area	3.5" to less 9"	---	$W \leq 0.03$	Ignore	Ignore	$L \leq 10.0$	$0.03 < W \leq 0.05$	4	$L \leq 5.0$	$0.05 < W \leq 0.10$	2	---	$W > 0.10$	As round type	Total		5	9" to 15"	---	$W \leq 0.05$	Ignore	Ignore	$L \leq 10.0$	$0.05 < W \leq 0.10$	5	---	$W > 0.10$	As round type	Total		5	Minor
		Dimension (diameter: Φ)		Acceptance (Q'ty)																																																			
A area	B area																																																						
$\Phi \leq 0.25$	Ignore	Ignore																																																					
$0.25 < \Phi \leq 0.50$	5																																																						
$\Phi > 0.50$	0																																																						
Total	5																																																						
module size	Length (L)	Width (W)	Acceptance (Q'ty)																																																				
			A area	B area																																																			
3.5" to less 9"	---	$W \leq 0.03$	Ignore	Ignore																																																			
	$L \leq 10.0$	$0.03 < W \leq 0.05$	4																																																				
	$L \leq 5.0$	$0.05 < W \leq 0.10$	2																																																				
	---	$W > 0.10$	As round type																																																				
	Total		5																																																				
9" to 15"	---	$W \leq 0.05$	Ignore	Ignore																																																			
	$L \leq 10.0$	$0.05 < W \leq 0.10$	5																																																				
	---	$W > 0.10$	As round type																																																				
	Total		5																																																				
07	<p>Polarizer Bubble</p> <table><thead><tr><th rowspan="2">Dimension (diameter: Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr></thead><tbody><tr><td>$\Phi \leq 0.25$</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>$0.25 < \Phi \leq 0.50$</td><td>4</td></tr><tr><td>$0.50 < \Phi \leq 0.80$</td><td>1</td></tr><tr><td>$\Phi > 0.80$</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></tbody></table>	Dimension (diameter: Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.25$	Ignore	Ignore	$0.25 < \Phi \leq 0.50$	4	$0.50 < \Phi \leq 0.80$	1	$\Phi > 0.80$	0	Total	5	Minor																																					
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NO	Item	Criterion	Level									
08	The crack of glass	Symbols : X: The length of crack Z: The thickness of crack T: The thickness of glass Y: The width of crack. W: terminal length a : LCD side length	Minor									
		8.1 General glass chip: 8.1.1 Chip on panel surface and crack between panels: 										
		<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$\leq a$</td><td>Crack can't enter viewing area</td><td>$\leq 1/2 t$</td></tr><tr><td>$\leq a$</td><td>Crack can't exceed the half of SP width.</td><td>$1/2 t < Z \leq 2 t$</td></tr></table>	X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$	$\leq a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	
X	Y	Z										
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08	The crack of glass	<p>Symbols :</p> <p>X: The length of crack Z: The thickness of crack t: The thickness of glass</p> <p>Y: The width of crack. W: terminal length a: LCD side length</p> <p>8.1.2 Corner crack:</p>  <table><thead><tr><th><u>X</u></th><th><u>Y</u></th><th><u>Z</u></th></tr></thead><tbody><tr><td>$\leq 1/5 a$</td><td>Crack can't enter viewing area</td><td>$Z \leq 1/2 t$</td></tr><tr><td>$\leq 1/5 a$</td><td>Crack can't exceed the half of SP width.</td><td>$1/2 t < Z \leq 2 t$</td></tr></tbody></table>	<u>X</u>	<u>Y</u>	<u>Z</u>	$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$	$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	Minor
		<u>X</u>	<u>Y</u>	<u>Z</u>								
$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$										
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<p>8.2 Protrusion over terminal:</p> <p>8.2.1 Chip on electrode pad:</p>  <table><thead><tr><th></th><th><u>X</u></th><th><u>Y</u></th><th><u>Z</u></th></tr></thead><tbody><tr><td><u>Front</u></td><td>$\leq a$</td><td>$\leq 1/2 W$</td><td>$\leq t$</td></tr><tr><td><u>Back</u></td><td>$\leq a$</td><td>$\leq W$</td><td>$\leq 1/2 t$</td></tr></tbody></table>		<u>X</u>	<u>Y</u>	<u>Z</u>	<u>Front</u>	$\leq a$	$\leq 1/2 W$	$\leq t$	<u>Back</u>	$\leq a$	$\leq W$	$\leq 1/2 t$
	<u>X</u>	<u>Y</u>	<u>Z</u>									
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08	The crack of glass	<p>Symbols:</p> <p>X: The length of crack Z: The thickness of crack t: The thickness of glass</p> <p>Y: The width of crack. W: terminal length a: LCD side length</p> <p>8.2.2 Non-conductive portion:</p>  <table><thead><tr><th><u>X</u></th><th><u>Y</u></th><th><u>Z</u></th></tr></thead><tbody><tr><td>$\leq 1/3 a$</td><td>$\leq W$</td><td>$\leq t$</td></tr></tbody></table> <p>If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>8.2.3 Glass remain:</p>  <table><thead><tr><th><u>X</u></th><th><u>Y</u></th><th><u>Z</u></th></tr></thead><tbody><tr><td>$\leq a$</td><td>$\leq 1/3 W$</td><td>$\leq t$</td></tr></tbody></table> <p>8.2.4 Cracking:</p>  <p>Not Allowed</p>	<u>X</u>	<u>Y</u>	<u>Z</u>	$\leq 1/3 a$	$\leq W$	$\leq t$	<u>X</u>	<u>Y</u>	<u>Z</u>	$\leq a$	$\leq 1/3 W$	$\leq t$	Minor
<u>X</u>	<u>Y</u>	<u>Z</u>													
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$\leq a$	$\leq 1/3 W$	$\leq t$													

◆ Specification For TFT-LCD Module 3.5" ~15"

(Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
		10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤ 1.5 mm.	Minor

4. Reliability Test

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION	
1	High Temperature Storage Test	Keep in 80 ±5℃ 240 hrs	
2	High Temperature Operating Test	Keep in 70 ±5℃ 240 hrs	
3	Low Temperature Storage Test	Keep in -30 ±5℃ 240 hrs	
4	Low Temperature Operating Test	Keep in -20 ±5℃ 240 hrs	
5	High Temperature / High Humidity Storage Test	Keep in 60 ℃ / 90% R.H duration for 240 hrs (Excluding the polarizer)	
6	Temperature Cycling Storage Test	<div>-30℃ → +25℃ → 80℃ → +25℃ (30mins) (5mins) (30mins) (5mins) ←──</div>	

◎Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state.

Do not change these conditions as such changes may affect practical display function.

(Normal operation state)

Temperature : +20~30℃, Humidity : 50~70%, Atmospheric pressure : 86~106Kpa

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

