

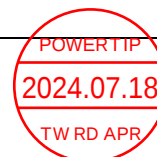
SPECIFICATIONS

CUSTOMER	:	-
SAMPLE CODE	:	SH800480T030-ZFR
MASS PRODUCTION CODE	:	PH800480T030-ZFR
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	002
DRAWING NO. (Ver.)	:	LMD-PH800480T030-ZFR(Ver.002)
PACKAGING NO. (Ver.)	:	PKG-PH800480T030-ZFR(Ver.001)

Customer Approved**Date:**

Approved	Checked	Designer
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- ☐ Preliminary specification for design input
☒ Specification for sample approval

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NO.PT-A-005-8

History of Version

<u>Date</u> (mm / dd / yyyy)	<u>Ver.</u>	<u>Edi.</u>	<u>Description</u>	<u>Page</u>	<u>Design by</u>
05/09/2024	01	001	Preliminary.	-	Yantong
07/08/2024	01	002	First Sample. Side View adjustment.	- Appendix	Yantong

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1. SPECIFICATIONS

1.1 Features

<u>Item</u>	<u>Standard Value</u>
Display Resolution	800 * 3 (RGB) * 480 Dots
LCD Type	Full Viewing Angle, Normally Black, Transmissive type, Black Panel Effect
Screen size(inch)	5.0 inch
Optical Type	OCR
Color configuration	R.G.B. Vertical Stripe
Backlight Type	White LED B/L
Weight	89g
Interface	24 Bits RGB interface
Driver IC	ST7262
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : http://www.powertip.com.tw/news_detail.php?Key=1&clD=1

1.2 Mechanical Specifications

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Outline Dimension	137.0(W) x 96.5(L) x 4.9(H)	mm

LCD panel

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Active Area	108.0 (W) x 64.8(L)	mm
Pixel Size	0.135 (W) * 0.135 (H)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Module

<u>Item</u>	<u>Symbol</u>	<u>Condition</u>	<u>Min.</u>	<u>Max.</u>	<u>Unit</u>	<u>Remark</u>
Power Supply	VDD	GND=0	-0.3	4.5	V	-
Operating Temperature	T _{OP} (Ts)	Note 1	-20	70	°C	
Storage Temperature	T _{ST} (Ta)	Note 2	-30	80	°C	
Storage Humidity	H _D	Ta < 60 °C	-	90	%RH	

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1 : Ts is the temperature of panel's surface.

Note 2 : Ta is the ambient temperature of samples.

1.4 DC Electrical Characteristics

Module

GND = 0V, Ta = 25°C

<u>Item</u>	<u>Symbol</u>	<u>Condition</u>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>Unit</u>
Power Supply	VDD	GND=0V	3.0	3.3	3.6	V
Input Voltage	V _{IH}	GND=0V	0.7VDD	-	VDD	V
	V _{IL}	GND=0V	0	-	0.3VDD	
Output H/L Level Voltage	VOH	GND=0V	VDD-0.4	-	VDD	V
	VOL	GND=0V	GND	-	GND+0.4	
Supply Current	IDD	VDD= 3.3 V Pattern=RGB *1	-	75	110	mA

Note1:Maximum current display

1.5 Optical Characteristics

TFT LCD Module

VDD= 3.3 V, Ta=25°C

Item		Symbol	Condition	Min.	Typ.	Max.	unit	
Response time	Tr+Tf	25℃	-	-	37	56	ms	Note 2
Viewing angle	Top	θY+	CR ≥ 10		80	-	Deg.	Note 4
	Bottom	θY-			80	-		
	Left	θX-			80	-		
	Right	θX+			80	-		
Contrast ratio		CR	(Ta = 25℃ θX , θY = 0°)	650	800	-	-	Note 3
Color of CIE Coordinate	White	X		0.28	0.33	-0.38	-	Note1
		Y		0.31	0.36	0.41		
	Red	X		0.56	0.61	0.66		
		Y		0.30	0.35	0.40		
	Green	X		0.32	0.37	0.42		
		Y		0.54	0.59	0.64		
	Blue	X		0.09	0.14	0.19		
		Y		0.07	0.12	0.17		
Average Brightness Pattern=white display (With B/L & T/P)*2		IV		IF= 40 mA	400	500	-	cd/m2
Uniformity (With B/L & T/P)*1		△B	IF= 40 mA	70	-	-	%	Note1

Note 1:

*1 : $\Delta B = B(\min) / B(\max) * 100\%$

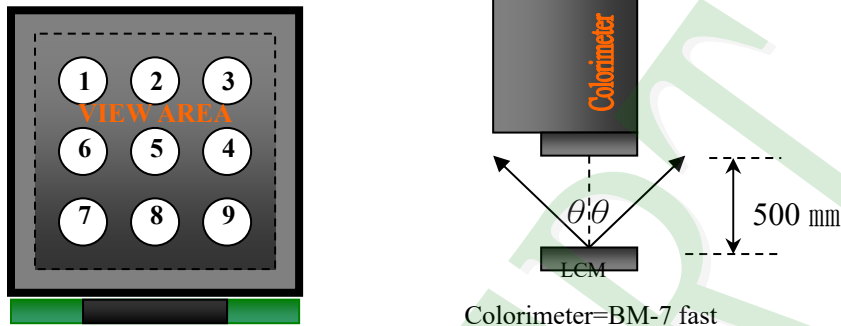
*2 : Measurement Condition for Optical Characteristics:

a : Environment: $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ / $60 \pm 20\%$ R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: 500 ± 50 mm , ($\theta = 0^{\circ}$)

c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.

d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

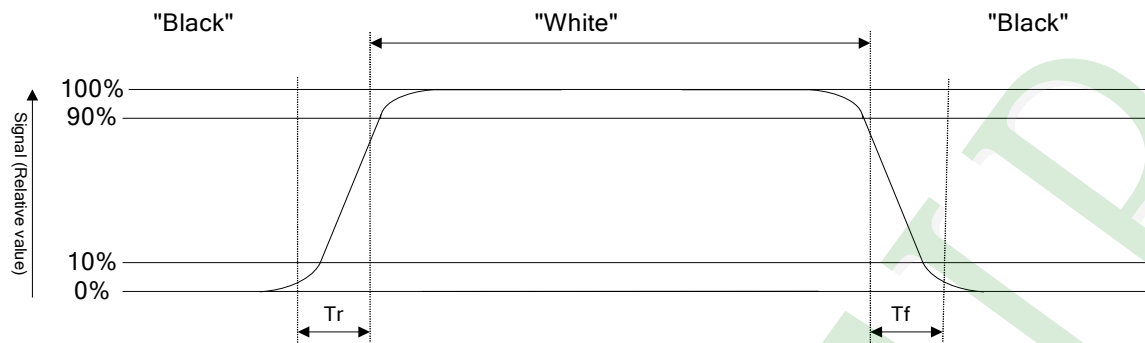
The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White



Normally Black



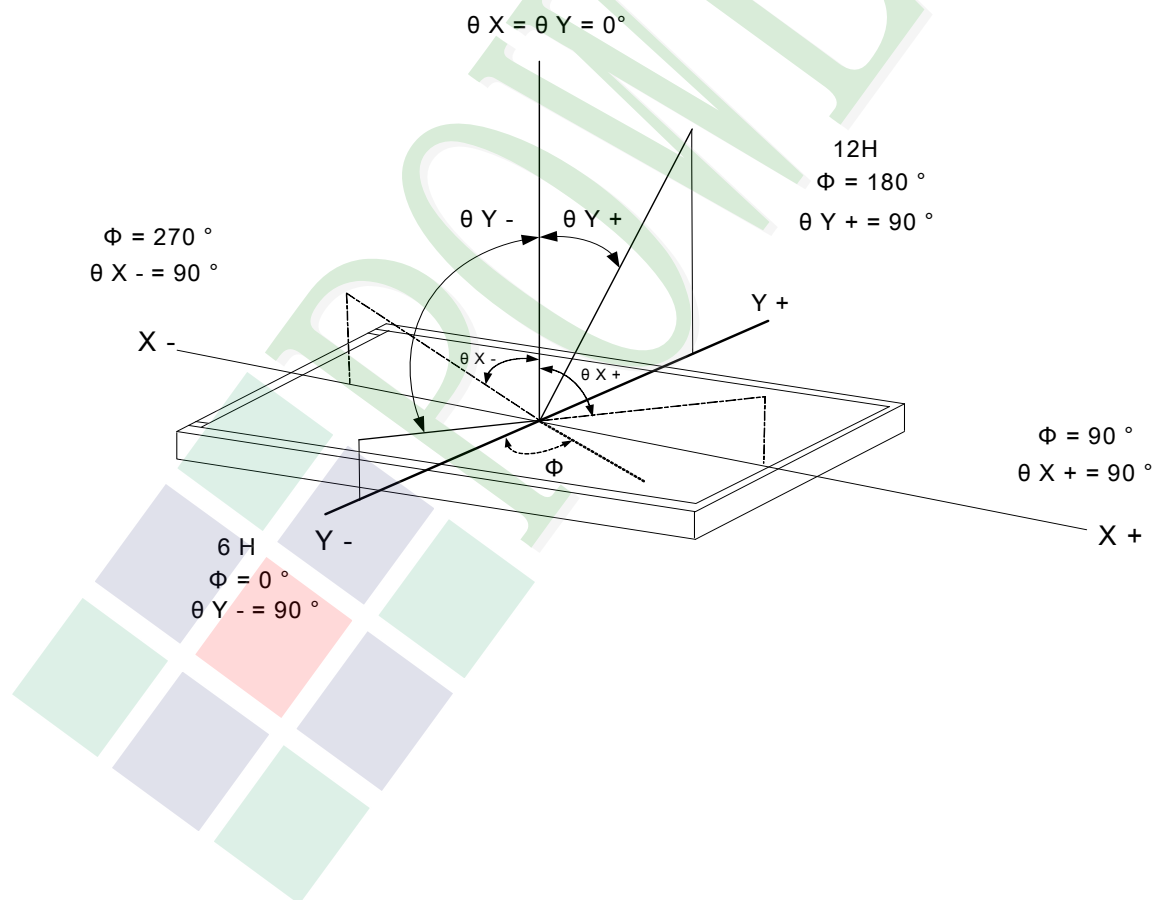
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



1.6 Backlight Characteristics

Maximum Ratings

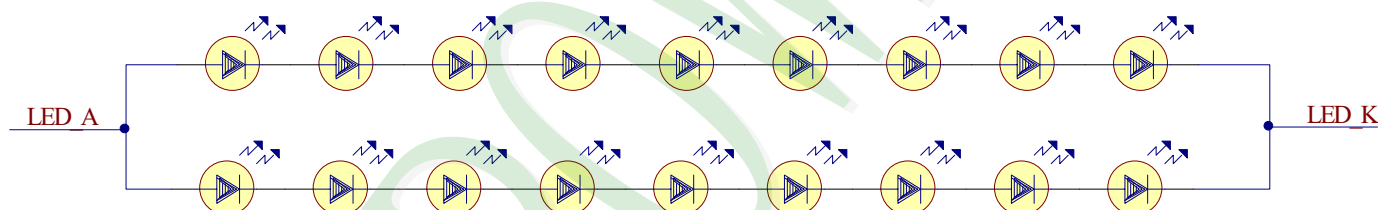
<u>Item</u>	<u>Symbol</u>	<u>Min.</u>	<u>Max.</u>	<u>Unit</u>	<u>Remark</u>
LED Forward Current	I_F	-	30*2	mA	-
LED Reverse Voltage	V_R	-	1.2*9	V	
Power Dissipation	PD		1782	mW	

Electrical / Optical Characteristics

<u>Item</u>	<u>Symbol</u>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>Unit</u>	<u>Remark</u>
LED Voltage	V_L	26.1	27.9	29.7	V	Note1
LED Current	I_L	-	40	-	mA	-
LED life time	-	50,000	-	-	H _r	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at $T_a=25^{\circ}\text{C}$ and $I_L=40\text{ mA}$.

Note 2: The “LED life time” is defined as the module brightness decrease to 50% original brightness at $T_a=25^{\circ}\text{C}$ and $I_L=40\text{ mA}$. The LED life time could be decreased if operating I_L is larger than 40 mA.



1.7 Touch Panel Characteristics

Features

<u>Item</u>	<u>Standard Value</u>
Touch Panel Size	5.0"
Touch type	Capacitive Touch Panel
Input Method	True Multi-Touch Capacitive Touch Panel True Multi-touch with up to 5 Points of Absolution
Output Interface	I ² C
IC	ICNT8952

I²C Address

<u>Bit 7</u>	<u>Bit 6</u>	<u>Bit 5</u>	<u>Bit 4</u>	<u>Bit 3</u>	<u>Bit 2</u>	<u>Bit 1</u>	<u>Bit 0</u>
1	0	0	1	0	0	0	R/W

Bit 0: 0 for Write / 1 for Read

Mechanical Specifications

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Viewing Area	109.0 (W) * 65.8 (L)	mm

Absolute Maximum Ratings

<u>Item</u>	<u>Symbol</u>	<u>Condition</u>	<u>Min.</u>	<u>Max.</u>	<u>Unit</u>
Supply voltage	TPVDD	-	-0.3	3.6	V
Operating Temperature	T _{OP}	Non condenssing	-20	70	°C
Storage Temperature	T _{ST}	Non condenssing	-30	80	°C

DC Electrical Characteristics

<u>Item</u>	<u>Symbol</u>	<u>Condition</u>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>Unit</u>
Supply voltage	TPVDD	25°C	-	3.3	-	V
Input high-level voltage	VIH	--	0.7 x TPVDD	-	TPVDD	V
Input low -level voltage	VIL	--	-0.3	-	0.3 x TPVDD	V
Output high -level voltage	VOH	--	0.7 x TPVDD	-	-	V
Output low -level voltage	VOL	--	-	-	0.3 x TPVDD	V

Optical Characteristics

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
Total light transmittance	35% or more	-
Hardness	$\geq 6H$	-

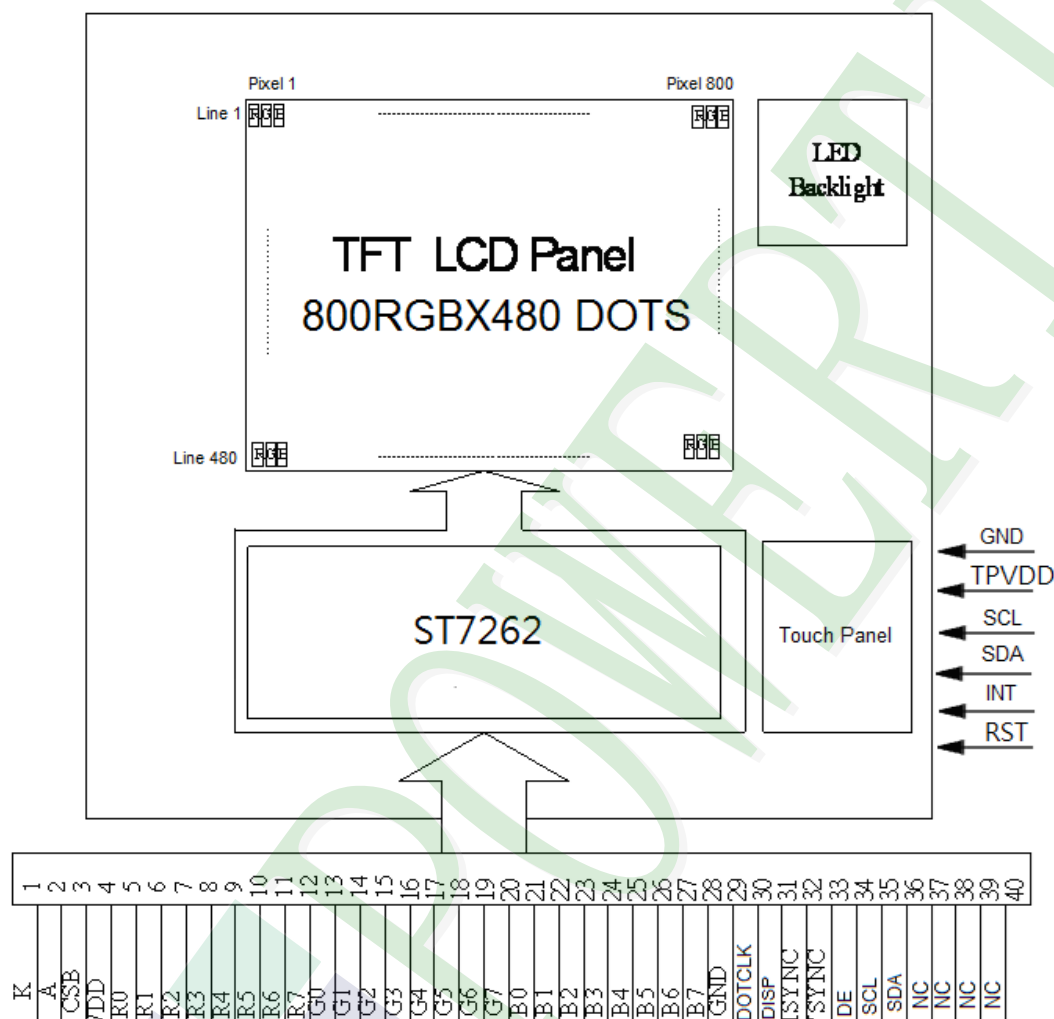
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



2.2 Interface Pin Description

TFT LCM Interface

<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
1	LED-	Power supply for LED Backlight cathode input
2	LED+	Power supply for LED Backlight anode input
3	CS(NC)	Chip select pin of serial interface.
4	VDD	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7

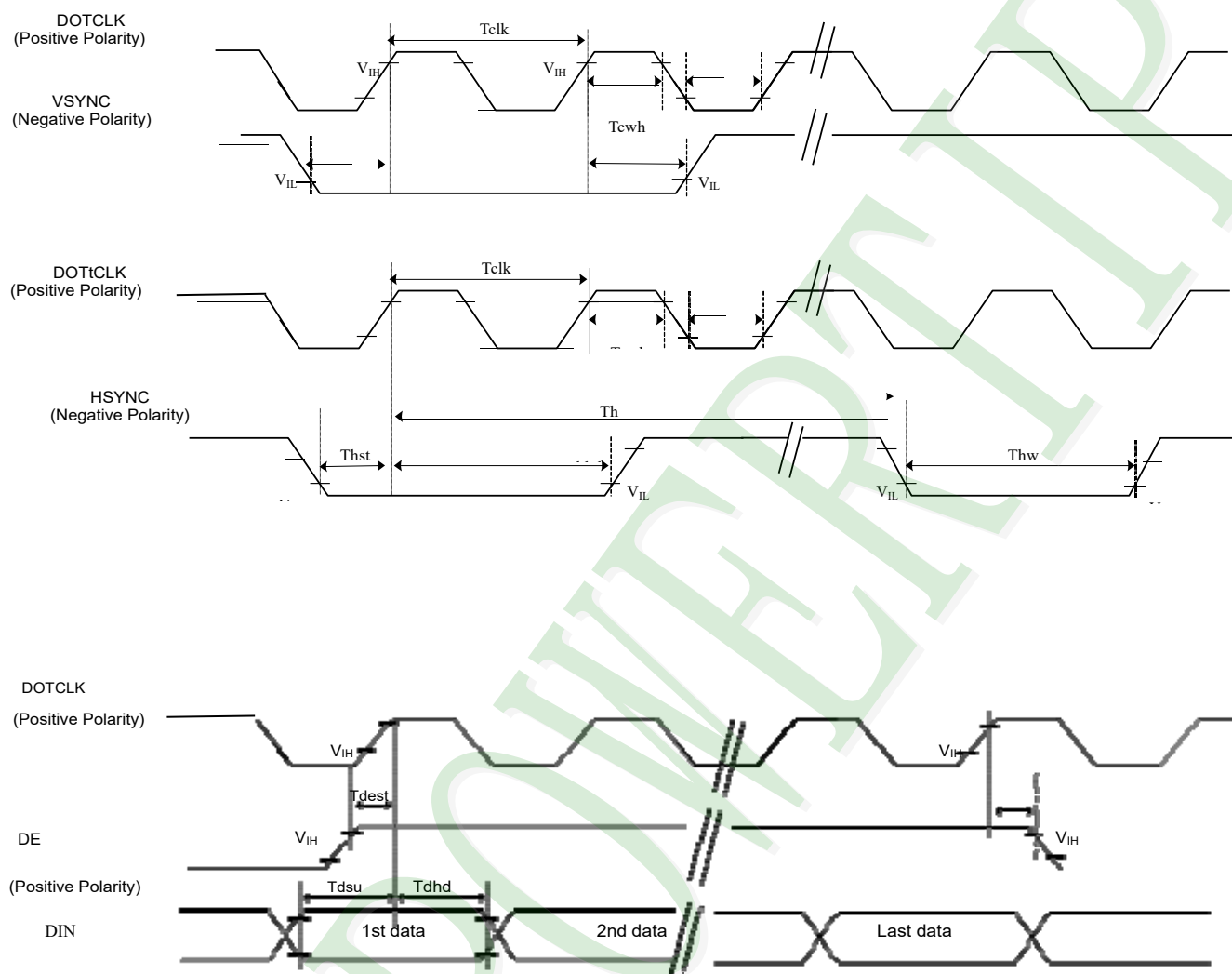
<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
21	B0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	B3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	B6	Blue data bit 6
28	B7	Blue data bit 7
29	GND	Ground
30	DOTCLK	Dot data clock, latching data at the rising edge
31	DISP	Display on/ off
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data enable
35	SCL(NC)	Data input and output in Serial communication/No connection when no using serial communication
36	SDA(NC)	Clock input in Serial communication / No connection when no using serial communication
37	NC	Not Connect
38	NC	Not Connect
39	NC	Not Connect
40	NC	Not Connect

Capacitive Touch Panel (CTP) Interface

<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
1	GND	Touch Panel Ground.
2	TPVDD	Power Supply Voltage (3.3V)
3	SCL	I2C Clock
4	SDA	I2C Data
5	INT	The interrupt the CTP to the Host
6	RST	RESET

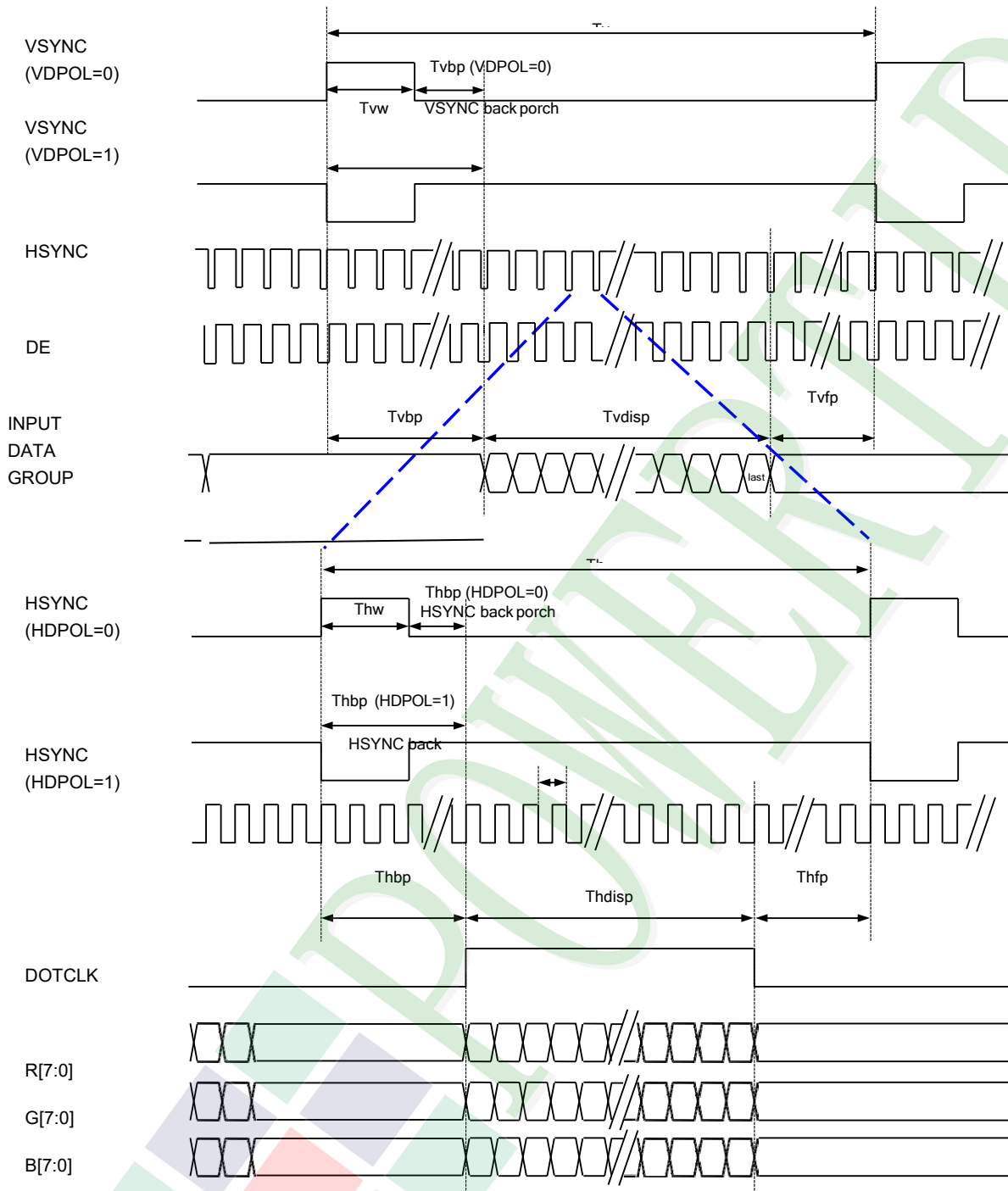
2.3 Timing Characteristics

2.3.1 System Bus Timing for RGB Interface

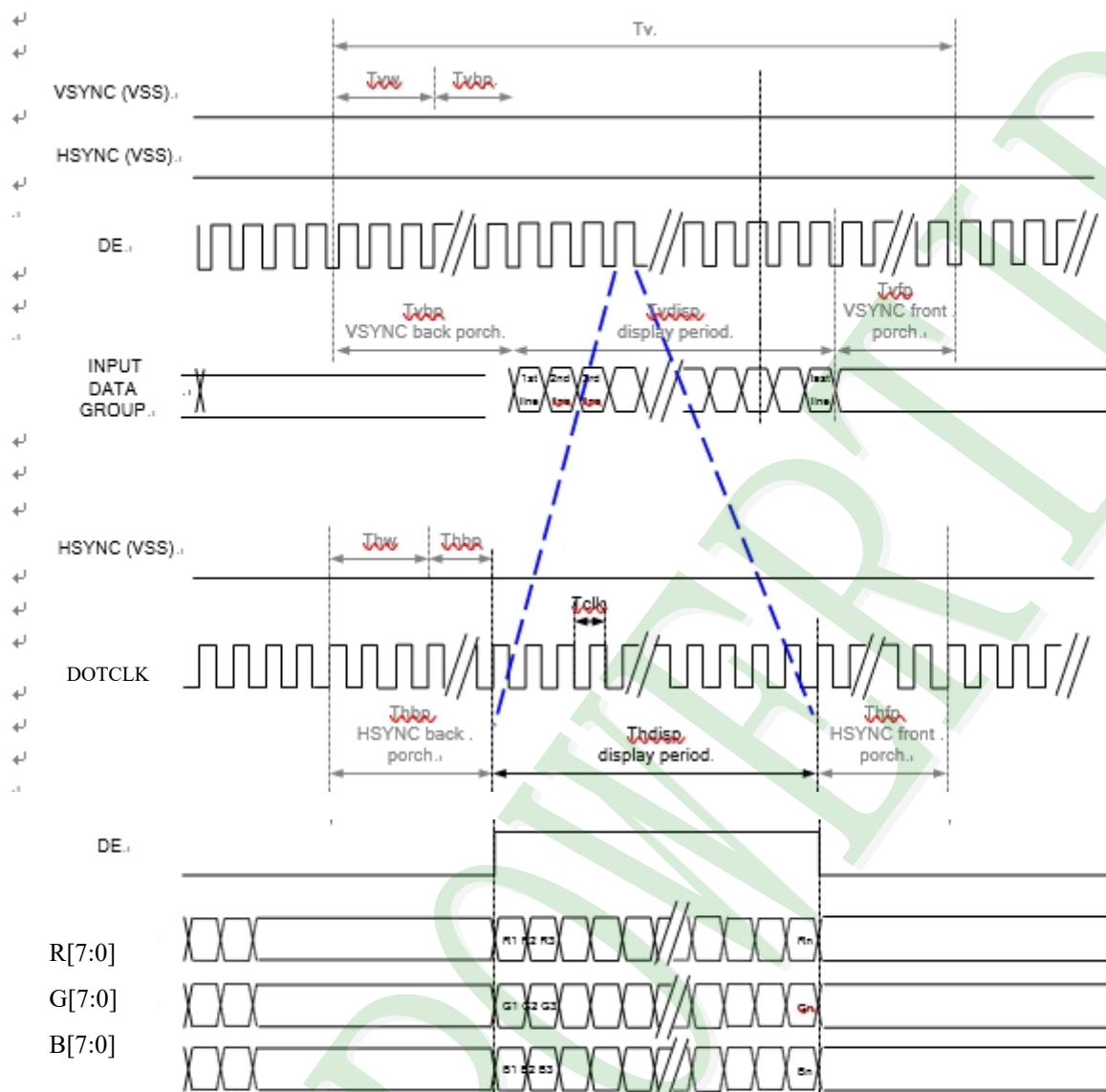


<u>Parameters</u>	<u>Symbol</u>	<u>Spec</u>			<u>Unit</u>	<u>Conditions</u>
		<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>		
VDD Power ON slew rate	TPOR	--	--	20	ms	From 0V to 99% VDD
GRB pulse width	tRSTW	10	50	--	us	R=10Kohm, C=1uF
CLK Pulse Duty	Tcw	40	50	60	%	
HSYNC Width	Thw	2	-	-	DCLK	
HSYNC Period	Th	55	60	65	us	
VSYNC Setup Time	Tvst	12	-	-	ns	
VSYNC Hold Time	Tvhd	12	-	-	ns	
HSYNC Setup Time	Thst	12	-	-	ns	
HSYNC Hold Time	Thhd	12	-	-	ns	
Data Setup Time	Tdsu	12	-	-	ns	
Data Hold Time	Tdhd	12	-	-	ns	
DE Setup Time	Tdest	12	-	-	ns	
DE Hold Time	Tdehd	12	-	-	ns	

2.3.3 SYNC-DE Mode



2.3.4 DE Mode

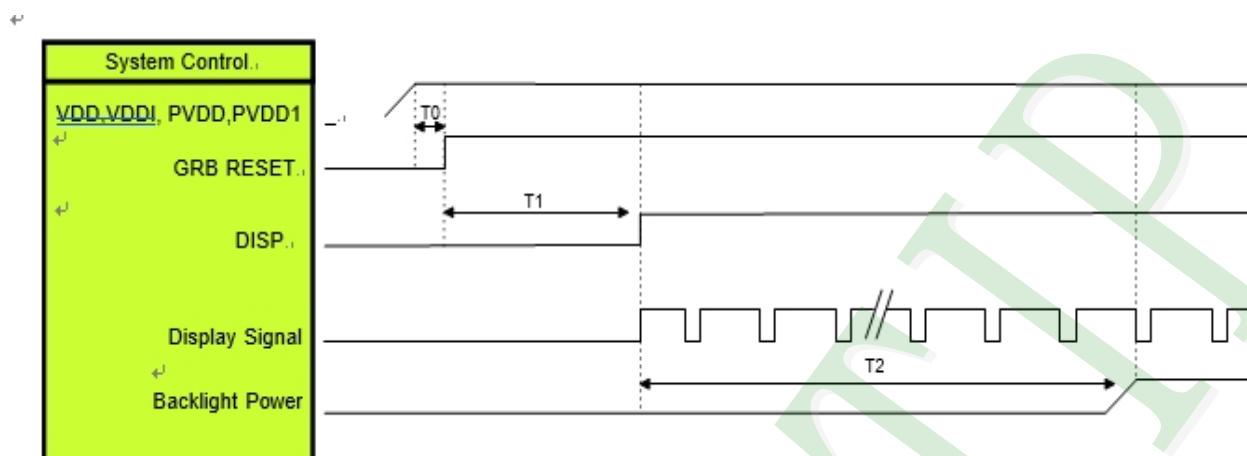


2.3.5 Parallel 24-bit RGB Input Timing Table

Parallel 24-bit RGB Interface Timing Table						
Item		Symbol	Min.	Typ.	Max.	Unit
DOTCLK Frequency		Fclk	23	25	27	MHz
HSYNC	Period Time	Th	808	816	896	DCLK
	Display Period	Thdisp	800			DCLK
	Back Porch	Thbp	4	8	48	DCLK
	Front Porch	Thfp	4	8	48	DCLK
	Pulse Width	Thw	2	4	8	DCLK
VSYNC	Period Time	Tv	488	496	504	HSYNC
	Display Period	Tvdisp	480			HSYNC
	Back Porch	Tvbp	4	8	12	HSYNC
	Front Porch	Tvfp	4	8	12	HSYNC
	Pulse Width	Tvw	2	4	8	HSYNC

2.4 POWER ON/OFF SEQUENCE

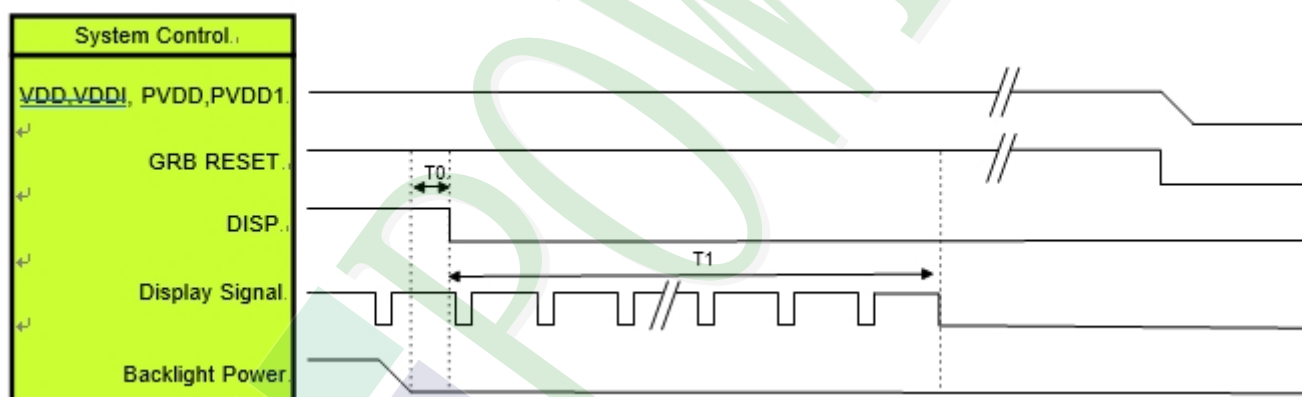
2.4.1 Power On Sequence



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0] Note: LVDS interface Display signal: DCLK P/N; RX[3:0]P/N

2.4.2 Power Off Sequence

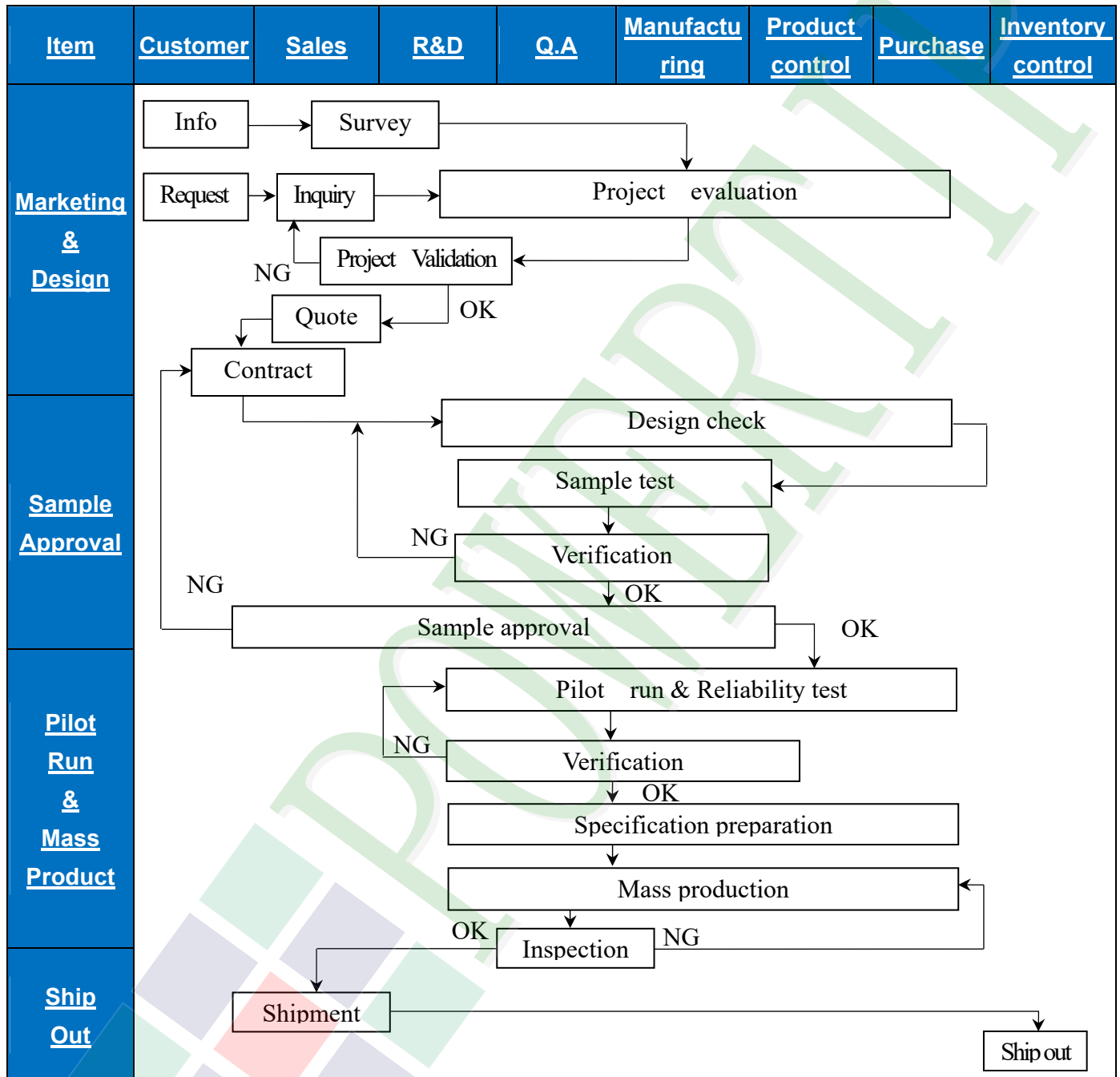


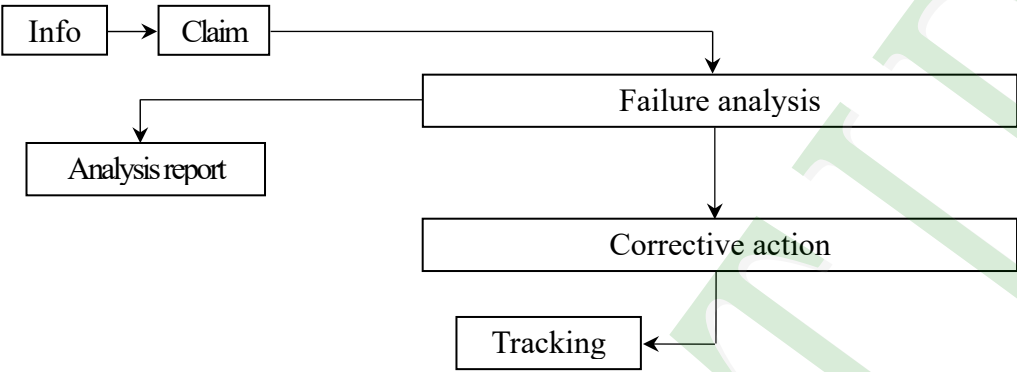
Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	100	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0] Note: LVDS interface Display signal: DCLK P/N; RX[3:0]P/N

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



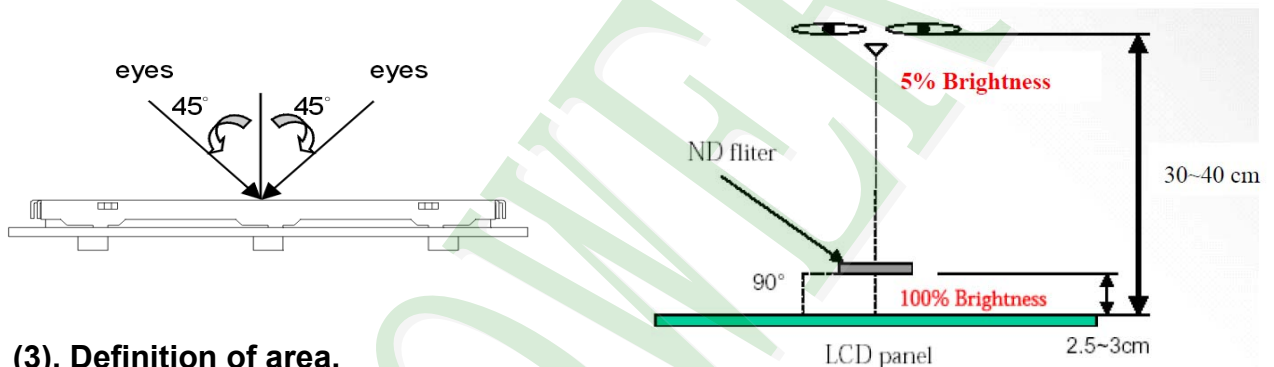
Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
<u>Sales Service</u>	 <pre> graph TD Info[Info] --> Claim[Claim] Claim --> Failure[Failure analysis] Claim --> Report[Analysis report] Failure --> Action[Corrective action] Action --> Tracking[Tracking] </pre>							
<u>Q.A Activity</u>	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

3.2. Inspection Specification

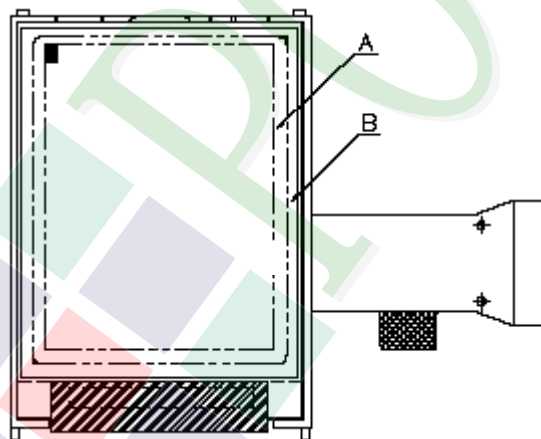
- ◆Scope : The document shall be applied to TFT-LCD Module for 3.5" ~15" (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II.
- ◆Equipment : Gauge 、MIL-STD 、Powertip Tester 、Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ◆OUT Going Defect Level : Sampling.
- ◆Standard of the product appearance test :

a. Manner of appearance test :

- (1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)
 , and distance of view must be at 30~40 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

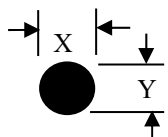
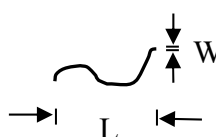
(4). Standard of inspection : (Unit : mm)

◆Specification For TFT-LCD Module 3.5" ~15" :
(Ver.B01)

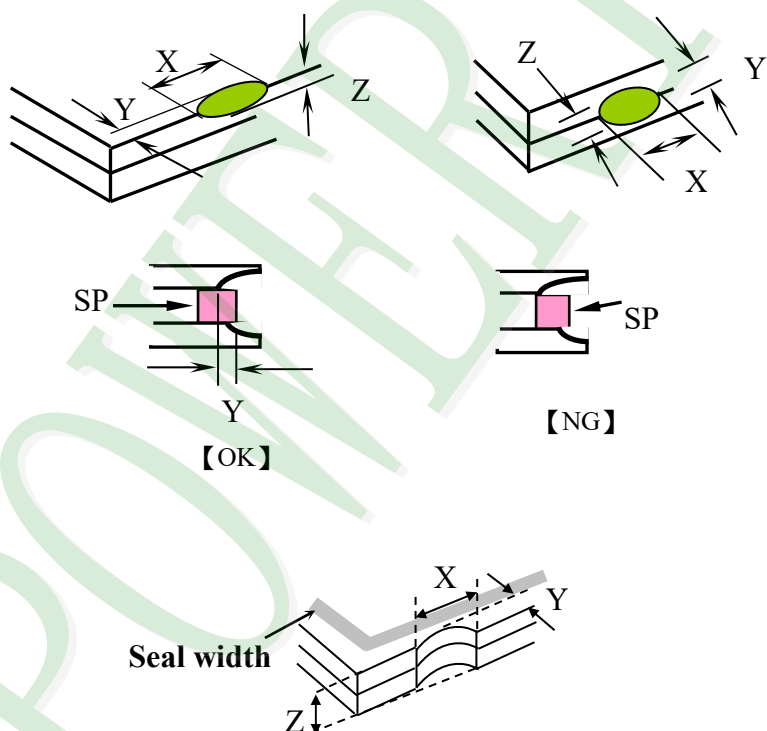
NO	Item	Criterion	Level												
01	Product condition	1. 1 The part number is inconsistent with work order of production.	Major												
		1. 2 Mixed product types.	Major												
		1. 3 Assembled in inverse direction.	Major												
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major												
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major												
04	Electrical Testing	4. 1 Missing line character and icon.	Major												
		4. 2 No function or no display.	Major												
		4. 3 Display malfunction.	Major												
		4. 4 LCD viewing angle defect.	Major												
		4. 5 Current consumption exceeds product specifications.	Major												
		4. 6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.	Minor												
05	Dot defect (Bright dot, Dark dot) On -display	<table><tr><th colspan="2">Item</th><th>Acceptance (Q'ty)</th></tr><tr><td rowspan="4">Dot Defect</td><td>Bright Dot</td><td>≤ 4</td></tr><tr><td>Dark Dot</td><td>≤ 5</td></tr><tr><td>Joint Dot</td><td>≤ 3</td></tr><tr><td>Total</td><td>≤ 7</td></tr></table>	Item		Acceptance (Q'ty)	Dot Defect	Bright Dot	≤ 4	Dark Dot	≤ 5	Joint Dot	≤ 3	Total	≤ 7	Minor
		Item		Acceptance (Q'ty)											
Dot Defect	Bright Dot	≤ 4													
	Dark Dot	≤ 5													
	Joint Dot	≤ 3													
	Total	≤ 7													
5.1 Inspection pattern: full white, full black, Red, Green and blue screens. 5.2 It is defined as dot defect if defect area > 1/2 dot. 5.3 The distance between two dot defect ≥ 5 mm. 5.4 Bright dot : Dots appear bright and unchanged in visible with 5% ND filter is defined. 5.5 Tiny bright dot: bright dot area ≤ 1/2 dot. a. Dots appear bright and unchanged in visible with 5% ND filter is defined defect and is judged in accordance with 6.1 b. Dots invisible with 5% ND Filter is Ignored.															

◆ Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

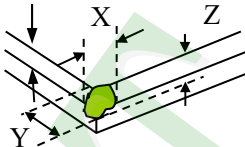
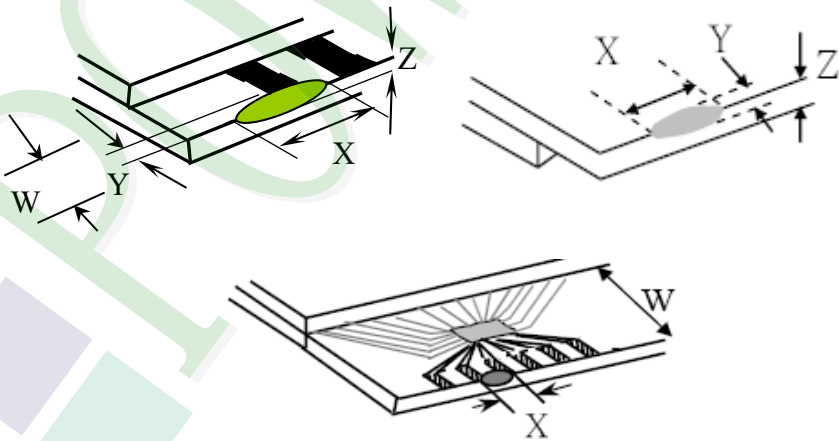
NO	Item	Criterion	Level																																																				
06	Black or white Dot, scratch, contamination Round type  $\Phi = (x + y) / 2$ Line type 	6. 1 Round type (Non-display or display): <table><tr><th rowspan="2">Dimension (diameter: Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>$\Phi \leq 0.25$</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>$0.25 < \Phi \leq 0.50$</td><td>5</td></tr><tr><td>$\Phi > 0.50$</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table> 6. 2 Line type(Non-display or display): <table><tr><th rowspan="2">module size</th><th rowspan="2">Length (L)</th><th rowspan="2">Width (W)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td rowspan="5">3.5" to less 9"</td><td>---</td><td>$W \leq 0.03$</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>$L \leq 10.0$</td><td>$0.03 < W \leq 0.05$</td><td>4</td></tr><tr><td>$L \leq 5.0$</td><td>$0.05 < W \leq 0.10$</td><td>2</td></tr><tr><td>---</td><td>$W > 0.10$</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr><tr><td rowspan="5">9" to 15"</td><td>---</td><td>$W \leq 0.05$</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>$L \leq 10.0$</td><td>$0.05 < W \leq 0.10$</td><td>5</td></tr><tr><td>---</td><td>$W > 0.10$</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr></table>	Dimension (diameter: Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.25$	Ignore	Ignore	$0.25 < \Phi \leq 0.50$	5	$\Phi > 0.50$	0	Total	5	module size	Length (L)	Width (W)	Acceptance (Q'ty)		A area	B area	3.5" to less 9"	---	$W \leq 0.03$	Ignore	Ignore	$L \leq 10.0$	$0.03 < W \leq 0.05$	4	$L \leq 5.0$	$0.05 < W \leq 0.10$	2	---	$W > 0.10$	As round type	Total		5	9" to 15"	---	$W \leq 0.05$	Ignore	Ignore	$L \leq 10.0$	$0.05 < W \leq 0.10$	5	---	$W > 0.10$	As round type	Total		5	Minor
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	07 Polarizer Bubble <table><tr><th rowspan="2">Dimension (diameter: Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>$\Phi \leq 0.25$</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>$0.25 < \Phi \leq 0.50$</td><td>4</td></tr><tr><td>$0.50 < \Phi \leq 0.80$</td><td>1</td></tr><tr><td>$\Phi > 0.80$</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table>	Dimension (diameter: Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.25$	Ignore	Ignore	$0.25 < \Phi \leq 0.50$	4	$0.50 < \Phi \leq 0.80$	1	$\Phi > 0.80$	0	Total	5	Minor																																					
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◆Specification For TFT-LCD Module 3.5" ~15" :
(Ver.B01)

NO	Item	Criterion	Level						
08	The crack of glass	Symbols : X: The length of crack Z: The thickness of crack T: The thickness of glass Y: The width of crack. W: terminal length a : LCD side length 8.1 General glass chip: 8.1.1 Chip on panel surface and crack between panels: 	Minor						
		<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$\leq a$</td><td>Crack can't enter viewing area</td><td>$\leq 1/2 t$</td></tr><tr><td>$\leq a$</td><td>Crack can't exceed the half of SP width.</td><td>$1/2 t < Z \leq 2 t$</td></tr></table>		X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$
X	Y	Z							
$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$							
$\leq a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$							

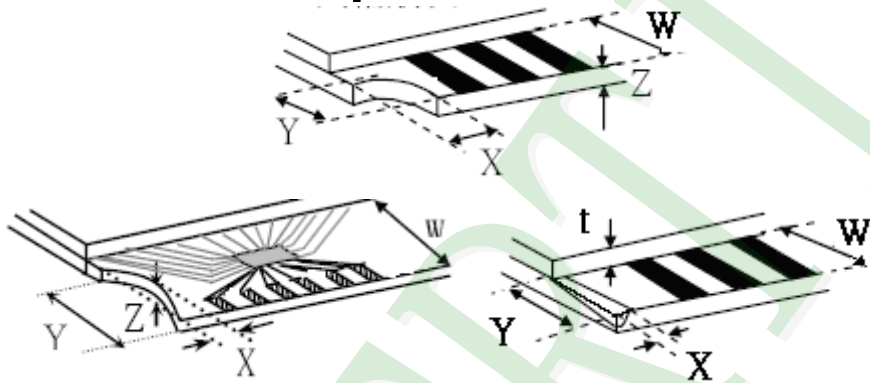
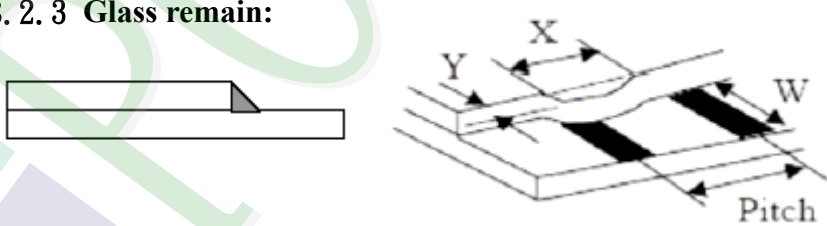
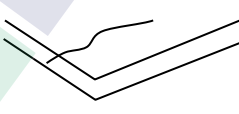
◆ Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level									
08	The crack of glass	Symbols : X: The length of crack Z: The thickness of crack t: The thickness of glass Y: The width of crack. W: terminal length a: LCD side length 8.1.2 Corner crack:  <table><thead><tr><th><u>X</u></th><th><u>Y</u></th><th><u>Z</u></th></tr></thead><tbody><tr><td>$\leq 1/5 a$</td><td>Crack can't enter viewing area</td><td>$Z \leq 1/2 t$</td></tr><tr><td>$\leq 1/5 a$</td><td>Crack can't exceed the half of SP width.</td><td>$1/2 t < Z \leq 2 t$</td></tr></tbody></table>	<u>X</u>	<u>Y</u>	<u>Z</u>	$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$	$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	Minor
		<u>X</u>	<u>Y</u>	<u>Z</u>								
$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$										
$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$										
8.2 Protrusion over terminal: 8.2.1 Chip on electrode pad:  <table><thead><tr><th></th><th><u>X</u></th><th><u>Y</u></th><th><u>Z</u></th></tr></thead><tbody><tr><td><u>Front</u></td><td>$\leq a$</td><td>$\leq 1/2 W$</td><td>$\leq t$</td></tr><tr><td><u>Back</u></td><td>$\leq a$</td><td>$\leq W$</td><td>$\leq 1/2 t$</td></tr></tbody></table>		<u>X</u>	<u>Y</u>	<u>Z</u>	<u>Front</u>	$\leq a$	$\leq 1/2 W$	$\leq t$	<u>Back</u>	$\leq a$	$\leq W$	$\leq 1/2 t$
	<u>X</u>	<u>Y</u>	<u>Z</u>									
<u>Front</u>	$\leq a$	$\leq 1/2 W$	$\leq t$									
<u>Back</u>	$\leq a$	$\leq W$	$\leq 1/2 t$									

◆Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level												
08	The crack of glass	Symbols: X: The length of crack Y: The width of crack. Z: The thickness of crack W: terminal length t: The thickness of glass a: LCD side length 8.2.2 Non-conductive portion:  <table> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>$\leq 1/3 a$</td> <td>$\leq W$</td> <td>$\leq t$</td> </tr> </table> If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8.2.3 Glass remain:  <table> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>$\leq a$</td> <td>$\leq 1/3 W$</td> <td>$\leq t$</td> </tr> </table> 8.2.4 Cracking:  Not Allowed	X	Y	Z	$\leq 1/3 a$	$\leq W$	$\leq t$	X	Y	Z	$\leq a$	$\leq 1/3 W$	$\leq t$	Minor
		X	Y	Z											
$\leq 1/3 a$	$\leq W$	$\leq t$													
X	Y	Z													
$\leq a$	$\leq 1/3 W$	$\leq t$													

◆Specification For TFT-LCD Module 3.5" ~15" :
(Ver.B01)

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	<u>Level</u>
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General appearance	10. 1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10. 3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Minor

5. Precaution Relating Product Handling

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonic solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}\text{C}$ and 3 ~ 5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution! (LCM products with Capacitive Touch Panel)
Strong EMI-sources such as switch-mode power supplies (SPS) can lead to touch malfunction (e.g., ghost-touches). Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attached with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-side tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

[illegible]

Ver.001		Packaging Specifications	Approve	Check	Contact
Documents NO.	PH800480T030-ZFR		Bright	Clare	MJ

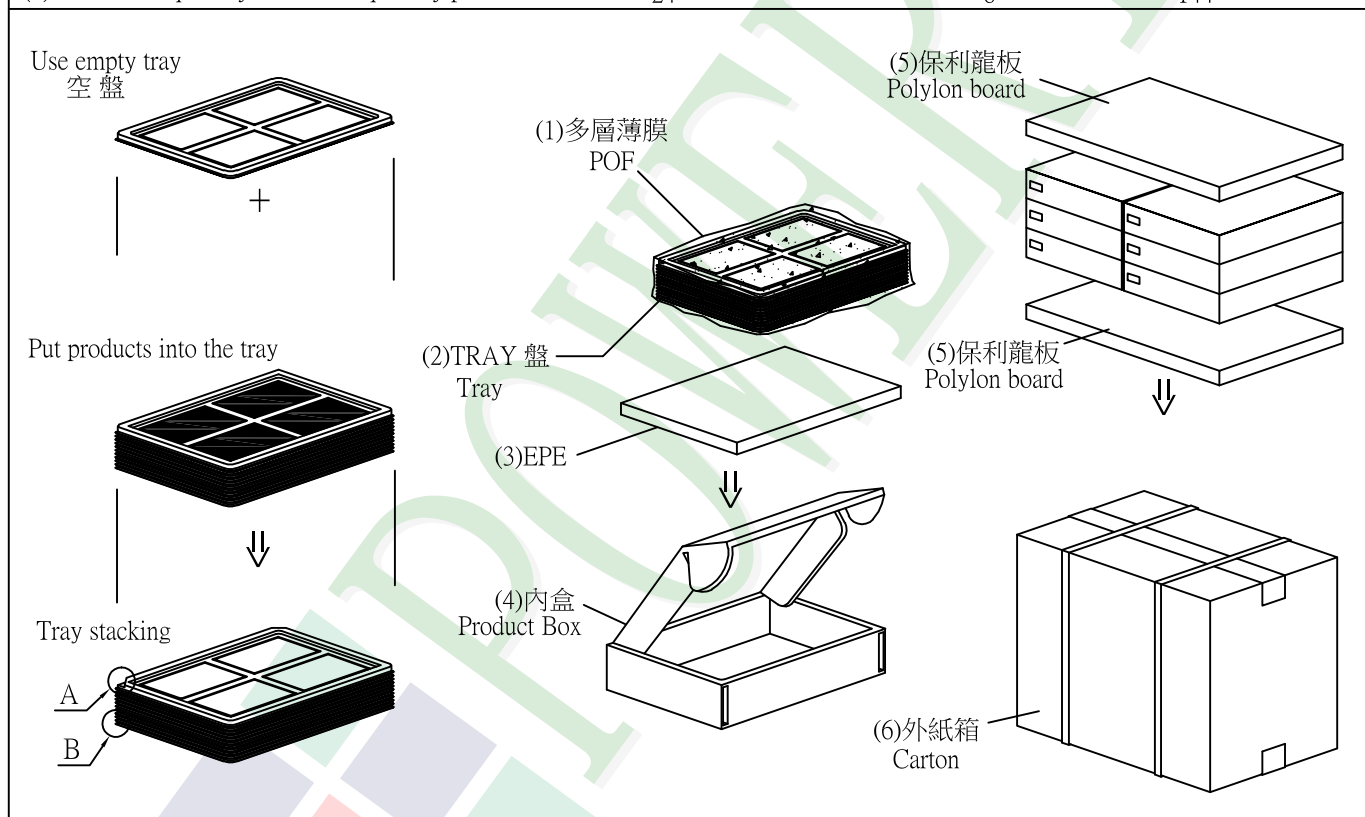
1.包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH800480T030-ZFR	137 X 96.5	0.0881	144	12.6864
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015	—	6	—
3	TRAY 盤 (2)Tray	TYSG000000187	352 X 260 X 12.8	0.0955	42	4.011
4	舒美墊(3) EPE	FOAM000000047	350 X 255 X 5	0.011	6	0.066
5	內盒(4)Product Box	BX36627063ABBA	383 X 270 X 66	0.182	6	1.092
6	保利龍板(5)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
7	外紙箱(6)Carton	BX57041027CCBA	570 X 410 X 265	1.0	1	1.0
8						
9						

2.一整箱總重量 (Total LCD Weight in carton) : 18.91 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

(1)LCM quantity per box : no per tray	4	x no of tray	6	=	24
(2)Total LCM quantity in carton : quantity per box	24	x no of boxes	6	=	144



特記事項 (REMARK)

4. TRAY盤相疊時,需旋轉180度,請詳見B視圖 Rotate tray 180 degrees and place on top of stack. Check the tray stack using Fig. B.	外購tray:TY00000000308 自製tray:TYSG000000187
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