SPECIFICATIONS

CUSTOMER .

SAMPLE CODE . SH192120T001-ZHC

MASS PRODUCTION CODE . PH192120T001-ZHC

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 004

DRAWING NO. (Ver.) . LMD- PH192120T001-ZHC (Ver.002)

PACKAGING NO. (Ver.) PKG- PH192120T001-ZHC (Ver.001)

Customer Approved

Date:

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□ Preliminary specification for design input

■ Specification for sample approval

POWERTIP 2024.04.03 TW RD APR

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History of Version

Date (mm / dd / yyyy)	<u>Ver.</u>	Edi.	<u>Description</u>	<u>Page</u>	Design by
03/24/2023	01	001	Preliminary.	-	lan
08/18/2023	01	002	First Sample	1	lan
09/13/2023	01	003	Modify Mechanical Specifications	4	lan
04/01/2024	01	004	Reduce VLED Current Reduce Backlight Current	5 9	lan



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1. SPECIFICATIONS

1.1 Features

<u>ltem</u>	<u>Standard Value</u>
Display Resolution	1920*3 (RGB) * 1200 Dots
LCD Type	Full Viewing Angle, Normally Black , Transmissive type
Screen size(inch)	10.1 inch
Color configuration	B.G.R. Vertical Stripe
Weight	464g
Interface	eDP1.4
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website:
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

<u>Item</u>	Standard Value	<u>Unit</u>
Outline Dimension	254.96 (W) * 173.6 (L) * 16.3 (H)	mm

LCD panel

<u>Item</u>	Standard Value	<u>Unit</u>
Active Area	216.576(W) * 135.36(L)	mm

Note: For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Module

<u>Item</u>	Symbol	<u>Condition</u>	Min.	Max.	<u>Unit</u>	Remark
Supply Voltage	VDD	GND=0V	-0.5	11.2	V	
Operating Temperature	Top (Ts)	Note 1	-20	+70	°C	
Storage Temperature	T _{ST} (Ta)	Note 2	-30	+85	°C	
Operating Humidity	H _D	Ta ≤ 40 °C	-	90	%	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = $25^{\circ}C$

<u>ltem</u>	<u>Symbol</u>	<u>Condition</u>	Min.	<u>Typ.</u>	Max.	<u>Unit</u>
Power Supply for TFT Panel	VDD	GND=0V	3.0	3.3	3.6	V
VDD Current	IDD	VDD=3.3V Red Pattern	-	650	980	mA
Power Supply for Backlight Unit	VLED	BL_GND =0V	2.7	12.0	24.0	V
VLED Current	ILED	VLED =12V	-	430	645	mA
PWM Signal Voltage	VIH		1.6	-	-	V
LED Enable Voltage	VIL	GND=0V	-	-	0.8	V
Input PWM Frequency	FPWM	GIND-0V	100		8k	Hz
PWM Duty Ratio	PWM		1	-	100	%



1.5 Optical Characteristics

Optical Specification

VDD=3.3V, Ta=25°C

<u>ltem</u>	Sym	<u>ıbol</u>	<u>Condition</u>	Min.	Typ.	Max.	<u>unit</u>	
Response time	Tr+	·Tf	θx=θy=0°	-	30	35	ms	Note 2
\rac{1}{2}	Тор	θΥ+		- <	85	-		
	Bottom	θΥ-	CD > 10	_	85	-	Dog	Note 4
Viewing angle	Left	θX-	CR ≥ 10		85	-	Deg.	Note 4
	Right	θX+		-	85	-		>
Contrast ratio	CI	R		800	1000	-		Note 3
	White	Х		0.26	0.31	0.36		
	vvriite	Υ	θx=θy=0°	0.31	0.36	0.41	_	Note1
	Red	Х		0.54	0.59	0.64		
Color of CIE		Υ		0.30	0.35	0.40		
Coordinate		X		0.30	0.35	0.40		
	Green	Y	VLED=12V	0.54	0.59	0.64		
	Blue	X	PWM="High"	0.08	0.13	0.18		
	Diue	Y	(Duty=100%)	0.07	0.12	0.17		
Average Brightness								
Pattern=white display	IV			700	850	-	cd/m ²	Note1
(With LCD \ TP)*1								
Uniformity	Δ	D		75	80		%	Note1
(With LCD \ TP)*2	Δ	D		75	OU	•	/0	NOLET





Note 1:

*1: △B=B(min) / B(max) * 100%

*2: Measurement Condition for Optical Characteristics:

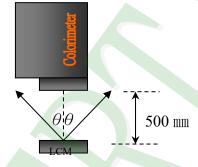
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency

b: Measurement Distance: 500 ± 50 mm, $(\theta = 0^{\circ})$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

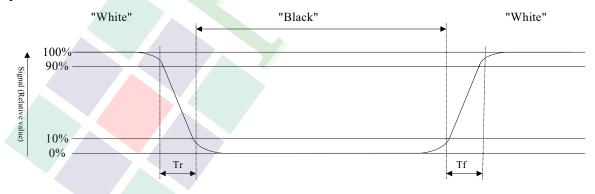
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note 2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

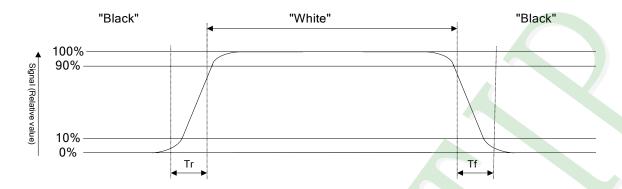
Refer to figure as below:

Normally White





Normally Black



Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

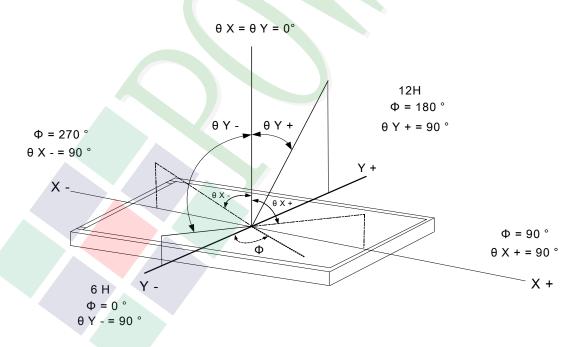
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note 4: Definition of viewing angle:

Refer to figure as below:



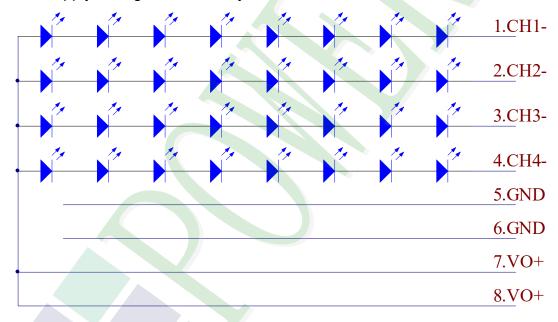


1.6 Backlight Unit Characteristics

Electrical / Optical Characteristics

<u>Item</u>	<u>Symbol</u>	Min.	<u>Typ.</u>	Max.	<u>Unit</u>	<u>Remark</u>
LED Voltage	Vf	22.4	24	26.8	V	Note1
LED Current	If	-	200		mA	
Average Brightness (without LCD) *1	IV	10600	14100	25600	-	cd/m ²
CIE Color Coordinate	X	0.25	0.28	0.31		
(Without LCD)	Y	0.25	0.28	0.31		

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and If=200 mA





1.7 Touch Panel Unit Characteristics

Features

<u>Item</u>	Standard Value
Touch Panel Size	10.1"
Touch type	Projective capacitive touch panel
Input Method	Finger
Support Operation	5 Points touch
Output Interface	I2C
IC	FT5726

Absolute Maximum Ratings

<u>ltem</u>	<u>Symbol</u>	Condition	Min.	Max.	<u>unit</u>
Operating Temperature	Тор		-20	+70	°C
Storage Temperature	Tst	-	-30	+80	°C

I²C Address

Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
0	1	1	1	0	0	0	R/W

R/W: 1: Read

0: write

DC Electrical Characteristics

<u>Item</u>	Symbol	Condition	Min.	<u>Typ.</u>	Max.	<u>unit</u>
Power Supply Voltage(I ² C)	VI2C	-	-	3.3	-	V

Optical Characteristics

<u>ltem</u>	<u>Standard Value</u>	<u>unit</u>
Total light transmittance	85% or more	-
Haze	3% or less	-



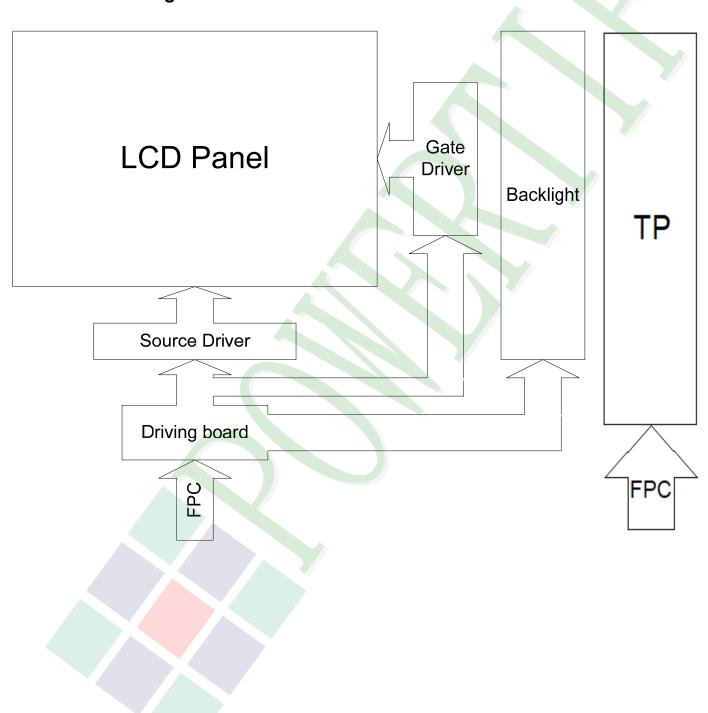
2. Module Structure

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

TFT LCM Interface

<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
1	NC	No connection
2	H-GND	Ground
3	LAN1_N	Main link Lane1 negative input (RXD1N)
4	LAN1_P	Main link Lane1 positive input (RXD1P)
5	H-GND	Ground
6	LAN0_N	Main link Lane0 negative input (RXD0N)
7	LAN0_P	Main link Lane0 positive input (RXD0P)
8	H-GND	High Speed Ground
9	AUX_P	True Signal Link _Auxiliry Channel
10	AUX_N	Complement Signal Link _Auxiliry Channel
11	H-Gnd	Ground
12	VDD	Power Supply, 3.3V (typ.)
13	VDD	Power Supply, 3.3V (typ.)
14	NC	No connection
15	H-GND	Ground
16	H-GND	Ground
17	HPD	HPD(Hot Plug Detect) Signal Pin
18	BL_GND	High Speed Ground
19	BL_GND	High Speed Ground
20	BL_GND	High Speed Ground
21	BL_GND	High Speed Ground
22	BL_EN	Backlight on/off Control pin
23	BL_PWM	Back light PWM Dimming
24	NC	No connection
25	NC	No connection
26	VLED	Backlight power
27	VLED	Backlight power
28	VLED	Backlight power
29	VLED	Backlight power
30	NC	No connection



TP Connector Interface

CN1 I²C

Pin No.	<u>Symbol</u>	<u>Function</u>
1	GND	Ground.
2	I ² C_SDA	I ² C Data
3	I ² C_SCL	I ² C Clock
4	I ² C_INT	Active Low
5	I ² C_RST	Active low global reset signal input.
6	VI2C	Power Supply Voltage (3.3V)

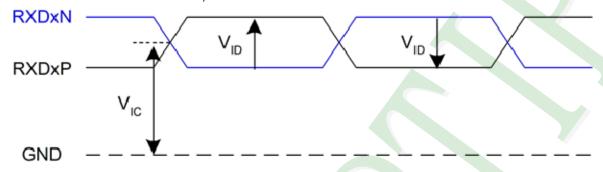


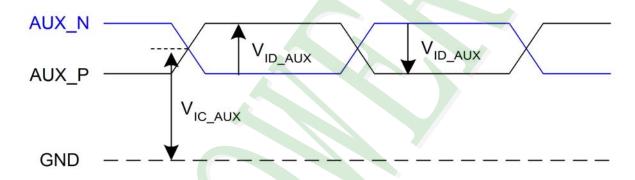


2.3 Timing Characteristics

2.3.1 Signal Electrical Characteristics

Input signals shall be low or High-impedance state when VDD is off. Signal electrical characteristics are as follows;





<u>Parameter</u>	<u>Symbol</u>	<u>Condition</u>	Min.	Max,	<u>Unit</u>
Main link common mode voltage	VIC	-	0	2.0	V
Main link swing voltage	VID	2.7Gbps	±100	±600	mV
Main link swing voltage		1.62Gbps	±100	±600	mV
AUX common mode voltage	VIC_AUX	-	0	2.0	V
AUX swing voltage	VID ALIV	Transmitting	±0.195	±0.69	V
AUX Swilly voltage	VID_AUX	Receiving	±0.16	±0.68	V



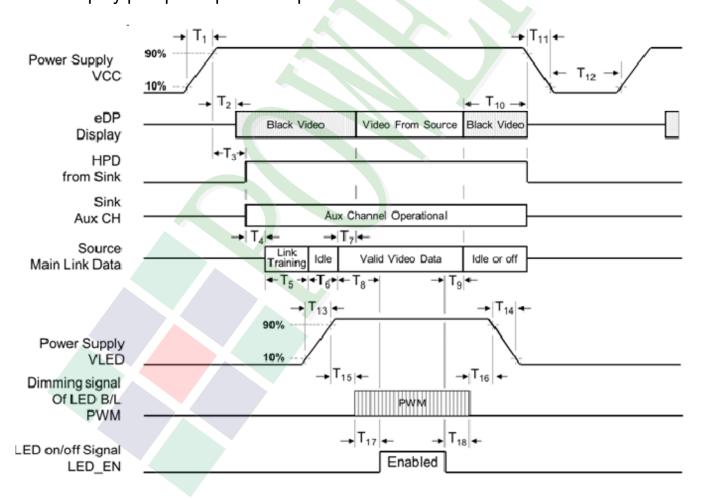
2.3.2 Interface Timings

DE mode

<u>ltem</u>	Min.	<u>Typ.</u>	Max.	<u>Unit</u>
Frame Rate	58	60	62	Hz
Frame Period	1230	1250	1270	line
Vertical Display Time		1200		line
Vertical Blanking Time	30	50	70	line
1 Line Scanning Time	2040	2060	2080	clock
Horizontal Display Time		1920		clock
Horizontal Blanking Time	120	140	160	clock
Clock Rate	153.21	154.5	155.57	MHz

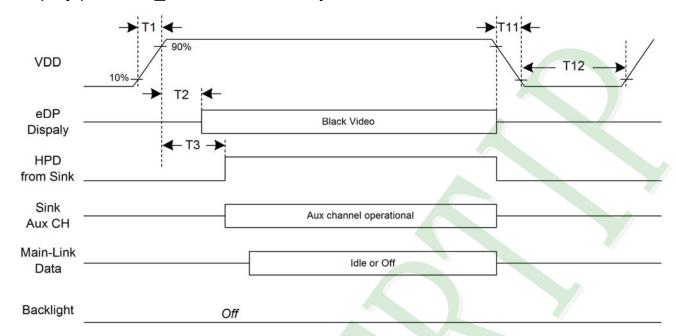
2.3.3 Power ON/OFF Sequence

Display port panel power sequence:





Display port AUX_CH transaction only:



<u>Parameter</u>	<u>Min</u>	<u>Max</u>	<u>Unit</u>	<u>Note</u>
T1	0.5	8	ms	
T2	0	200	ms	Note1
Т3	0	200	ms	
T4			1	
T5	-			
Т6				
T7	16	50	ms	
T8			-	
Т9				Note1
T10	32	500	ms	
T11		20	ms	
T12	700		ms	
T13	0.5	10	ms	
T14	0.5	10	ms	
T15	10		ms	
T16	10		ms	
T17	0		ms	
T18	0		ms	

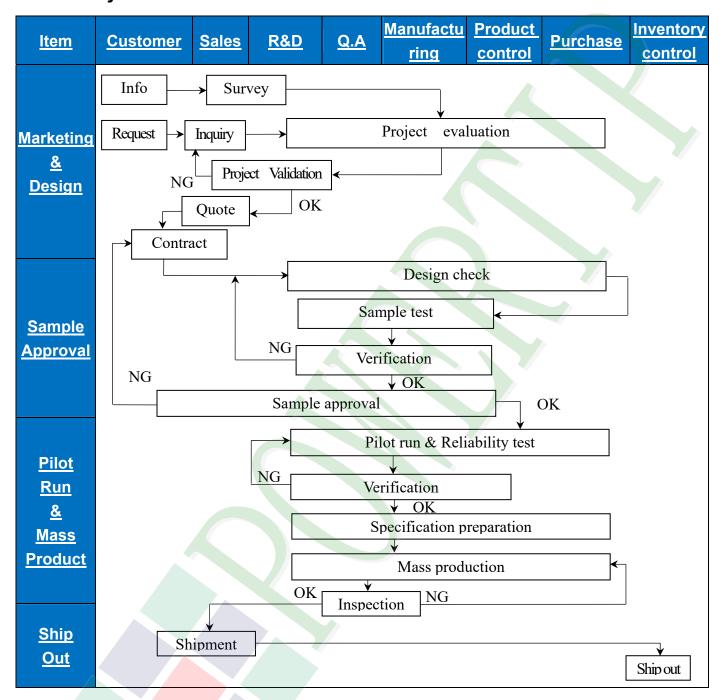


- **Note 1:** The Sink must include the ability to generate black video autonomously. The Sink mustautomatically enable black video under the following conditions:
 - Up on VDD power-on (within T2 max)
 - When the "No Video Stream_Flag" (VB-ID Bit 3) is received from the Source (at the end ofT9)
 - When no Main Link data, or invalid video data, is received from the Source. Black video must be displayed within 50ms (max) from the start of either condition. Video data can be deemed invalid
- **Note 2:** The Sink may implement the ability to disable the black video function, as described in Notes 1, above, for system development and debugging purposes.
- **Note 3:** The Sink must support Aux Channel polling by the Source immediately following VDD power-on without causing damage to the Sink device (the Source can re-try if the Sink is notready). The Sink must be able to respond to an Aux Channel transaction with the time specified within T3 max.

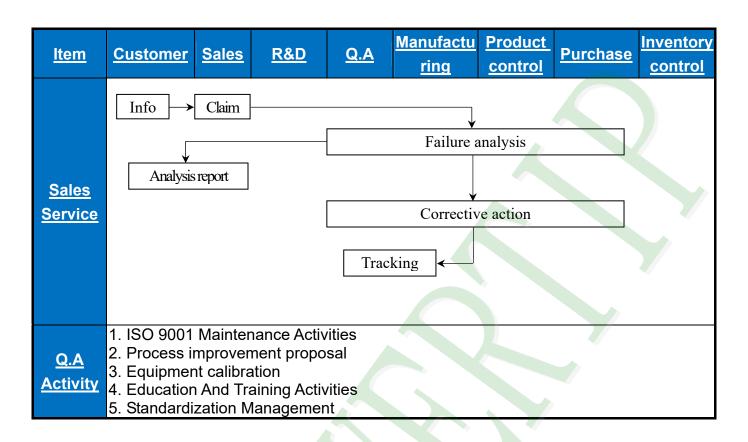


3. Quality Assurance System

3.1 Quality Assurance Flow Chart



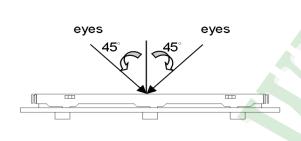


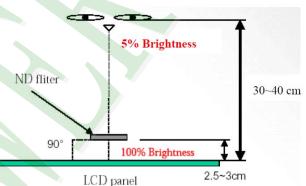




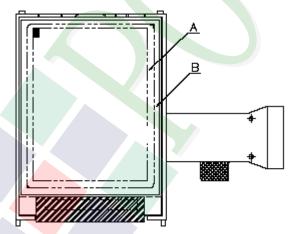
3.2. Inspection Specification

- ◆Scope: The document shall be applied to TFT-LCD Module for 3.5"-15" (Ver.B01).
- ♦Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- **◆**Equipment: Gauge, MIL-STD, Powertip Tester, Sample
- ◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5
- **♦**OUT Going Defect Level: Sampling.
- **♦**Standard of the product appearance test:
 - a. Manner of appearance test:
 - (1). The test best be under 20W×2 fluorescent light(about 300lux ∼500lux)
 - , and distance of view must be at 30~40 cm.
 - (2). The test direction is base on about around 45° of vertical line.





(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module 3.5"~15":

NO	Item	Criterion	Level	
		1.1The part number is inconsistent with work order of production.	Major	
01	Product condition	1.2 Mixed product types.	Major	
		1.3 Assembled in inverse direction.	Major	
02	Quantity	2.1The quantity is inconsistent with work order of production.	Major	
03	Outline dimension	3.1Product dimension and structure must conform to structure diagram.		
		4.1 Missing line character and icon.	Major	
		4.2 No function or no display.	Major	
	Flootwicel	4.3 Display malfunction.	Major	
04	Electrical Testing	4.4 LCD viewing angle defect.	Major	
	_	4.5 Current consumption exceeds product specifications.	Major	
		4.6Mura cannot be seen through 5% ND filter at 50% Gray , should be judged by the viewing angle of 90 degree.	Minor	
05	Dot defect (Bright dot, Dark dot) On -display	Stripht Dot Section Section	Minor	



◆Specification For TFT-LCD Module 3.5" ~15":

			LCD Module 3.5 ~15 :				(ver.Bu1)
<u>NO</u>	<u>ltem</u>			<u>Criterion</u>			<u>Level</u>
		6.1 Round type	e (Non-displa	y or display):			
		Dimension	on (diamet	er: Accepta	nce (Q'ty)		
		<u>Φ)</u>		<u>A area</u>	B area	<u>a</u>	
			$\Phi \leq 0.25$	Ignore			
	Black or white	0.25	$< \Phi \le 0.50$	5	lanor		
	Dot, scratch,		$\Phi > 0.50$	0	Ignor	e	
	contamination		Total	5			
	Round type	6.2 Line type(N	lon-display o	r display):			
	$ \rightarrow _{X} \leftarrow \downarrow$				Accept	tance	
	Y	module size	<u>Length</u>	Width (W)	(Q't		
06	→		<u>(L)</u>		A area	B area	Minor
	$\Phi = (x+y)/2$			W ≤ 0.03	Ignore	-	
				.03 <w≦0.05< td=""><td>4</td><td>-</td><td></td></w≦0.05<>	4	-	
	3.5	3.5" to less	L ≦5.0	0.05 <w≦0.10< td=""><td>2</td><td></td><td></td></w≦0.10<>	2		
	Line type	<u>9"</u>		W >0.10	As round	Ignore	
	↓			VV > 0.10	type		
	✓ Ť W			Total Total	5	-	
	→ı _L			W ≤ 0.05	Ignore		
	_		L ≦10.0 C	0.05 <w≦0.10< td=""><td>5</td><td>-</td><td></td></w≦0.10<>	5	-	
		9" to 15"		W > 0.40	As	Ignore	
				W >0.10	round type		
					5	-	
					<u> </u>	<u> </u>	
		Dimension (diameter: Φ)		ance (Q'ty		
				A area	Ba	<u>area</u>	
			$\Phi \leq 0.25$	Ignore			
	Polarizer		Φ ≤ 0.50	4			
07	Bubble		$\Phi \leq 0.80$	1	lgn	ore	Minor
			Φ >0.80	0			
		10	otal	5			



◆Specification For TFT-LCD Module 3.5" ~15":

<u>NO</u>	<u>ltem</u>	<u>Criterion</u>			
NO 08	Item The crack of glass	Symbols: X: The length of crack Z: The thickness of crack T: The thickness of glass 8.1 General glass chip: 8.1.1 Chip on panel surface and crack between panels: X: The width of crack. W: terminal length a: LCD side length X: The width of crack. W: terminal length a: LCD side length R.1.1 Chip on panel surface and crack between panels:	Level		
		Seal width $\underline{\underline{X}}$ $\underline{\underline{Y}}$ $\underline{\underline{Z}}$			
		≤ a Crack can't enter viewing area ≤1/2 t			
		≤a Crack can't exceed the half of SP width. 1/2 t < Z ≤2 t			



♦Specification For TFT-LCD Module 3.5" ~15":

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	Level
		X: The length of crack Z: The thickness of crack t: The thickness of glass 8.1.2 Corner crack:	
		$\frac{X}{2}$ $\frac{Y}{2}$ $\frac{Z}{2}$ ≤ 1/2 t	
		viewing area $= 1/2 t$ $\leq 1/5 \text{ a} \text{Crack can't exceed the half of SP width.} 1/2 t < Z \leq 2 t$	
08	The crack of glass	8.2 Protrusion over terminal:	Minor
		8.2.1 Chip on electrode pad:	
		W X	
		$\begin{array}{c cccc} \underline{X} & \underline{Y} & \underline{Z} \\ \hline \underline{Front} & \leq a & \leq 1/2 W & \leq t \\ \hline \underline{Back} & \leq a & \leq W & \leq 1/2 t \end{array}$	



◆Specification For TFT-LCD Module 3.5″ ~15″:

NO	ltem	Criterion			
08	The crack of glass	Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8.2.2 Non-conductive portion: X Y Z Z ≤ 1/3 a ≤ W ≤ t of the lTO must remain and be inspected according to electrode terminal specifications 8.2.3 Glass remain: X Y Z Z ≤ 1/3 W ≤ t Not Allowed	Minor		



◆Specification For TFT-LCD Module 3.5"~15":

NO	<u>Item</u>	Criterion	
09	Backlight elements	9.1 Backlight can't work normally.	<u>Level</u> Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General	10.1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
		10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Minor



4. Reliability Test

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION (TOTILE OF)		
1	High Temperature Storage Test	Keep in 85 ±5℃ 240 hrs		
2	Low Temperature Storage Test	Keep in -30 ±5°C 240 hrs		
3	High Temperature / High Humidity Storage Test	Keep in 60 ℃ / 90% R.H duration for 240 hrs (Excluding the polarizer)		
4	Temperature Cycling Storage Test	-30°C → +25°C → 85°C → +25°C (30mins) (5mins) (30mins) (5mins) 20 Cycle		
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15°C ~35°C 2. Humidity relative: 30%~60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)		
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration: 1.5 mm Each direction (X, Y, Z) duration for 2 hrs 		
7	Drop Test (Packaged)	Packing Weight (Ko 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454 Drop Direction : %1 corner / 3 e	122 76 61 46 edges / 6 sides each 1time	

©Result Evaluation Criteria:

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function.

(Normal operation state) Temperature: +20~30°C **Humidity**: 50~70%

Atmospheric pressure: 86~106Kpa



5. Precaution Relating Product Handling

5.1 Safety

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 Handling

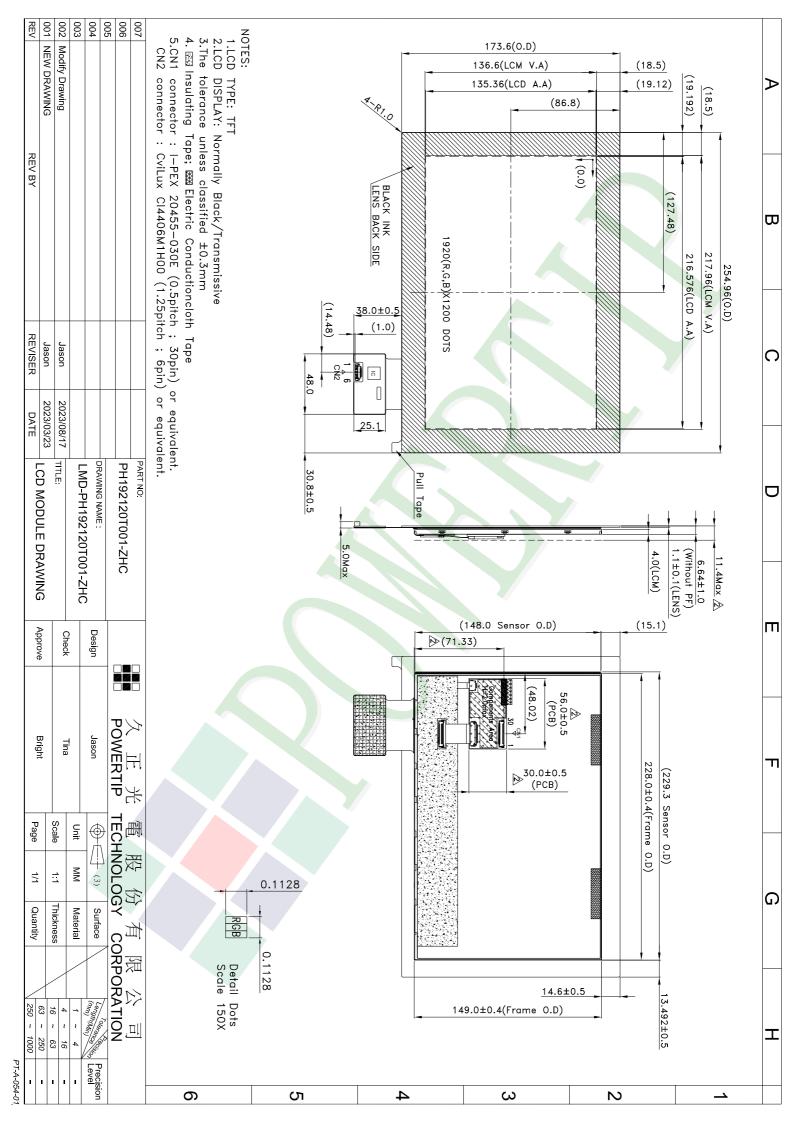
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution! (LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
 Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 Caution: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attached with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-side tape for the attachment operation

5.3 Storage

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 Terms of Warranty

- 5.4.1 Applicable warrant period
 - The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Approve Check Design Ver.001 Packaging Specifications Bright Tina Jason Documents NO. PKG-PH192120T001-ZHC 1.包裝材料規格表 (Packaging Material): (per carton) No. Model Dimensions (mm) 1Pcs Weight Quantity Total Weight PH192120T001-ZHC 1 成品 (LCM) 254.96 X 173.0 X 11.4 0.464 20 9.28 2 多層薄膜(1)POF OTFILM0BA03ABA 3 TYSG000000706 352 X 260 X 20.8 TRAY 盤 (2)Tray 2.4 0.1 24 4 内盒(3)Product Box 393 X 274 X 107 4 BX00000000022 0.2611.044 5 保利龍板(4)Polylon board OTPLB00000008 550 X 393 X 15 0.022 2 0.044 6 外紙箱(5)Carton 570 X 410 X 265 BX57041027CCBA 1.39 1.39 7 舒美墊(6)EPE FOAM00000047 350 X 255 X 5 0.011 0.044 8 9 10 2.一 整箱總重量 (Total LCD Weight in carton): 14.20 Kg±10% 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)LCD quantity per box : no per tray x no of tray 5 5 (2)Total LCD quantity in carton: quantity per box 5 x no of boxes 20 4 Use empty tray (4)保利龍板 空盤 Polylon board (1)多層薄膜 POF Put products and EPE into the tray (2)TRAY 盤 LCM (4)保利龍板 Tray Polylon board ₩ (5)外紙箱 ∜ (6) 舒美墊 Carton **EPE** (3)內盒 Tray stacking Product Box 特 記 事 項 (REMARK) 斜角 Detail B Tray 2 圓角 Tray 1 4. TRAY盤相疊時,需旋轉180度,請詳見B視圖 Rotate tray 180 degrees and place on top of stack.

Check the tray stack using Fig. B.