



Recommended PCB Layout

Tolerance: ±0.05mm

Solder Area [ ] Component Outline

Pin	Signal	Mating Sequence	Pin	Signal	Mating Sequence
A1	GND	First	B12	GND	First
A2	SSTXp1	Second	B11	SSRXp1	Second
А3	SSTXn1	Second	B10	SSRXn1	Second
A4	VBUS	First	В9	VBUS	First
A5	CC1	Second	В8	SBU2	Second
A6	Dp1	Second	В7	Dn2	Second
A7	Dn1	Second	В6	Dp2	Second
A8	SBU1	Second	B5	CC2	Second
A9	VBUS	First	B4	VBUS	First
A10	SSRXn2	Second	В3	SSTXn2	Second
A11	SSRXp2	Second	B2	SSTXp2	Second
A12	GND	First	B1	GND	First
SHELL		GND		SHELL	GND

Part Num	ber		Product Description					
USB4200			USB3.2 Gen2, Type C Receptacle, Dual Row SMT,					
Drawing Date			Flag Type, PCB Top Mount, 24 Pin					
2nd March 2023			riag Type, FOB Top Mount, 24 Fill					
Ву	KY	Tolerances (Except as Noted)		Units:		This drawing is confidential and	l	
Detail	Drawing	Length	Angle	Metric (mm)	RoHS	copyright of Global Connector Technology, Ltd (GCT).	l	
	Release	X.X ± 0.30 X.XX ± 0.20	-	•=	COMPLIANT	This drawing must not be copied	⊢	
Revision	A1				2011/65/EU	or disclosed without written		



Not to Scale CC Sheet N CC 2/3

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