SPECIFICATIONS

CUSTOMER .

SAMPLE CODE . SH128800T007-ZHA

MASS PRODUCTION CODE . PH128800T007-ZHA

SAMPLE VERSION . 02

SPECIFICATIONS EDITION . 006

DRAWING NO. (Ver.) . LMD-PH128800T007-ZHA (Ver.004)

PACKAGING NO. (Ver.) . PKG-PH128800T007-ZHA (Ver.002)

Customer Approved

Date:

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□ Preliminary specification for design input

■ Specification for sample approval

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History of Version

<u>Date</u> (mm / dd / yyyy)	<u>Ver.</u>	Edi.	<u>Description</u>	<u>Page</u>	Design by
03/27/2023	01	001	Preliminary.	-	lan
05/24/2023	01	002	First Sample Modify Dimension and Add Conductive tape	- Appendix	lan
08/04/2023	01	003	Modify temperature range	5 25	lan
10/02/2023	01	004	Modify Viewing angle Modify Backlight Modify Timing Characteristics Modify Packaging Specifications Modify Drawing	6 9 13 · 14 Appendix Appendix	lan
12/22/2023	02	O05 Second Sample Modify Backlight Unit Power Consumption			lan
03/22/2024	02	006	Modify Outline Dimension Modify backlight power consumption Modify Backlight Add 4-M2.0*0.4 and Modify Drawing	4 5 9 Appendix	lan



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1. SPECIFICATIONS

1.1 Features

<u>Item</u>	<u>Standard Value</u>
Display Resolution	1280 *3 (RGB) * 800 Dots
LCD Type	Full Viewing Angle , Normally Black, Transmissive type
Screen size(inch)	10.1 inch
Color configuration	R.G.B. Vertical Stripe
Weight	289g
Interface	LVDS
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website:
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

<u>ltem</u>	Standard Value	<u>Unit</u>
Outline Dimension	229.8 (W) * 149.0 (L) * 8.0 max. (H)	mm

LCD panel

<u>Item</u>	Standard Value	<u>Unit</u>
View Area	217.96 (W) * 136.6 (L)	mm
Active Area	216.96 (W) * 135.6 (L)	mm

Note: For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Module

<u>ltem</u>	<u>Symbol</u>	Condition	Min.	Max.	<u>Unit</u>	Remark
Logic Supply Voltage	V_{DD}	GND=0V	-0.3	+4.0	V	
Logic Input Signal Voltage	V _{signal}	GND=0V	-0.3	+4.0	V	
Power Supply for Backlight Unit	LED_Vcc	LED_GND=0V	-0.3	+18.0	V	->
Operating Temperature	Top (Ts)	Note 1	-30	+80	°C	
Storage Temperature	T _{ST} (Ta)	Note 2	-30	+80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

GND = 0V, Ta = 25°C

<u>ltem</u>	Symbol	Condition	Min.	Typ.	Max.	<u>Unit</u>
Logic Supply Voltage	V_{DD}	GND=0V	3.0	3.3	3.6	V
Logic Current	loo		-		0.31	Α
Logic Power Consumption	PV _{DD}	V _{DD} =3.3V	-	ı	1	W
Power Supply for Backlight Unit	LED_Vcc	LED_GND=0V	9	12.0	18.0	V
Backlight Unit Power Consumption	PLED_Vcc	LED_Vcc =12V	-	6	9	W
PWM Signal Voltage	VIH		1.6	-	-	V
LED Enable Voltage	VIL		-	-	0.8	V
Input PWM Frequency	FPWM	GND=0V	100	1	8k	Hz
PWM Duty Ratio	PWM		1	-	100	%



1.5 Optical Characteristics

VDD=3.3V, Ta=25°C

<u>ltem</u>	<u>Symbol</u>		Condition	Min.	Typ.	Max.	unit			
D ('	Tr		Tr		Ta = 25°C	-	10	20		N. (O
Response time	-	Γf	θX, θY = 0°	-	15	30	ms	Note 2		
	Тор	θΥ+		-	80	-				
Viewing angle	Bottom	θΥ-	CR ≥ 10	_	80	1	Deg.	Note 4		
viewing angle	Left	θΧ-	CR 2 10	6	80		Deg.	Note 4		
	Right	θΧ+		V	80	ı		>		
Contrast ratio	1	CR		800	1000	•		Note 3		
	White	Х		0.27	0.32	0.37				
	vvriite	Υ		0.30	0.35	0.40	-	Note1		
	Red	Х	Ta = 25°C θX , θY = 0°	0.54	0.59	0.64				
Color of CIE	Neu	Υ		0.3	0.35	0.4				
Coordinate	Green	X		0.29	0.34	0.39				
	Green	Y		0.54	0.59	0.64				
	X	X		0.08	0.13	0.18				
	Blue	Y		0.07	0.12	0.17	1			
Average Brightness Pattern=white display (With LCD)*2			LED_Vcc =12.0V PWM="High" (Duty=100%)	850	1000	-	cd/m ²	Note1		
Uniformity (With LCD)*1	ΔВ		LED_V _{CC} =12.0V PWM="High" (Duty=100%)	70	-	-	%	Note1		



Note 1:

*1: △B=B(min) / B(max) * 100%

*2: Measurement Condition for Optical Characteristics:

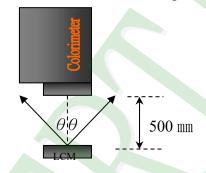
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency

b: Measurement Distance: 500 ± 50 mm, $(\theta = 0^{\circ})$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

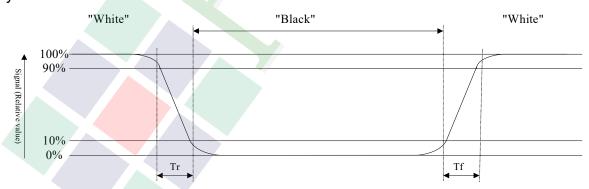
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note 2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

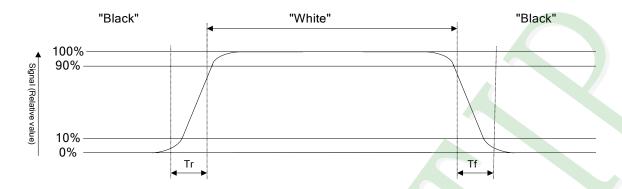
Refer to figure as below:

Normally White





Normally Black



Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

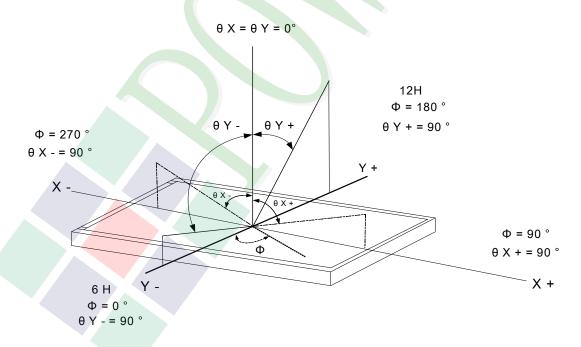
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note 4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Characteristics

Maximum Ratings

<u>ltem</u>	<u>Symbol</u>	Min.	Max.	<u>Unit</u>	<u>Remark</u>
LED Forward Current	l _F	-	600	mA	
LED Reverse Voltage	VR	-	13.2	V	
Power Dissipation	PD		20460	mW	

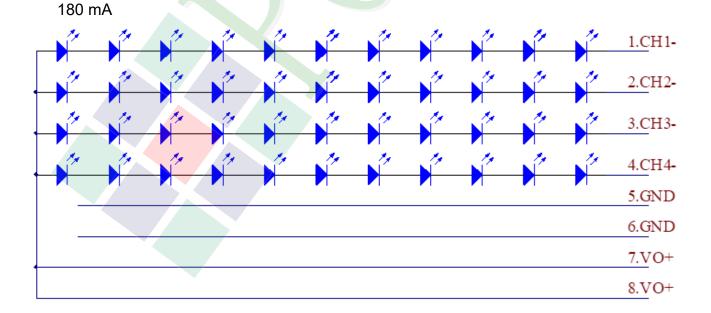
Electrical / Optical Characteristics

<u>Item</u>	<u>Symbol</u>	Min.	Typ.	Max.	<u>Unit</u>	<u>Remark</u>
LED Voltage	Vf	29.7	31.9	34.1	V	Note1
LED Current	If	-	180	1	mA	-
Average Brightness (without LCD) *1	IV	17000	19000	28000	-	cd/m ²
CIE Color Coordinate	X	0.25	0.28	0.335		
(Without LCD)	Y	0.25	0.28	0.335		
LED life time	-	50,000	1	-	Hr	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and If=180 mA

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at

Ta=25℃ and If=180 mA. The LED life time could be decreased if operating I_L is larger than





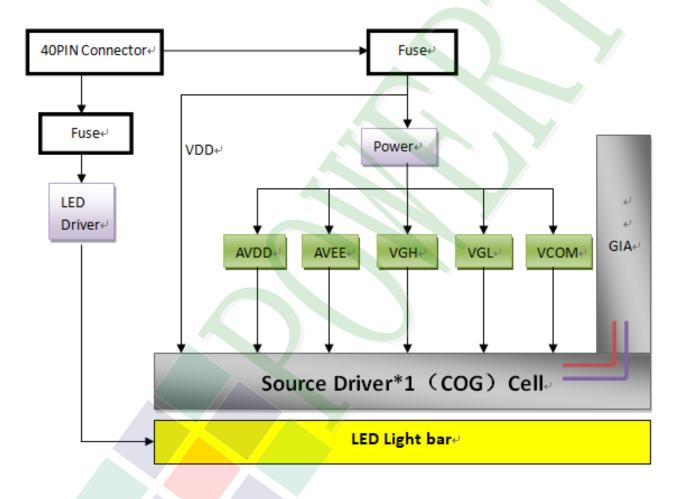
2. Module Structure

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

TFT LCM Interface

Pin#	<u>Name</u>	<u>Description</u>
1	NC	No Connection.
2	VDD	Power Supply.
3	VDD	Power Supply.
4	NC	No Connection.
5	NC	No Connection.
6	NC	No Connection.
7	NC	No Connection.
8	LV0N	-LVDS Differential Data Input.
9	LV0P	+LVDS Differential Data Input.
10	GND	Power ground.
11	LV1N	-LVDS Differential Data Input.
12	LV1P	+LVDS Differential Data Input.
13	GND	Power ground.
14	LV2N	-LVDS Differential Data Input.
15	LV2P	+LVDS Differential Data Input.
16	GND	Power ground.
17	LVCLKN	-LVDS Differential Clock Input.
18	LVCLKP	+LVDS Differential Clock Input.
19	GND	Power ground.
20	LV3N	-LVDS Differential Data Input.
21	LV3P	+LVDS Differential Data Input.
22	GND	Power ground.
23	LED_GND	Ground for LED Driving.
24	LED_GND	Ground for LED Driving.
25	LED_GND	Ground for LED Driving.
26	NC	No Connection.
27	LED_PWM	PWM Input Signal for Backlight Diver.
28	LED_EN	Backlight Enable Pin.
29	NC	No Connection.



Pin#	<u>Name</u>	<u>Description</u>
30	NC	No Connection.
31	LED_VCC	Power Supply for Backlight Diver.
32	LED_VCC	Power Supply for Backlight Diver.
33	LED_VCC	Power Supply for Backlight Diver.
34	NC	No Connection.
35	BIST	Self Test Enable. When it is not used, please don't connect to GND, connecting to Normal High(3.3V) is recommended
36	NC	No Connection.
37	NC	No Connection.
38	NC	No Connection.
39	NC	No Connection.
40	NC	No Connection.



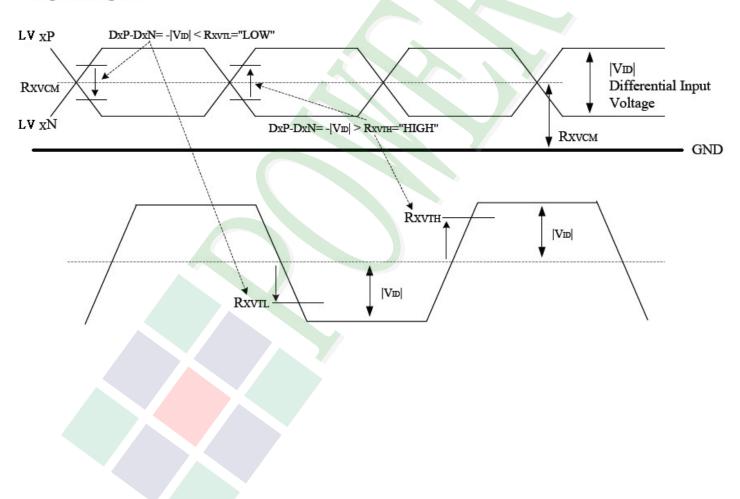
2.3 Timing Characteristics

2.3.1 Signal Electrical Characteristics For LVDS Receiver

The built-in LVDS receiver is compatible with (ANSI/TIA/TIA-644) standard.

<u>Parameter</u>	<u>Symbol</u>	Min.	Typ.	Max.	<u>Unit</u>	Conditions	
Differential Input High Threshold voltage	R_{xVTH}	1	-	100	mV	7	
Differential Input Low Threshold voltage	R _{xVTL}	-100	-	-	mV	$R_{xVCOM} = +1.2V$	
Differential input common mode voltage	R _{xVCOM}	0.7		1.6	V	_	
Differential voltage	V _{ID}	200	-	600	mV	-	

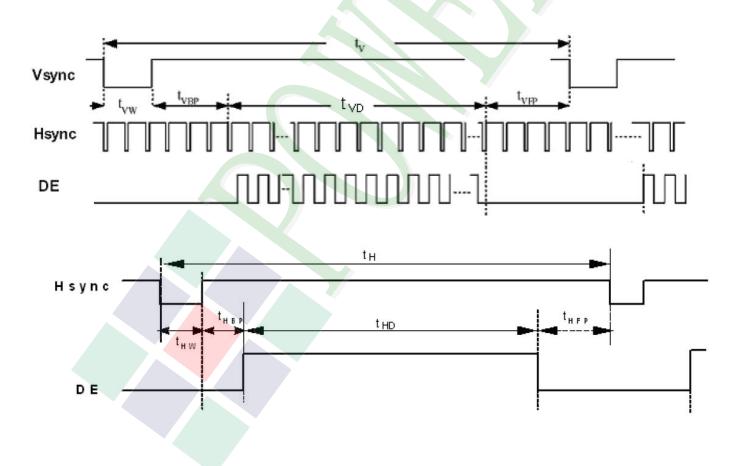
Single-end Signals





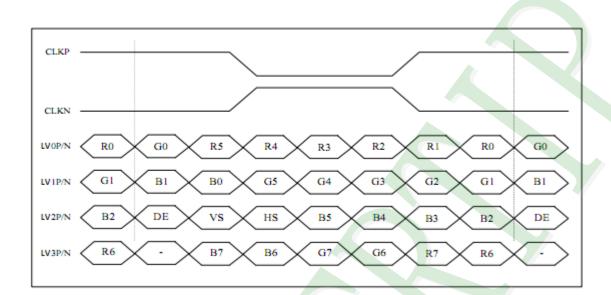
2.3.2 Input Timing

<u>Parameter</u>	<u>Symbol</u>	Min.	Typ.	Max.	<u>Unit</u>
Clock Frequency	1/Tc	66.3	72.4	78.9	MHz
Horizontal display area	tHD		1280		Тс
HSYNC pulse width	tHPW	2	4	40	Тс
HSYNC back porch(with pulse width)	tHBP	88	88	88	Tc
HSYNC front porch	tHFP	12	72	132	Тс
Vertical display area	tVD		800		tH
VSYNC pulse width	tVPW	2	-	20	tH
VSYNC back porch(with pulse width)	tVBP	23	23	23	tH
VSYNC front porch	tVFP	1	15	49	tH





2.3.3 LVDS Data Input Format

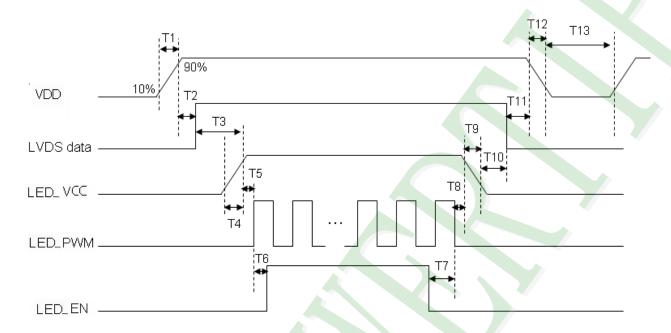






2.3.4 Power ON/OFF Sequence

- 1. Interface signals are also shown in the chart. Signals from any system shall be Hi-resistance state or low level when VDD voltage is off.
- 2. Please set timing according to the following figures, otherwise it may cause image sticking

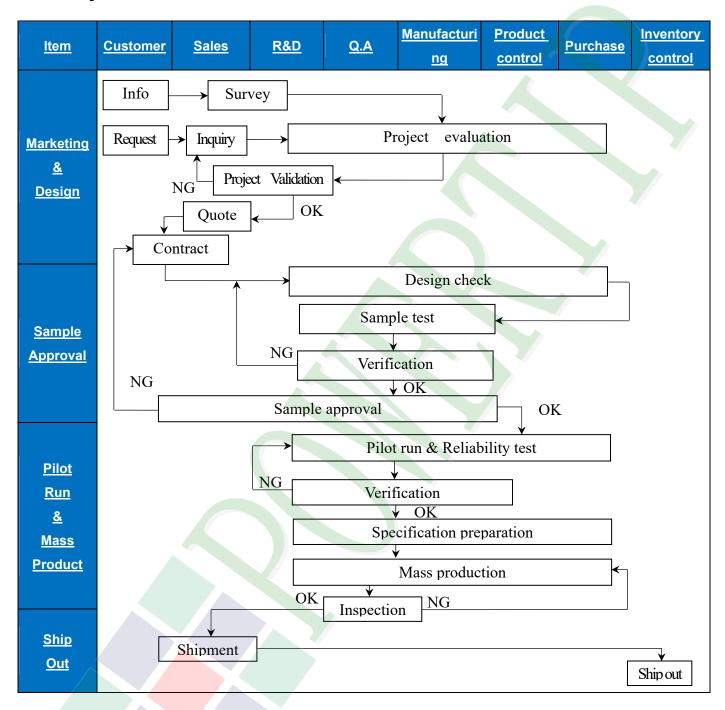


<u>Parameter</u>	Symbol	<u>Unit</u>	<u>Min</u>	Typ.	<u>Max</u>
VDD Rise Time (10% to 90%)	T1	ms	0.5		10
VDD Good to Signal Valid	T2	ms	30		90
Signal Valid to Backlight On	Т3	ms	200		
Backlight Power On Time	T4	ms	0.5		
Backlight LED_VCC Good to System PWM On	T5	ms	10		
System PWM On to Backlight LED_EN On	Т6	ms	10		
Backlight LED_ EN Off to System PWM Off	Т7	ms	0		
System PWM Off to B/L Power Disable	Т8	ms	10		
Backlight Power Off Time	Т9	ms	0.5	10	30
Backlight Off to Signal Disable	T10	ms	200		
Signal Disable to Power Down	T11	ms	0		50
VDD Fall Time	T12	ms	0.5	10	30
Power Off	T13	ms	500		

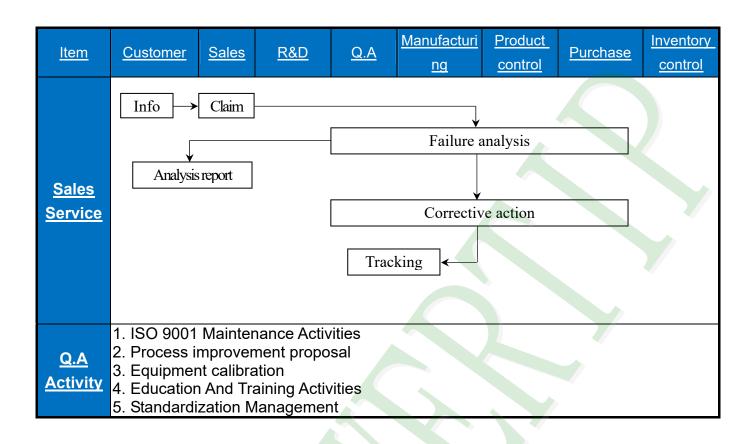


3. Quality Assurance System

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5"-15" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

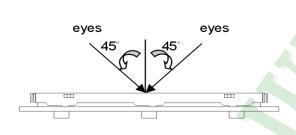
◆OUT Going Defect Level: Sampling

◆Standard of the product appearance test:

a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light(about 300lux \sim 500lux) and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.



5% Brightness

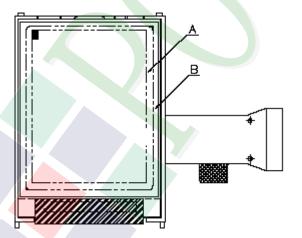
ND fliter

30~40 cm

LCD panel

2.5~3cm

(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module 3.5"~15":

NO	<u>ltem</u>	<u>Criterion</u>	<u>Level</u>		
		The part number is inconsistent with work order of production.	Major		
01	Product condition	1.2 Mixed product types.	Major		
		1.3 Assembled in inverse direction.	Major		
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major		
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major		
		4.1 Missing line character and icon.	Major		
		4.2 No function or no display.	Major		
		4.3 Display malfunction.	Major		
04	Electrical Testing	4.4 LCD viewing angle defect.			
		4.5 Current consumption exceeds product specifications.			
		4.6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.	Minor		
		<u>Item</u> <u>Acceptance (Q'ty)</u>			
		Bright Dot ≤ 4			
		Dot Dark Dot ≤ 5 Defect Joint Dot ≤ 3			
	Dot defect	Joint Dot ≤ 3			
		Total ≤ 7			
05	(Bright dot, Dark dot) On -display	 5.1 Inspection pattern: full white, full black, Red, Green and blue screens. 5.2 It is defined as dot defect if defect area > 1/2 dot. 5.3 The distance between two dot defect ≥5 mm. 5.4 Bright dot: Dots appear bright and unchanged in visible with 5% ND filter is defined. 5.5 Tiny bright dot: bright dot area ≤1/2 dot. a. Dots appear bright and unchanged in visible with 5% ND filter is defined defect and is judged in accordance with 6.1 b. Dots invisible with 5% ND Filter is Ignored 	Minor		



◆Specification For TFT-LCD Module 3.5″ ~15″:

<u>Item</u>		<u>Criterion</u>					Level
	6.1 Round type (N	6.1 Round type (Non-display or display):					
Black or white Dot, scratch, contamination	Dimension 0.25 <	$ \begin{array}{c} $	er: Φ) 25 50	,	B are	ea	
			r displa	y):	Accontar	200 (O'ty)	
Y	module size	<u>Length</u> (L)			A area	B area	Minor
$\Phi = (x+y)/2$		L ≦ 10.0			4		IVIII 101
1	3.5" to less 9"	L ≦5.0 			As round	Ignore	
Line type			Total		type 5		
✓ [¥] W	4		W	≦ 0.05	Ignore		
→ L +	0" to 45"	L ≦ 10.0	0.05 <	⟨W ≤ 0.10	5	lanoro	
	9" to 15"		W	>0.10	As round type	ignore	
			Total		5		
	Dimension (c	diameter:	Ф)				
Polarizer	Ф	0.25		Ignore		<u> </u>	
Bubble				4			Minor
				1	lgı	nore	
	Black or white Dot, scratch, contamination Round type $X \stackrel{\longleftarrow}{=} X$ $\Phi = (x+y)/2$ Line type L Polarizer	Black or white Dot, scratch, contamination Round type	Black or white Dot, scratch, contamination Round type $\Phi = 0.0.25 < \Phi \le 0.0.025 < \Phi \le 0.025 <$	Black or white Dot, scratch, contamination Round type (Non-display or display or displa	Black or white Dot, scratch, contamination Round type $(x + y)/2$ Line type Line type Line type $(x + y)/2$ L	Black or white Dot, scratch, contamination Round type	Black or white Dot, scratch, contamination Round type $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$



◆Specification For TFT-LCD Module 3.5″ ~15″

<u>NO</u>	<u>ltem</u>	<u>Criterion</u>	Level
NO 08	The crack of glass	Symbols: X: The length of crack Z: The thickness of crack W: te	ne width of crack. erminal length CD side length
		X Y Crack can't enter	<u>Z</u>
		≥ a viewing area	≦1/2 t
		≦a Crack can't exceed the half of SP width.	1/2 t < Z ≦2 t



◆Specification For TFT-LCD Module 3.5″ ~15″:

<u>NO</u>	<u>ltem</u>	<u>Criterion</u>	Level						
		X: The length of crack Z: The thickness of crack t: The thickness of glass 8.1.2 Corner crack:							
		X Y Z							
		≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t							
		\leq 1/5 a Crack can't exceed the half of SP width. 1/2 t $<$ Z \leq 2 t							
08	The crack of glass	8.2 Protrusion over terminal:	Minor						
		8.2.1 Chip on electrode pad: $X $							



◆Specification For TFT-LCD Module 3.5″ ~15″:

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	Level
	Item The crack of glass		Level



◆Specification For TFT-LCD Module 3.5″ ~15″

<u>NO</u>	<u>ltem</u>	<u>Criterion</u>	Level
		9.1 Backlight can't work normally.	Major
09	Backlight elements	9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
	10 General appearance	10.1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
		10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
10		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
4		10.6 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤ 1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

4.	Reliability lest Col	T T T T T T T T T T T T T T T T T T T	(vei.bui)				
<u>NO.</u>	TEST ITEM	TEST CO	ONDITION				
1	High Temperature Storage Test	Keep in 80 ±5°C 240 hrs	Keep in 80 ±5℃ 240 hrs				
2	Low Temperature Storage Test	Keep in -30 ±5℃ 240 hrs					
3	High Temperature / High Humidity Storage Test	Keep in 60 $^{\circ}$ / 90% R.H duration for 240 hrs (Excluding the polarizer)					
4	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$					
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15°C ~35°C 2. Humidity relative: 30% ~60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)					
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz freque The amplitude of vibration: Each direction (X, Y, Z) dur 	1.5 mm				
7	Drop Test (Packaged)	Packing Weight (Kd 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454 Drop Direction : %1 corner / 3 e	122 76 61 46 edges / 6 sides each 1time				

©Result Evaluation Criteria:

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function.

(Normal operation state) Temperature: +20~30°C

Humidity : 50~70%

Atmospheric pressure: 86~106Kpa



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

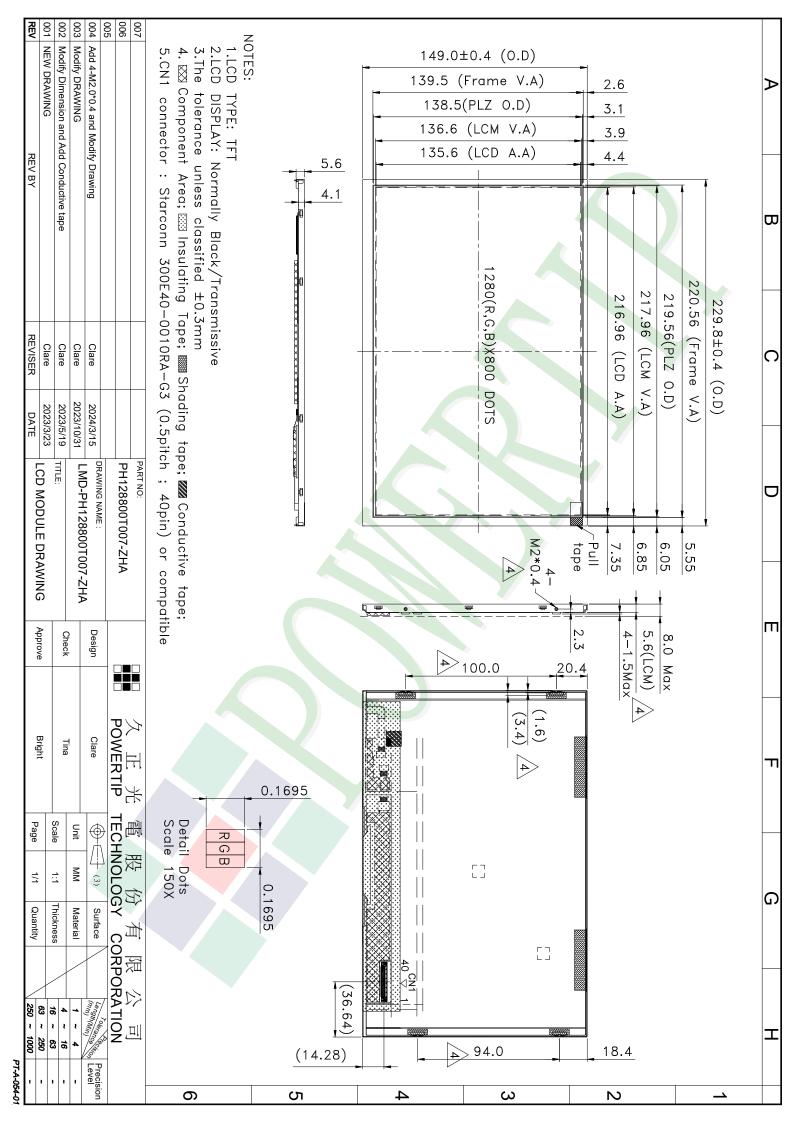
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
 Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Ver.002			Approve	Check	Contact
Documents NO. PKG-PH128800T007-	Bright	Tina	Clare		
1.包裝材料規格表(Packaging	Material) : (per cart	ton) 🖄			
No. Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1 成品 (LCD)	PH128800T007-ZHA	229.8 X 149.0	0.2889	18	5.2002
2 多層薄膜(1)POF	OTFILMOBAO3ABA			3	
3 TRAY 盤 (2)Tray	TYSG000000687	517 X 377 X 17.3	0.19	2 12	2.28
4 内盒(3)Product Box	BX0000000071	558 X 393 X 68	0.6	3	1.8
5 保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
6 外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.0	1	1.0
7 EPE(6)EPE	OTFOAMEP0003BA	333 X 218 X 10	0.022	3	0.066
8 EPE(7)EPE	FOAM00000047 /2	350 X 255 X 5	0.011	6	0.066
9					
2.一 整箱總重量 (Total LCD Wei	ght in carton): 10.	47 Kg±10%	<u>^</u>		
3.單箱數量規格表(Packaging Spe	cifications and Quanti	ty):			
(1)LCD quantity per box : no (2)Total LCD quantity in cart			of tray 3 of boxes 3	= =	6 18
Use empty tray)保利龍板 lylon board		
空盤	(1)多層薄膜——			\times	
	POF				
	(2)TRAY 盤—				
+	Tray				
			(6)EPE		
Put products into the tray				_	
	(6)EPE		7		
	(7)EPE /2		(4)保利龍板	<u> </u>	
	(7)ELE <u>72</u>	7	Polylon boa		
		11,			
		V			
	T	41 / //	外紙箱		
	(3)內盒、		arton		
Tray stacking	Product Box				
A					
<u>B</u>					
	特記	事項(REMA	RK)		
	2.OTFOAMEP0003BA	, ,			
A 斜角 Detail	B 尺寸 333.0 X 54.				
	2.OTFOAMEP0003BA				
	Size: 333.0 X 54	. 5mm			
T	ray 2 333.0				
圓角 T	ray 1	 			
1	1	0.			
1. TRAY盤相疊時,需旋轉180度,請詳見B		218.			
Rotate tray 180 degrees and place	ou 2/2				
top of stack. Check the tray stack using Fig. B.					