

DATA SHEET

THYRISTOR SURGE SUPPRESSORS MODEMS/LINE CARD

P2300SD

RoHS compliant & Halogen free





Thyristor Surge Suppressors (TSS) Data Sheet

Description

DO-214AA Thyristor solid state protection thyristor protect telecommunications equipment such as modems, line cards, fax machines, and other CPE.



It is used to enable equipment to meet various regulatory requirements including GR 1089, ITU K.20, K.21 and K.45, IEC 60950, UL 60950, and TIA-968(formerly known as FCC Part 68).

Features

Compared to surge suppression using other technologies, This device offer absolute surge protection regardless of the surge current available and the rate of applied voltage (dv/dt).

- Cannot be damaged by voltage
- Eliminate hysteresis and heat dissipation typically found with clamping devices
- Eliminate voltage overshoot caused by fast-rising transients
- Are non-degenerative
- Will not fatigue
- Have low capacitance, making them ideal for high-speed transmission equipment
- Meets MSL level 1, per J-STD-020

Electrical Parameters

Parameter	Definition
V _{DRM}	Peak Off-state Voltage – maximum voltage that can be applied while maintaining off state
Vs	Switching Voltage – maximum voltage prior to switching to on state
V _T	On-state Voltage – maximum voltage measured at rated on-state current
I _{DRM}	Leakage Current – maximum peak off-state current measured at V _{DRM}
Is	Switching Current – maximum current required to switch to on state
lτ	On-state Current – maximum rated continuous on-state current
I _H	Holding Current – typical current required to maintain on state
Co	Off-state Capacitance – typical capacitance measured in off state
I _{PP}	Peak Pulse Current – maximum rated peak impulse current



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Electrical Characteristics

Part Number	V _{DRM} (V)	Vs (V)	V _T (V)	I _{DRM} (µA)	I _S (mA)	I _T (A)	I _H (mA)	C _O (pF)	I _{PP} 10/1000μs (A)	V _{PP} 10/700µs (V)	Marking Code
P2300SD	190	260	4	5	800	2.2	50	100	200	8000	P23D

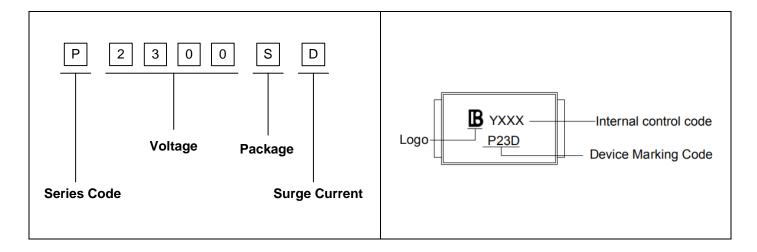
Notes:

- All measurements are made at an ambient temperature of 25 $^{\circ}$ C. IPP applies to -40 $^{\circ}$ C through +85 $^{\circ}$ C temperature range.
- Off-state capacitance(Co) is measured at 1 MHz with a 2V bias and is typical value.

Thermal Considerations

Package DO-214AA/SMB	Symbol	Parameter	Value	Unit
	TJ	Operating Junction Temperature	-40 to +125	°C
	Ts	Storage Temperature Range	-40 to +150	$^{\circ}$
	$R_{\theta JA}$	Junction to Ambient on printed circuit	90	°C/W

Part Number Code and Marking



Characteristics Curves

Figure 1. V-I Characteristics

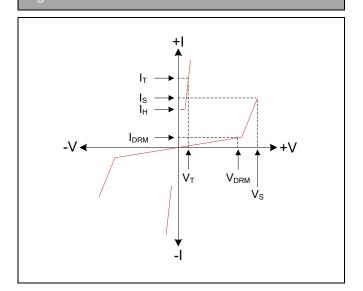


Figure 3. Normalized Vs Change versus Junction Temperature

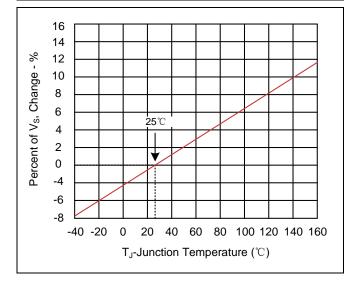


Figure 2. tr x td Pulse Wave-form

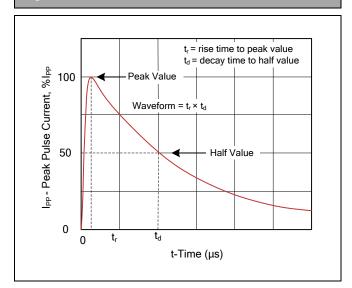
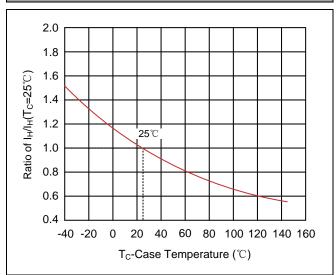
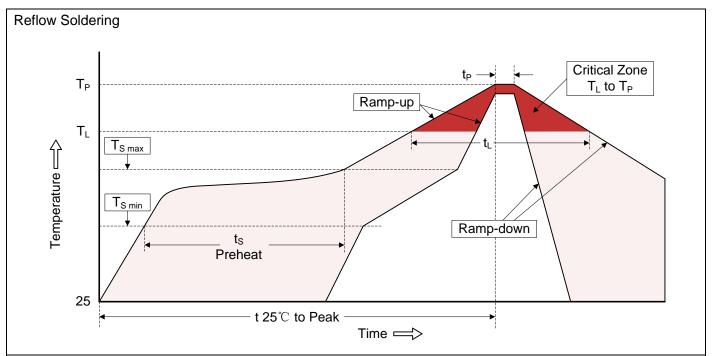


Figure 4. Normalized DC Holding Current versus Case Temperature



Recommended Soldering Conditions

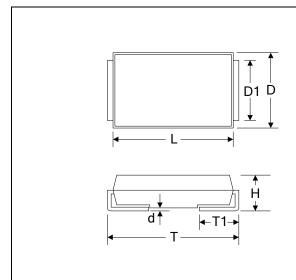


Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T _L to T _P)	3°C/second max.
Preheat -Temperature Min (T _{S min}) -Temperature Max (T _{S max}) -Time (min to max) (t _S)	150°C 200°C 60-180 seconds
T _{S max} to T _L -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature (T _L) -Time (t _L)	217°C 60-150 seconds
Peak Temperature (T _P)	260°C
Time within 5°C of actual Peak Temperature (t _P)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

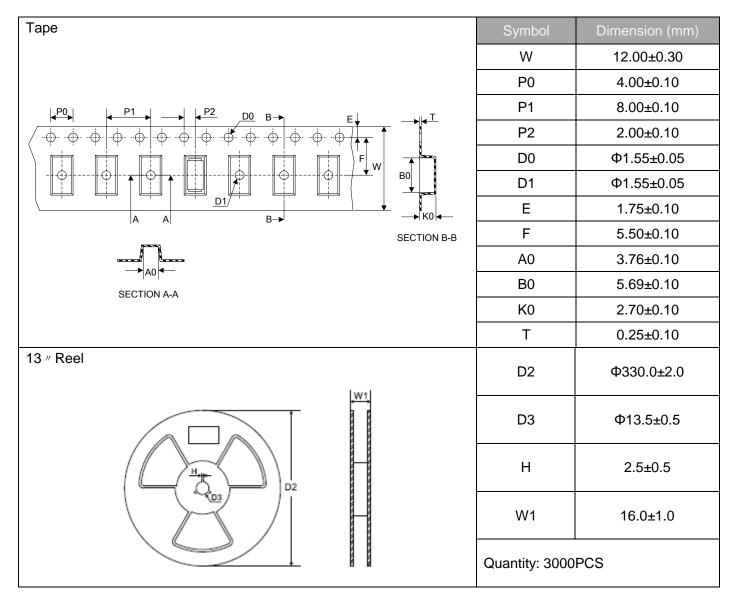
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Dimensions (SMB/DO-214AA)



Symbol	Millim	neters	Inches		
	Min.	Max.	Min.	Max.	
L	4.06	4.70	0.160	0.185	
D	3.30	3.94	0.130	0.155	
D1	1.90	2.20	0.075	0.086	
Т	5.21	5.59	0.205	0.220	
T1	0.76	1.52	0.030	0.060	
d	-	0.203	-	0.008	
Н	1.95	2.65	0.077	0.104	

Packaging





Circuit Protection Components

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