

Innovative Service Around the Globe

DATA SHEET THYRISTOR SURGE SUPPRESSORS MODEMS/LINE CARD PXXXXTA series

RoHS compliant & Halogen free



THYRISTOR SURGE SUPPRESSORS PXXXXTA

Thyristor Surge Suppressors (TSS) Data Sheet

Description

DO-214AC Thyristor solid state protection thyristor protect telecommunications equipment such as modems, line cards, fax machines, and other CPE.

P Series devices are used to enable equipment to meet various regulatory requirements including GR 1089, ITU K.20, K.21 and K.45, IEC 60950, UL 60950, and TIA-968 (formerly known as FCC Part 68).

Features

Compared to surge suppression using other technologies, P Series devices offer absolute surge protection regardless of the surge current available and the rate of applied voltage (dv/dt). P Series devices:

- Cannot be damaged by voltage
- Eliminate hysteresis and heat dissipation typically found with clamping devices
- Eliminate voltage overshoot caused by fast-rising transients
- Are non-degenerative
- Will not fatigue
- Have low capacitance, making them ideal for high-speed transmission equipment
- Meets MSL level 1, per J-STD-020
- Safety certification: UL: E244458

Electrical Parameters

Parameter	Definition						
V _{DRM}	Peak Off-state Voltage – maximum voltage that can be applied while maintaining off state						
Vs	Switching Voltage – maximum voltage prior to switching to on state						
VT	On-state Voltage – maximum voltage measured at rated on-state current						
IDRM	Leakage Current – maximum peak off-state current measured at VDRM						
ls	Switching Current – maximum current required to switch to on state						
IT	On-state Current – maximum rated continuous on-state current						
Iн	Holding Current –typical current required to maintain on state						
Co	Off-state Capacitance – typical capacitance measured in off state						
I _{PP}	Peak Pulse Current – maximum rated peak impulse current						
I _{TSM}	Peak One-cycle Surge Current – maximum rated one-cycle AC current						
di/dt	Rate of Rise of Current – maximum rated value of the acceptable rate of rise in current over time						



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YAGEO Circuit Protection
THYRISTOR SURGE SUPPRESSORS PXXXXTA

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Electrical Characteristics

Part Number	V _{DRM} (V)	Vs (V)	V _T (V)	I _{DRM} (μΑ)	I _S (mA)	I _T (A)	I _H (mA)	C _O (pF)	Marking
P0080TA	6	25	4	5	800	2.2	50	50	P008A
P0300TA	25	40	4	5	800	2.2	50	70	P03A
P0640TA	58	77	4	5	800	2.2	150	50	P06A
P0720TA	65	88	4	5	800	2.2	150	50	P07A
P0900TA	75	98	4	5	800	2.2	150	45	P09A
P1100TA	90	130	4	5	800	2.2	150	45	P11A
P1300TA	120	160	4	5	800	2.2	150	45	P13A
P1500TA	140	180	4	5	800	2.2	150	40	P15A
P1800TA	170	220	4	5	800	2.2	150	40	P18A
P2300TA	190	260	4	5	800	2.2	150	35	P23A
P2600TA	220	300	4	5	800	2.2	150	35	P26A
P3100TA	275	350	4	5	800	2.2	150	30	P31A
P3500TA	320	400	4	5	800	2.2	150	30	P35A

Notes:

• All measurements are made at an ambient temperature of 25 °C. IPP applies to -40 °C through +85 °C temperature range.

• Off-state capacitance(Co) is measured at 1 MHz with a 2V bias and is typical value.

• For surge ratings, see table below.

Surge Ratings

Series	Ι _{ΡΡ} 2×10μs (A)	l _{₽₽} 8×20µs (A)	I _{РР} 10×160µs (А)	I _{РР} 10×560µs (А)	I _{РР} 10×1000µs (А)	І _{тѕм} 60Нz (А)	di/dt (A/µs)
A	150	150	90	50	45	20	500

Thermal Considerations

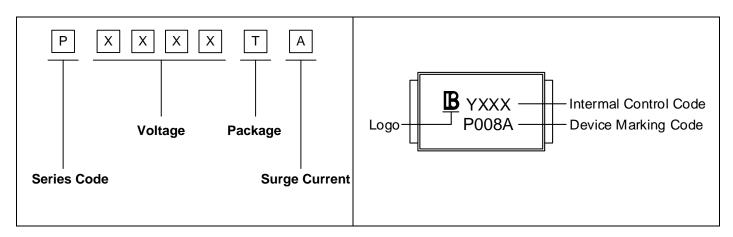
Package DO-214AC/SMA	Symbol	Parameter	Value	Unit
	TJ	Operating Junction Temperature	-40 to +125	°C
	Ts	Storage Temperature Range	-40 to +150	°C
	$R_{ extsf{ heta}JA}$	Junction to Ambient on printed circuit	120	°C /W

THYRISTOR SURGE SUPPRESSORS PXXXTA

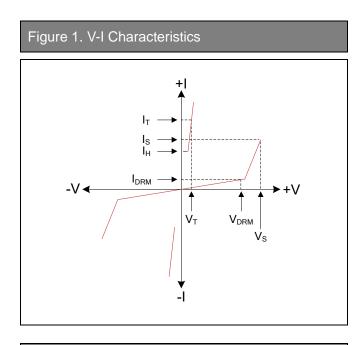
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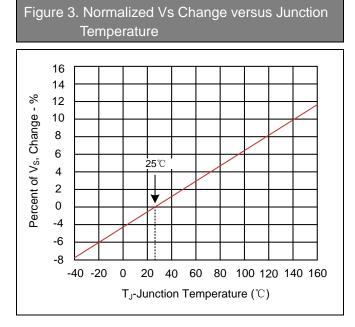
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Part Number Code and Marking



Characteristics Curves





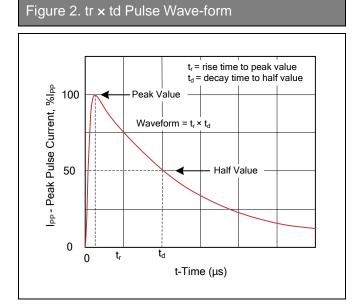
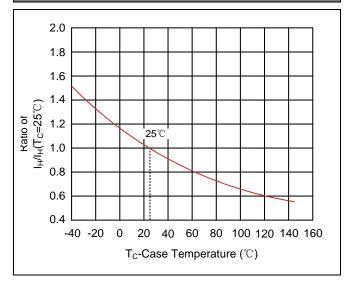


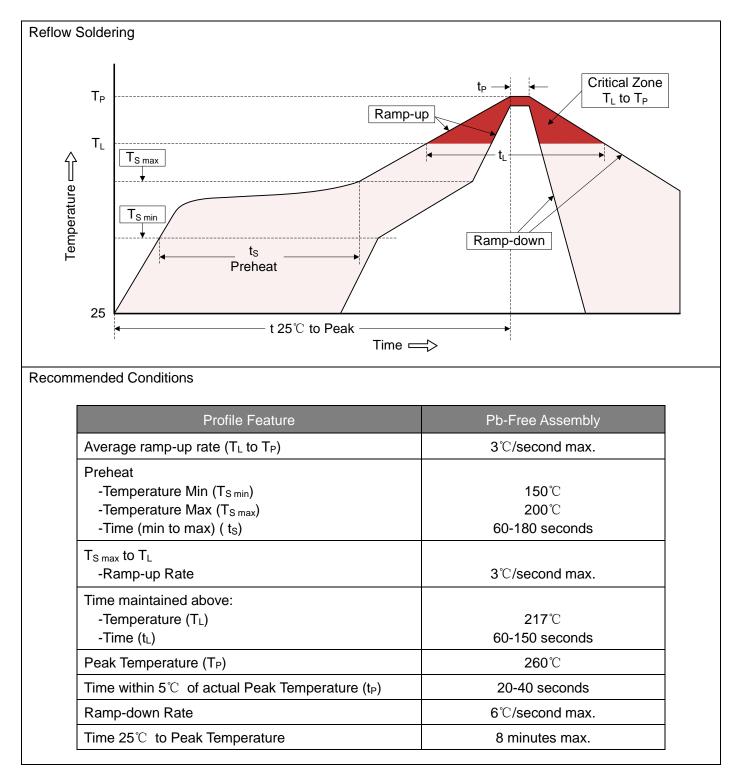
Figure 4. Normalized DC Holding Current versus Case Temperature

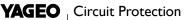


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Recommended Soldering Conditions

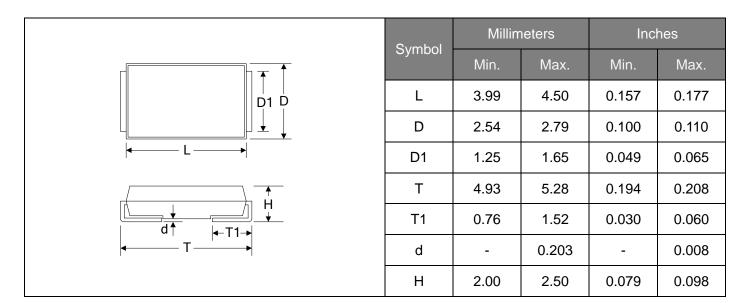




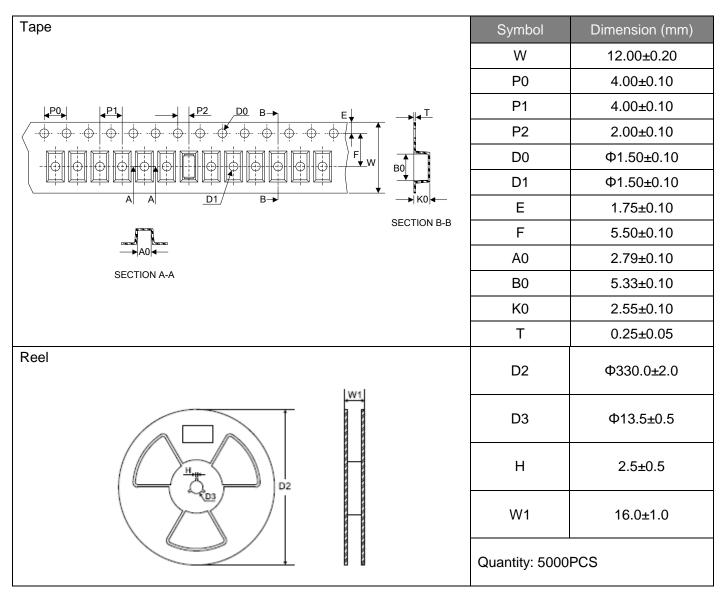
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Dimensions (SMA/DO-214AC)



Packaging



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