#### **SPECIFICATIONS**

CUSTOMER .

SAMPLE CODE . SH128800T007-ZHA02

MASS PRODUCTION CODE . PH128800T007-ZHA02

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 002

DRAWING NO. (Ver.) . LMD-PH128800T007-ZHA02 (Ver.003)

PACKAGING NO. (Ver.) PKG-PH128800T007-ZHA02 (Ver.001)

# **Customer Approved**

Date:

Approved	Checked	Designer
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- □ Preliminary specification for design input
- Specification for sample approval

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# **History of Version**

Date (mm / dd / yyyy)	<u>Ver.</u>	Edi.	<u>Description</u>	<u>Page</u>	Design by
09/22/2023	01	001	Preliminary.	-	Yuan
03/29/2024	01	002	First Sample Modify Drawing ADD SCREW HOLES	- Appendix Appendix	Yuan



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### 1. SPECIFICATIONS

### 1.1 Features

<u>ltem</u>	Standard Value			
Display Resolution	1280 *3 (RGB) * 800 Dots			
LCD Type	Full Viewing Angle , Normally Black, Transmissive type			
Screen size(inch)	10.1 inch			
Color configuration	R.G.B. Vertical Stripe			
Weight	253g			
Interface	HDMI			
	THIS PRODUCT CONFORMS THE ROHS OF PTC			
ROHS	Detail information please refer website:			
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1			

# 1.2 Mechanical Specifications

<u>Item</u>	Standard Value	<u>Unit</u>
Outline Dimension	229.8 (W) * 149.0 (L) * 23.5max. (H)	mm

### LCD panel

<u>Item</u>	Standard Value	<u>Unit</u>
View Area	217.96 (W) * 136.6 (L)	mm
Active Area	216.96 (W) * 135.6 (L)	mm

Note: For detailed information please refer to LCM drawing.



### 1.3 Absolute Maximum Ratings

#### Module

<u>ltem</u>	<u>Symbol</u>	Condition	Min.	Max.	Unit	Remark
Power Supply Voltage	VCC	-	-0.3	+18.0	٧	
Logic Voltage	BL_PWM	-	-0.3	+5.5	V	_
Operating Temperature	Top (Ts)	Note 1	-30	+80	°C	
Storage Temperature	T <sub>ST</sub> (Ta)	Note 2	-40	+85	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

### 1.4 DC Electrical Characteristics

GND = 0V, Ta = 25°C

<u>Item</u>	Symbol	<u>Status</u>	Condition	Min.	<u>Typ.</u>	Max.	<u>Unit</u>
Power Supply Voltage	VCC		VCC-GND	11.5	12.0	12.5	V
Power Supply Current	ICC	I	VCC=12.0V	-	0.65	1.0	Α
Logic Voltage	BL_PWM	-	BL_EN=5.0V	0	-	5.0	V
PWM Frequency	FPWM		-	100	-	20K	HZ
PWM Duty Ratio	PWM	- 1	-	5	-	100	%

Note: Maximum current from RGB full-display



# 1.5 Optical Characteristics

VDD=3.3V, Ta=25°C

<u>ltem</u>	<u>Symbol</u>		Condition	Min.	Typ.	Max.	<u>unit</u>	
Response time	Tr	+Tf	Ta = 25°C θX, θY = 0°	-	25	50	ms	Note 2
	Тор	θΥ+		- <	85	-		
Viowing angle	Bottom	θΥ-	CR ≥ 10		85	-	Dog	Note 4
Viewing angle	Left	θX-	CR 2 10		85	-	Deg.	Note 4
	Right	θX+		-	85	-		>
Contrast ratio		CR		600	800	-		Note 3
	White	Х		0.27	0.32	0.37		
	vvriite	Υ		0.30	0.35	0.40		
	Red	Х	Ta = 25°C θX , θY = 0°	0.54	0.59	0.64		Note1
Color of CIE	rtcu	Υ		0.30	0.35	0.40		
Coordinate	Croon	ordinate Green		0.29	0.34	0.39		NOLCI
	Giccii	Υ		0.54	0.59	0.64		
	Blue	Χ		0.08	0.13	0.18		
	Dide	Υ		0.07	0.12	0.17		
Average Brightness Pattern=white display (With LCD)*2	IF		LED_Vcc =12.0V PWM="High" (Duty=100%)	850	1000	-	cd/m <sup>2</sup>	Note1
Uniformity (With LCD)*1	ΔΒ		LED_Vcc =12.0V PWM="High" (Duty=100%)	70	-	-	%	Note1



#### Note 1:

\*1: △B=B(min) / B(max) \* 100%

\*2: Measurement Condition for Optical Characteristics:

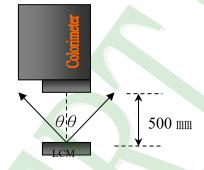
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency

b: Measurement Distance:  $500 \pm 50$  mm,  $(\theta = 0^{\circ})$ 

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

#### Note 2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

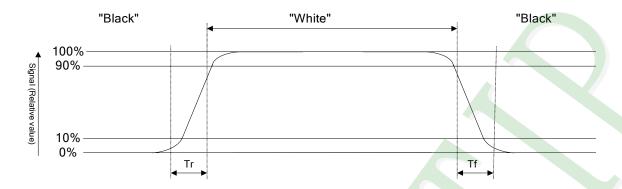
Refer to figure as below:

#### Normally White





#### Normally Black



#### Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

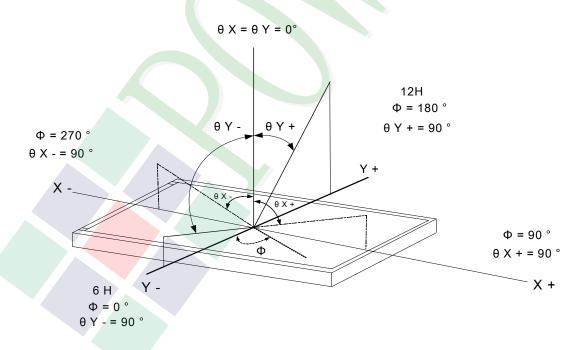
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

### Note 4: Definition of viewing angle:

Refer to figure as below:





### 1.6 Backlight Unit Characteristics

### **Maximum Ratings**

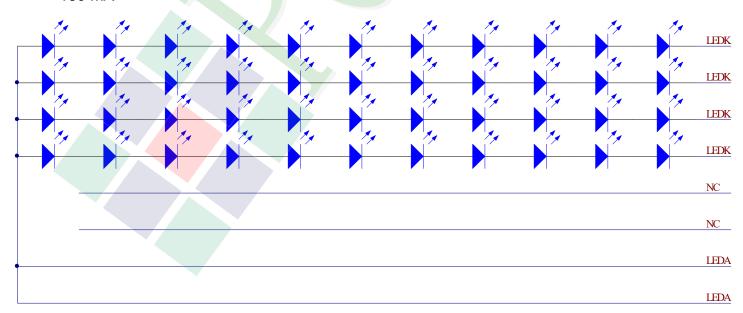
<u>ltem</u>	<u>Symbol</u>	<u>Min.</u>	Max.	<u>Unit</u>	<u>Remark</u>	
LED Reverse Current	I <sub>R</sub>	-	20	uA	Dor I CD	
LED Reverse Voltage	VR	-	5	٧	Per LED	

#### **Electrical / Optical Characteristics**

<u>Item</u>	<u>Symbol</u>	Min.	<u>Тур.</u>	Max.	<u>Unit</u>	<u>Remark</u>
LED Voltage	Vf	29.7	31.9	34.1	V	Note1
LED Current	If	-	180	1	mA	-
Average Brightness (without LCD) *1	IV	18000	23000	28000	-	cd/m²
CIE Color Coordinate	X	0.255	0.28	0.335		
(Without LCD)	Y	0.255	0.28	0.335		
LED life time		50,000	-	-	Hr	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and If=180 mA

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and If=180 mA. The LED life time could be decreased if operating I<sub>L</sub> is larger than 180 mA





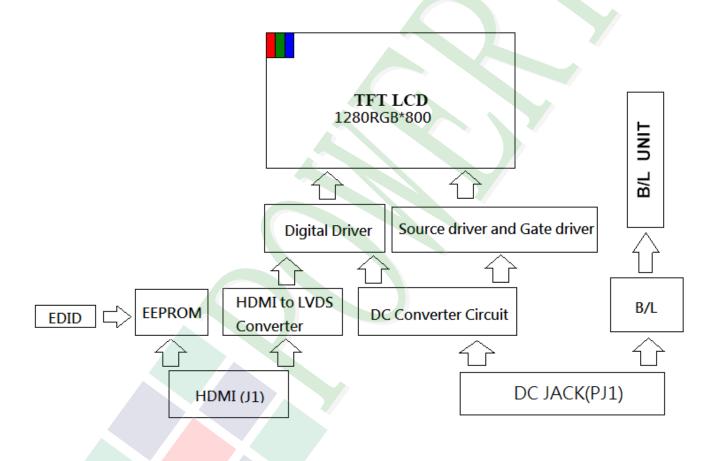
#### 2. Module Structure

## 2.1 Counter Drawing

## 2.1.1 LCM Mechanical Diagram

\* See Appendix

## 2.1.2 Block Diagram



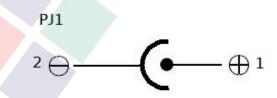


## 2.2 Interface Pin Description

### 2.2.1 (J1: HDMI 1.3 A type Interface)

Pin#	<u>Name</u>	<u>Description</u>
1	TX2+	TMDS Data 2+
2	TX2 Shield	TMDS Data 2 Shield
3	TX2-	TMDS Data 2-
4	TX1+	TMDS Data 1+
5	TX1 Shield	TMDS Data 1 Shield
6	TX1-	TMDS Data 1-
7	TX0+	TMDS Data 0+
8	TX0 Shield	TMDS Data 0 Shield
9	TX0-	TMDS Data 0-
10	TXC+	TMDS Clock+
11	TXC Shield	TMDS Clock Shield
12	TXC-	TMDS Clock-
13	CEC	CEC
14	NC	No connection
15	SCL	Serial Clock for DDC
16	SDA	Serial Data for DDC
17	GND	Power Ground
18	V5V	+5V Power for HDMI
19	Hot Plug Detect	Hot Plug Detect

# 2.2.2 (PJ1: POWER DC JACK Interface)



Hold Φ6.4mm / Center Pin Φ 2.0mm

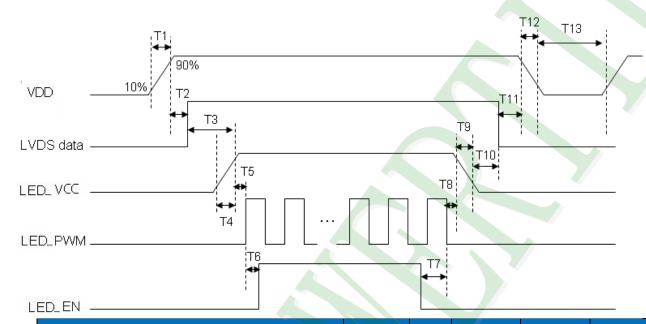
Pin#	<u>Name</u>	<u>Description</u>
1	VCC	+12V Power
2	GND	Power Ground



### 2.3 Power Supply Characteristics

### 2.3.1 Power ON/OFF Sequence

- 1. Interface signals are also shown in the chart. Signals from any system shall be Hi-resistance state or low level when VDD voltage is off.
- 2. Please set timing according to the following figures, otherwise it may cause image sticking



<u>Parameter</u>	Symbol	<u>Unit</u>	<u>Min</u>	Typ.	<u>Max</u>
VDD Rise Time (10% to 90%)	T1	ms	0.5		10
VDD Good to Signal Valid	T2	ms	30	I	90
Signal Valid to Backlight On	Т3	ms	200	I	
Backlight Power On Time	T4	ms	0.5		
Backlight LED_VCC Good to System PWM	T5	ms	10		
On					
System PWM On to Backlight LED_EN On	Т6	ms	10	1	
Backlight LED_ EN Off to System PWM Off	Т7	ms	0	-1	
System PWM Off to B/L Power Disable	Т8	ms	10		
Backlight Power Off Time	Т9	ms	0.5	10	30
Backlight Off to Signal Disable	T10	ms	200	I	
Signal Disable to Power Down	T11	ms	0	1	50
VDD Fall Time	T12	ms	0.5	10	30
Power Off	T13	ms	500	-	

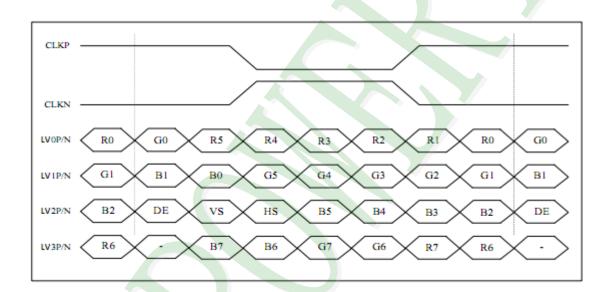


## 2.3.2 Input Timing

<u>Parameter</u>	<u>Symbol</u>	Min.	<u>Typ.</u>	Max.	<u>Unit</u>
LVDS Clock Frequency	Fclk	70.0	72.4	76.6	MHz
H Total Time	HT	1410	1440	1470	Clocks
H Active Time	HA		1280	1	
V Total Time	VT	828	838	868	Lines
V Active Time	VA		800		
Frame Rate	FV	-	60	-	Hz

Note1: HT \* VT \*Frame Frequency ≤ (76.6) MHz

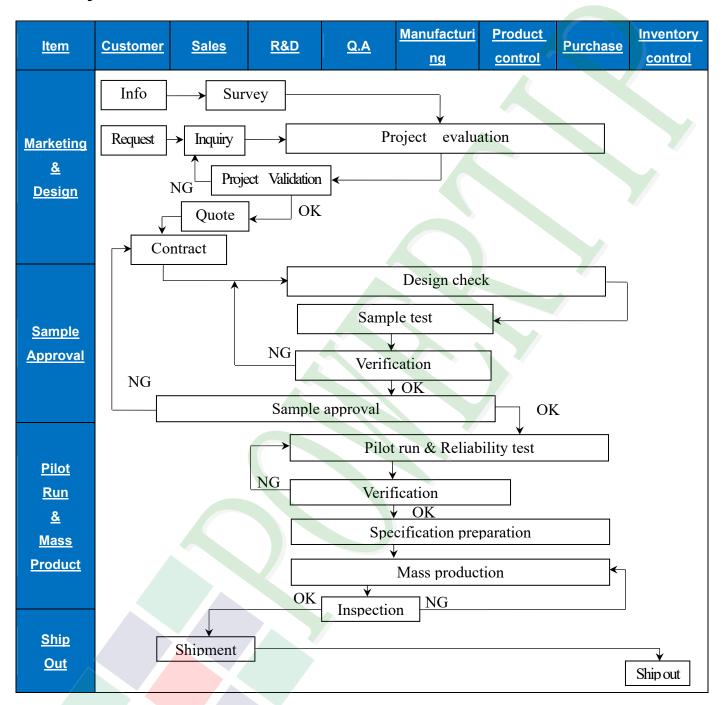
Note2: All reliabilities are specified for timing specification based on refresh rate of 60Hz.



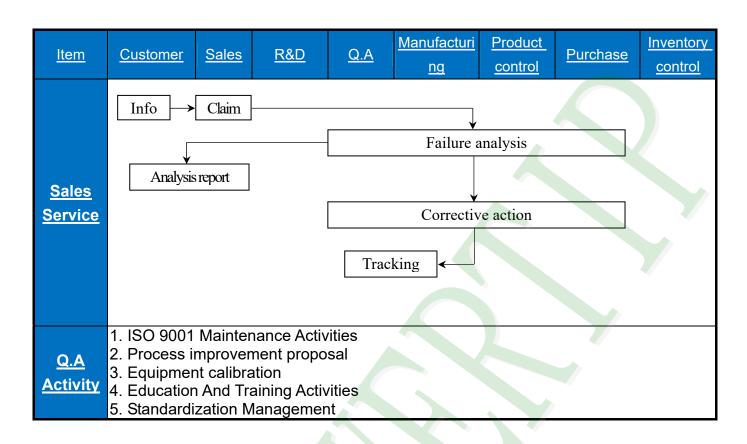


### 3. Quality Assurance System

## 3.1 Quality Assurance Flow Chart









### 3.2 Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5"-15" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

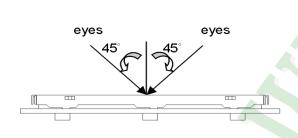
◆OUT Going Defect Level: Sampling

◆Standard of the product appearance test:

a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light(about 300lux  $\sim$ 500lux) and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.



5% Brightness

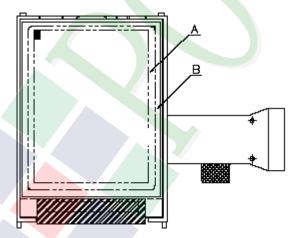
30~40 cm

100% Brightness

LCD panel

2.5~3cm

(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



# ◆Specification For TFT-LCD Module 3.5"~15":

NO	<u>Item</u>	<u>Criterion</u>		
		1.1 The part number is inconsistent with work order of production.	<u>Level</u> Major	
01	01 Product condition	1.2 Mixed product types.		
		1.3 Assembled in inverse direction.		
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major	
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major	
		4.1 Missing line character and icon.	Major	
		4.2 No function or no display.	Major	
0.4	Flackii al Tackia	4.3 Display malfunction.	Major	
04	Electrical Testing	4.4 LCD viewing angle defect.	Major	
		4.5 Current consumption exceeds product specifications.	Major	
		4.6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.		
		<u>Item</u> <u>Acceptance (Q'ty)</u>		
		Bright Dot ≤ 4		
	Dot defect	Dot Dark Dot ≦ 5		
		Defect Joint Dot ≦ 3		
	Dot dollost	Total ≤ 7		
05	5.1 Inspection pattern: full white, full black, Red, Green and blue screens.  5.2 It is defined as dot defect if defect area > 1/2 dot.  5.3 The distance between two dot defect ≥5 mm.  5.4 Bright dot: Dots appear bright and unchanged in visible with 5% ND filter is defined.  5.5 Tiny bright dot: bright dot area ≤1/2 dot.  a. Dots appear bright and unchanged in visible with 5% ND filter is defined defect and is judged in accordance with 6.1  b. Dots invisible with 5% ND Filter is Ignored		Minor	



## ◆Specification For TFT-LCD Module 3.5″ ~15″:

NO	<u>Item</u>				Level			
		6.1 Round type (Non-display or display):						
					A	(Olt		
		Dimension	n (diamete	<u>er: Φ)</u>	Accept A area	ance (Q'ty B ar		
			Ф ≦ 0.	.25	Ignore	<u>D</u> (II)	54	
	Disale an orbita	0.25 <	<u> </u>		5			
	Black or white Dot, scratch,		Φ > 0.		0	Igno	re	
	contamination		Total		5			
	Round type							
		6.2 Line type(Nor	n-display d	or displa	y):			
	$\rightarrow$ X	module size	Length	VA/i	dth (W)	Acceptar	nce (Q'ty)	
	Y	illoudie Size	<u>(L)</u>			A area	B area	
06	$\Phi = (x + x)/2$		 L ≦		≦ 0.03	Ignore		Minor
	$\Phi = (x+y)/2$		10.0	0.03 <	$W \leq 0.05$	4		
		3.5" to less 9"	L ≦5.0	0.05 <	$W \leq 0.10$	2	Ignore	
	Line type		_	W	>0.10	As round type		
	↓			Total		5		
	│			W	≦ 0.05	Ignore		
	→ı <sub>L</sub> .		L ≦ 10.0	0.05 <	$\langle W \leq 0.10 \rangle$	5		
		<u>9" to 15"</u>		W	>0.10	As round	Ignore	
					0.10	type	-	
				Total		5		
					Access	(0)4	-1	
		Dimension (	<u>diameter:</u>	Ф)	Accept A area	tance (Q'ty B	<u>narea</u>	
	Polarizer	4	0.25		Ignore			
07	Bubble	0.25 < ₵	0.50 ≥		4			Minor
		0.50 < Φ	≥ 0.80		1	lg	nore	
			○ >0.80		0			
		To	tal		5			



### ◆Specification For TFT-LCD Module 3.5″ ~15″

<u>NO</u>	<u>Item</u>	<u>Criterion</u>		
08	The crack of glass	X: The length of crack Z: The thickness of crack T: The thickness of glass  8.1 General glass chip: 8.1.1 Chip on panel surface and crack between panels:  SP  Y  ING  Seal width  X  Y: The width of crack. W: terminal length a: LCD side length  ING  ING  ING  ING  ING  ING  ING  IN	Level	
		$egin{array}{ c c c c c } \hline X & Y & \underline{Z} \\ & \leq & & Crack can't enter & & \leq 1/2 t \\ \hline & & viewing area & & & \end{array}$		
		$\leq$ a Crack can't exceed the half of SP width. 1/2 t $<$ Z $\leq$ 2 t		



### ◆Specification For TFT-LCD Module 3.5″ ~15″:

<u>NO</u>	<u>ltem</u>	<u>Criterion</u>		
		X: The length of crack Z: The thickness of crack t: The thickness of glass  8.1.2 Corner crack:		
		<u>X</u> <u>Y</u> <u>Z</u>		
		$\leq$ 1/5 a Crack can't enter viewing area $Z \leq 1/2 t$		
		$\leq$ 1/5 a Crack can't exceed the half of SP width. 1/2 t $<$ Z $\leq$ 2 t		
00	The small of misse		) / ·	
08	The crack of glass	8.2 Protrusion over terminal:	Minor	
	8.2.1 Chip on electrode pad:  X X X X X X X X X X X X X X X X X X			
		$\begin{array}{c cccc} \underline{X} & \underline{Y} & \underline{Z} \\ \hline \textbf{Front} & \leq \mathbf{a} & \leq 1/2  \mathbf{W} & \leq \mathbf{t} \\ \hline \textbf{Back} & \leq \mathbf{a} & \leq \mathbf{W} & \leq 1/2  \mathbf{t} \\ \end{array}$		



## ◆Specification For TFT-LCD Module 3.5″ ~15″:

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	Level
			Level



### ◆Specification For TFT-LCD Module 3.5″ ~15″

NO	<u>ltem</u>	<u>Criterion</u>	Level
		9.1 Backlight can't work normally.	Major
09 Backlight elements	9.2 Backlight doesn't light or color is wrong.	Major	
		9.3 Illumination source flickers when lit.	Major
		10.1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
40	General	10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
10	appearance	10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
•		10.6 The PCB or FPC between B/L assembled distance (PCB or FPC ) is $\leq$ 1.5 mm.	Minor



### 4. RELIABILITY TEST

### 4.1 Reliability Test Condition

(Ver.B01)

4.1	Reliability Test Condition (ver.bu1)				
<u>NO.</u>	TEST ITEM	TEST CO	TEST CONDITION		
1	High Temperature Storage Test	Keep in 85 ±5°C 240 hrs			
2	Low Temperature Storage Test	Keep in -40 ±5℃ 240 hrs			
3	High Temperature / High Humidity Storage Test	Keep in 60 ℃ / 90% R.H durati (Excluding the polarizer)	Keep in 60 ℃ / 90% R.H duration for 240 hrs (Excluding the polarizer)		
		-40°C → +25°C	→ <b>85</b> °C → <b>+25</b> °C		
4	Temperature Cycling Storage Test	(30mins) (5mins)	(30mins) (5mins)		
	Storage rest	20 (	Cycle		
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15° 2. Humidity relative: 30%~60°			
v	200 1000	<ol> <li>Humidity relative: 30%~60%</li> <li>Energy Storage Capacitance(Cs+Cd): 150pF±10%</li> <li>Discharge Resistance(Rd): 330Ω±10%</li> <li>Discharge, mode of operation:</li> <li>Single Discharge (time between successive discharges at least 1 sec)</li> <li>(Tolerance if the output voltage indication: ±5%)</li> </ol>			
6	Vibration Test (Packaged)	<ol> <li>Sine wave 10~55 Hz frequency (1 min/sweep)</li> <li>The amplitude of vibration: 1.5 mm</li> <li>Each direction (X, Y, Z) duration for 2 hrs</li> </ol>			
7	Drop Test (Packaged)	Packing Weight (Kg 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454	122 76 61		
	(i denaged)	Over 454	46		
		Drop Direction : %1 corner / 3 e			

#### **©Result Evaluation Criteria:**

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function.

(Normal operation state) Temperature: +20~30°C

Humidity : 50~70%

Atmospheric pressure: 86~106Kpa



#### 5. PRECAUTION RELATING PRODUCT HANDLING

#### **5.1 SAFETY**

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### **5.2 HANDLING**

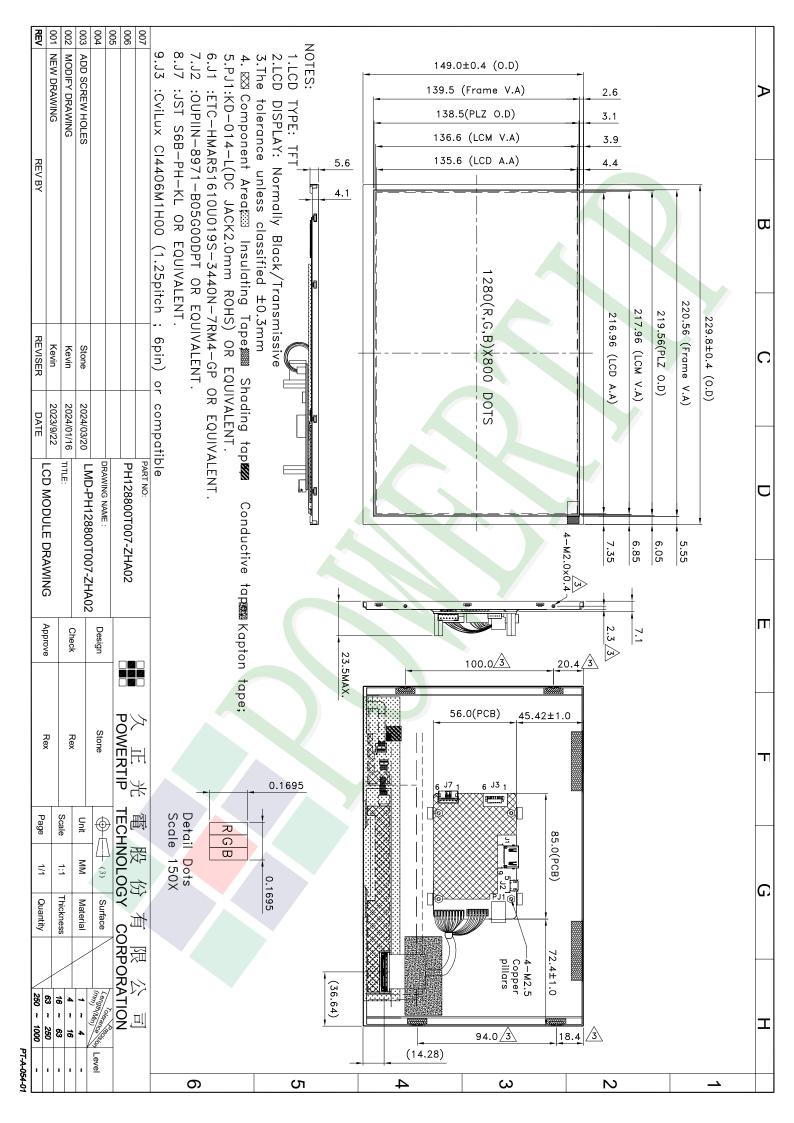
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!( LCM products with Capacitive Touch Panel)
  Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
  Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

#### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

#### **5.4 TERMS OF WARRANTY**

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
  - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



#### Approve Check Ver.001 Packaging Specifications Documents NO. PKG-PH128800T007-ZHA02 Oliver Stone 1.Packaging Material: (per carton) 1Pcs Weight No. Item Model Dimensions (mm) Quantity PH128800T007-ZHA02 1 成品 (LCM) 229.8 X 149 0.2567 8 2 300 X 240 靜電袋(1)Antistatic Bag BAG000000021 0.01 8 3 A10隔板(2)A10 Partition BX0000000119 340 X 174 X 7 0.031 10 4 B10隔板(3)B10 Partition BX0000000118 500 X 174 X 7 0.037 2 5 舒美墊(4)EPE FOAM00000244 500 X 340 X 20 0.036 2 6 0.75 1 外箱C6 (5)Carton BX0000000120 514 X 354 X 228 7 舒美墊(6)EPE FOAM00000245 170 X 130 X 15 0.01 6 8 舒美墊(7)EPE FOAM00000246 170 X 23 X 15 0.001 38 9 333 X 218 X 10 0.022 EPE(8)EPE OTFOAMEP0003BA 4 10 EPE(9)EPE OTFOAMEP0002BA 333 X 218 X 5 0.011 4 2. 一整箱總重量 (Total LCD Weight in carton): 3.57 Kg±10% 3. 單箱數量規格表 (Packaging Specifications and Quantity): ,B10隔板 (1)Quantity Of Spacer: A10隔板 X 10 X 2 (2) Total LCM quantity in carton: quantity per box x no of boxes 8 1 = (4) 舒美墊 **EPE** (1)靜電袋+LCM Antistatic Bag+LCM (5)外紙箱 Carton (8)(9)舒美墊EPE (2)(3)隔板 Partition (6)(7)舒美墊EPE (註 Remark 1) (5) 舒美墊 EPE 特 記 事 項 (REMARK) 2. 10.0t EPE(舒美墊),可裁切成166.5x218mm3. 5.0t EPE(舒美墊),可裁切成166.5x218mm 1. LCM排放示意圖(前後間隔不放置): 1. LCM placed as figure showing: 2pcs 2pcs (First and last slot should be empty) 333 333

218

| 模組(LCM) X 1pcs.

裁切線

裁切線

Contact

Kevin

Total Weight

2.0536

0.08

0.31

0.074

0.072

0.75

0.06

0.038

0.088

0.044