# **FEATURES**

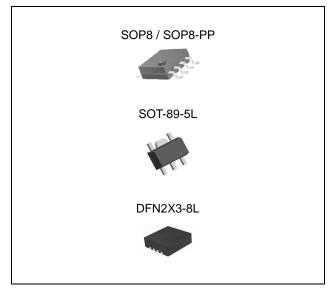
- Ultra Low Dropout Voltage
- Compatible with low ESR MLCC as Input / Output Capacitor
- · Good Line and Load Regulation
- · Guaranteed Output Current of 1A
- Available in SOP8, SOP8-PP, SOT-89-5L, DFN2X3-8L Packages
- Fixed Output: 1.0V, 1.2V, 1.5V, 1.8V, 2.5V, and 3.3V
- VOUT Power OK Signal
- Programmable Soft-Start Function
- · Output Auto Discharge Function
- · Over-Temperature/Over-Current Protection

#### **APPLICATION**

- LCD TVs and SETTOP Boxes
- Battery Powered Equipment
- · Motherboards and Graphic Cards
- Microprocessor Power Supplies
- Peripheral Cards
- · High Efficiency Linear Regulators
- · Battery Chargers

## **DESCRIPTION**

The TJ4310 series of high performance ultra-low dropout linear regulators operates from 2.5V to 5.5V input supply and provides ultra-low dropout voltage, high output current with low ground current. Wide range of preset output voltage options are available. These ultra-low dropout linear regulators respond fast to step changes in load which makes them suitable for low voltage micro-processor applications. The TJ4310 is developed on a CMOS process technology which allows low quiescent current operation independent of output load current. This CMOS process also allows the TJ4310 to operate under extremely low dropout conditions.



#### ORDERING INFORMATION

Device	Package		
TJ4310GD-ADJ	SOP8		
TJ4310GD-X.X			
TJ4310GDP-ADJ	SOP8-PP		
TJ4310GDP-X.X			
TJ4310GF-ADJ	SOT-89-5L		
TJ4310GQ-ADJ	DFN2X3-8L		

X.X = Output Voltage = 1.0, 1.2, 1.5, 1.8, 2.5, and 3.3

## ABSOLUTE MAXIMUM RATINGS (Note 1)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Input Supply Voltage (Survival)	V <sub>IN</sub>	-	6.5	V
Maximum Output Current	I <sub>MAX</sub>	-	1	Α
Lead Temperature (Soldering, 5 sec)	T <sub>SOL</sub>		260	°C
Storage Temperature Range	T <sub>STG</sub>	-65	150	°C
Operating Junction Temperature Range	$T_JOPR$	-40	125	°C
Package Thermal Resistance *	O <sub>JA-SOP8-PP</sub>	68		°C/W

<sup>\*</sup> Calculated from package in still air, mounted to 2.6mm X 3.5mm(minimum foot print) 2 layer PCB without thermal vias per JESD51 standards.

