SP	FC	IFI	CAT	NS
JE			CAI	110

CUSTOMER .

SAMPLE CODE . SH128800T006-ZHC02

MASS PRODUCTION CODE . PH128800T006-ZHC02

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 005

DRAWING NO. (Ver.) . LMD-PH128800T006-ZHC02 (Ver.003)

PACKAGING NO. (Ver.) PKG-PH128800T006-ZHC02 (Ver.001)

Customer Approved

Date:

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□ Preliminary specification for design input

■ Specification for sample approval

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History of Version

Date (mm / dd / yyyy)	<u>Ver.</u>	Edi.	<u>Description</u>	<u>Page</u>	Design by
05/30/2023	01	001	Preliminary.	-	lan
10/06/2023	01	002	First Sample Modify Drawing	- Appendix	lan
11/21/2023	01	003	Modify Backlight LED Voltage	9	lan
11/24/2023	01	004	Modify Outline Dimension	4	lan
03/15/2024	01	005	Reduce backlight power consumption Modify Dimension	5,9 Appendix	lan
				>	



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1. SPECIFICATIONS

1.1 Features

<u>ltem</u>	Standard Value
Display Resolution	1280 *3 (RGB) * 800 Dots
LCD Type	Full Viewing Angle , Normally Black, Transmissive type
Screen size(inch)	10.1 inch
Color configuration	R.G.B. Vertical Stripe
Weight	479g
Interface	LVDS
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website:
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

<u>ltem</u>	Standard Value	<u>Unit</u>
Outline Dimension	254.96 (W) * 173.6 (L) * 14.75(MAX)(H)	mm

LCD panel

<u>Item</u>	<u>Standard Value</u>	<u>Unit</u>
View Area	217.96 (W) * 136.6 (L)	mm
Active Area	216.96 (W) * 135.6 (L)	mm

Note: For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Module

<u>ltem</u>	<u>Symbol</u>	Condition	Min.	Max.	<u>Unit</u>	Remark
Logic Supply Voltage	V_{DD}	GND=0V	-0.3	+4.0	V	
Logic Input Signal Voltage	V _{signal}	GND=0V	-0.3	+4.0	V	
Power Supply for Backlight Unit	LED_Vcc	LED_GND=0V	-0.3	+18.0	V	-
Operating Temperature	Top (Ts)	Note 1	-20	+70	°C	
Storage Temperature	Tsr(Ta)	Note 2	-30	+80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

GND = 0V, Ta = 25°C

<u>ltem</u>	<u>Symbol</u>	Condition	Min.	<u>Typ.</u>	Max.	<u>Unit</u>
Logic Supply Voltage	V_{DD}	GND=0V	3.0	3.3	3.6	V
Logic Current	IDD	V _{DD} =3.3V	-	260	450	mA
Power Supply for Backlight Unit	LED_Vcc	LED_GND=0V	0	12.0	18.0	V
Backlight Unit Power Consumption	PLED_Vcc	LED_Vcc =12V	-	6	9	W
PWM Signal Voltage	VIH		2	-	-	V
LED Enable Voltage	VIL		-	-	8.0	V
Input PWM Frequency	FPWM	GND=0V	100	-	8k	Hz
PWM Duty Ratio	PWM		5	-	100	%



1.5 Optical Characteristics

VDD=3.3V, Ta=25°C

<u>Item</u>	<u>Symbol</u>		Condition	Min.	Typ.	Max.	<u>unit</u>	
Response time	Tr	+Tf	Ta = 25°C θX, θY = 0°	-	25	50	ms	Note 2
	Тор	θΥ+		- (85	-		
Viouing angle	Bottom	θΥ-	CR ≥ 10		85	-	Dog	Note 4
Viewing angle	Left	θX-	CR 2 10		85	-	Deg.	Note 4
	Right	θX+		<u>-</u>	85	-		,
Contrast ratio)	CR		600	800	-		Note 3
	\\/\bito	Х		0.25	0.30	0.35		Note1
	White	Υ		0.29	0.34	0.39	-	
	Red	Х	Ta = 25°C θX , θY = 0°	0.53	0.58	0.63		
Color of CIE	Reu	Υ		0.30	0.35	0.40		
Coordinate	Green	X		0.25	0.30	0.35		
		Y		0.53	0.58	0.63		
	Blue	X		0.08	0.13	0.18		
		Y		0.09	0.14	0.19		
Average Brightness Pattern=white display (With LCD)*2	IV		LED_Vcc =12.0V PWM="High" (Duty=100%)	700	850	-	cd/m ²	Note1
Uniformity (With LCD)*1	ΔВ		LED_V _{CC} =12.0V PWM="High" (Duty=100%)	70	-	-	%	Note1



Note 1:

*1: △B=B(min) / B(max) * 100%

*2: Measurement Condition for Optical Characteristics:

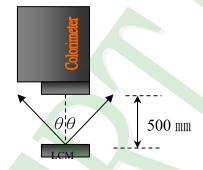
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency

b: Measurement Distance: 500 ± 50 mm, $(\theta = 0^{\circ})$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

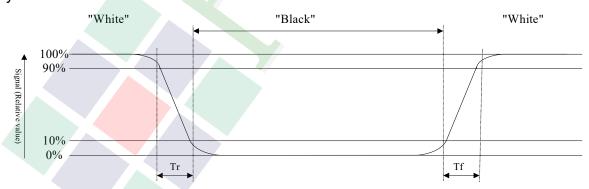
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note 2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

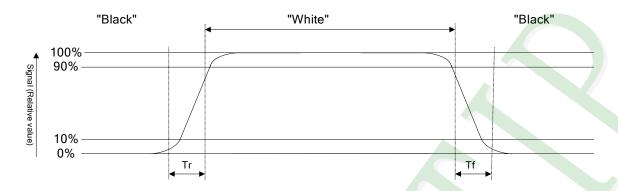
Refer to figure as below:

Normally White





Normally Black



Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

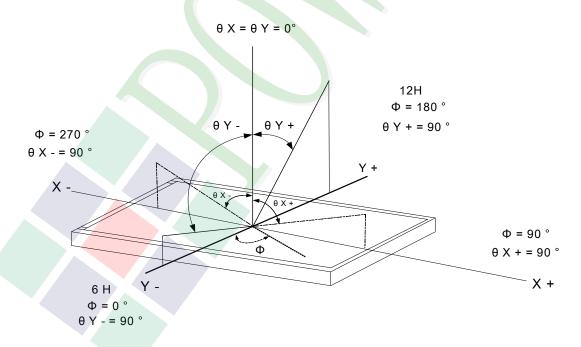
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note 4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Unit Characteristics

Maximum Ratings

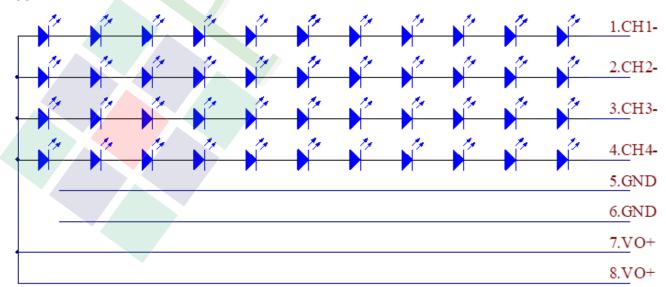
<u>ltem</u>	<u>Symbol</u>	Min.	Max.	<u>Unit</u>	<u>Remark</u>
LED Forward Current	l _F	-	600	mA	
LED Reverse Voltage	VR	-	13.2	V	
Power Dissipation	PD		20460	mW	

Electrical / Optical Characteristics

<u>Item</u>	<u>Symbol</u>	Min.	Typ.	Max.	<u>Unit</u>	<u>Remark</u>
LED Voltage	Vf	29.7	31.9	34.1	V	Note1
LED Current	If	-	180	1	mA	-
Average Brightness (without LCD) *1	IV	17000	19000	28000	-	cd/m ²
CIE Color Coordinate	X	0.25	0.28	0.335		
(Without LCD)	Y	0.25	0.28	0.335		
LED life time	-	50,000	1	-	Hr	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}$ C and If =180 mA

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and If=180 mA. The LED life time could be decreased if operating I_L is larger than 180 mA





1.7 Touch Panel Unit Characteristics

Features

<u>Item</u>	Standard Value
Touch Panel Size	10.1"
Touch type	Projective capacitive touch panel
Input Method	Finger
Support Operation	5 Points touch
Output Interface	I2C · USB
IC	ILI2132

Absolute Maximum Ratings

<u>ltem</u>	<u>Symbol</u>	Condition	Min.	Max.	<u>unit</u>
Operating Temperature	Тор		-20	+70	°C
Storage Temperature	Tst	-	-30	+80	°C

I²C Address

Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
1	0	0	0	0	0	1	R/W

R/W: 1: Read

0: write

DC Electrical Characteristics

<u>Item</u>	Symbol	Condition	<u>Min.</u>	<u>Typ.</u>	Max.	<u>unit</u>
Power Supply Voltage(I ² C)	VI2C	-	-	3.3	-	V
Power Supply Voltage(USB)	VUSB	-	1	5	-	V

Optical Characteristics

<u>Item</u>	Standard Value	<u>unit</u>
Total light transmittance	85% or more	-
Haze	3% or less	-



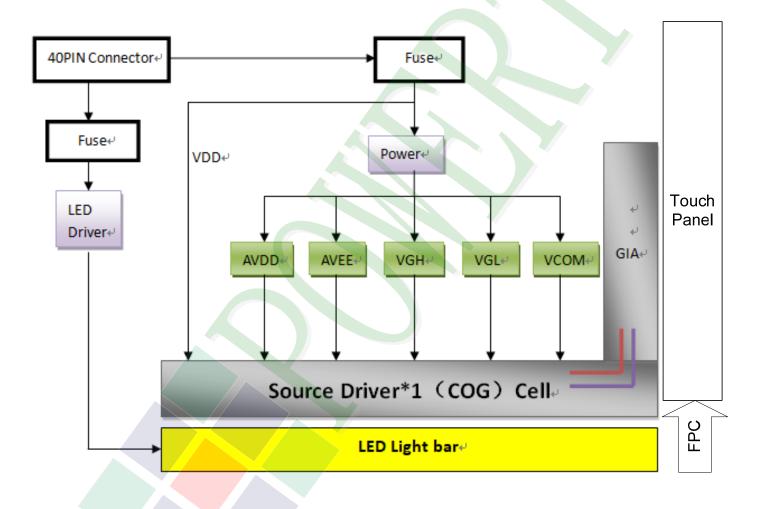
2. Module Structure

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

TFT LCM Interface

Pin#	<u>Name</u>	<u>Description</u>
1	NC	No Connection.
2	VDD	Power Supply.
3	VDD	Power Supply.
4	NC	No Connection.
5	NC	No Connection.
6	NC	No Connection.
7	NC	No Connection.
8	LV0N	-LVDS Differential Data Input.
9	LV0P	+LVDS Differential Data Input.
10	GND	Power ground.
11	LV1N	-LVDS Differential Data Input.
12	LV1P	+LVDS Differential Data Input.
13	GND	Power ground.
14	LV2N	-LVDS Differential Data Input.
15	LV2P	+LVDS Differential Data Input.
16	GND	Power ground.
17	LVCLKN	-LVDS Differential Clock Input.
18	LVCLKP	+LVDS Differential Clock Input.
19	GND	Power ground.
20	LV3N	-LVDS Differential Data Input.
21	LV3P	+LVDS Differential Data Input.
22	GND	Power ground.
23	LED_GND	Ground for LED Driving.
24	LED_GND	Ground for LED Driving.
25	LED_GND	Ground for LED Driving.
26	NC	No Connection.
27	LED_PWM	PWM Input Signal for Backlight Diver.
28	LED_EN	Backlight Enable Pin.
29	NC	No Connection.



Pin#	<u>Name</u>	<u>Description</u>
30	NC	No Connection.
31	LED_VCC	Power Supply for Backlight Diver.
32	LED_VCC	Power Supply for Backlight Diver.
33	LED_VCC	Power Supply for Backlight Diver.
34	NC	No Connection.
35	BIST	Self Test Enable. When it is not used, please don't connect to GND,
		connecting to Normal High(3.3V) is recommended
36	NC	No Connection.
37	NC	No Connection.
38	NC	No Connection.
39	NC	No Connection.
40	NC	No Connection.

TP Connector Interface

CN1 I²C

<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
1	GND	Ground.
2	I ² C_SDA	I ² C Data
3	I ² C_SCL	I ² C Clock
4	I ² C_INT	Active Low
5	I ² C_RST	Active low global reset signal input.
6	VI2C	Power Supply Voltage (3.3V)

CN2 USB

<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
1	VUSB	Power Supply Voltage (5V)
2	USB_DN	Negative Data
3	USB_DP	Positive Data
4	GND	Ground
5	GND	Ground



2.3 Timing Characteristics

2.3.1 Signal Electrical Characteristics For LVDS Receiver

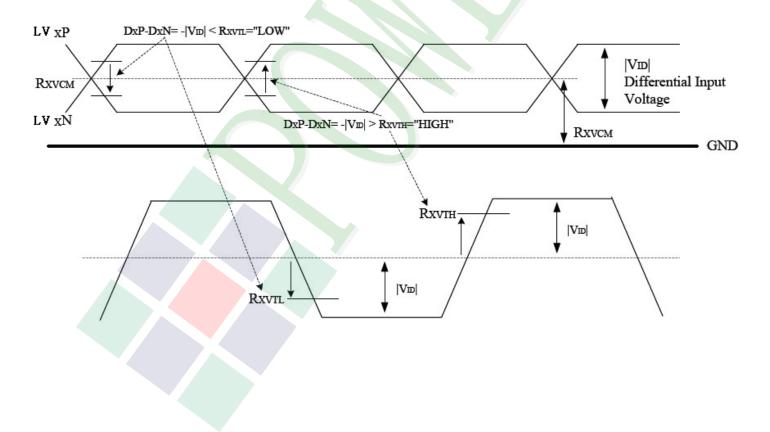
The built-in LVDS receiver is compatible with (ANSI/TIA/TIA-644) standard.

<u>Parameter</u>	<u>Symbol</u>	Min.	Typ.	Max.	<u>Unit</u>	Conditions
Differential Input High Threshold	Vth	ı	ı	100	mV	V _{CM} =+1.2V
Differential Input Low Threshold	VtI	-100	-	-	mV	V _{CM} =+1.2V
Input voltage range(singled-end)	RXVIN	0.7		1.7	V	
Magnitude Differential Input Voltage	V _{ID}	200	- (600	mV	-
Common Mode Voltage	V_{CM}	1	1.2	1.4	٧	VID =0.2

Note (1) Input signals shall be low or Hi-resistance state when VDD is off.

Note (2) All electrical characteristics for LVDS signal are defined and shall be measured at theinterface connector of LCD.

Single-end Signals





2.3.2 Input Timing

<u>Parameter</u>	<u>Symbol</u>	Min.	<u>Typ.</u>	Max.	<u>Unit</u>
LVDS Clock Frequency	Fclk	70.0	72.4	76.6	MHz
H Total Time	HT	1410	1440	1470	Clocks
H Active Time	HA		1280	1	
V Total Time	VT	828	838	868	Lines
V Active Time	VA		800		
Frame Rate	FV	-	60	-	Hz

Note1: HT * VT *Frame Frequency ≤ (76.6) MHz

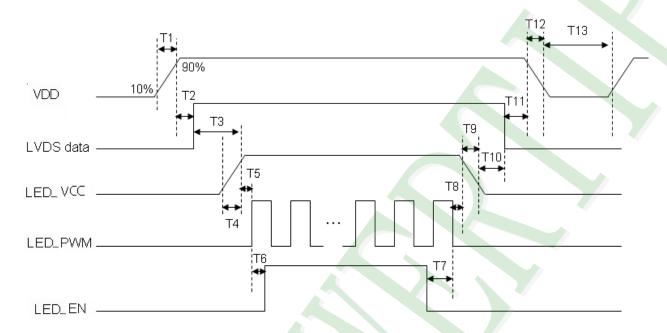
Note2: All reliabilities are specified for timing specification based on refresh rate of 60Hz.





2.3.4 Power ON/OFF Sequence

- 1. Interface signals are also shown in the chart. Signals from any system shall be Hi-resistance state or low level when VDD voltage is off.
- 2. Please set timing according to the following figures, otherwise it may cause image sticking

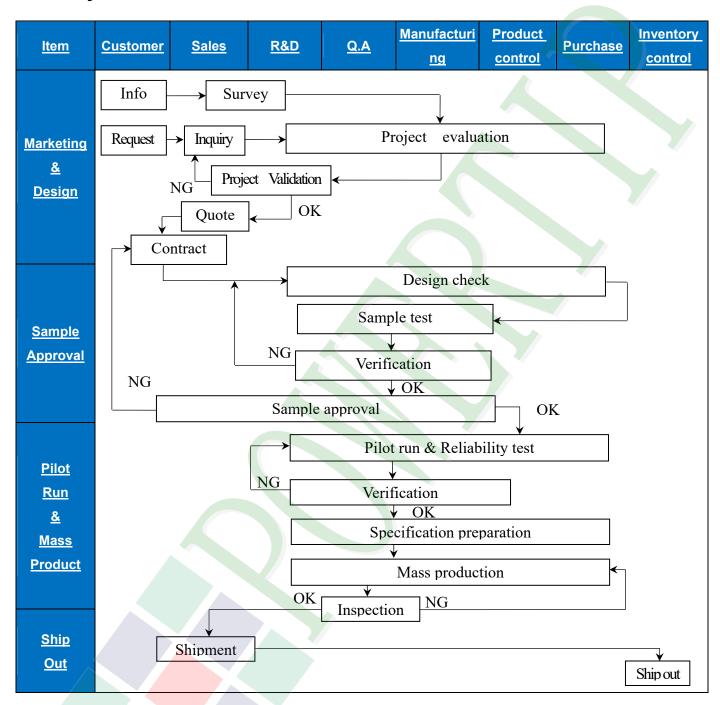


<u>Parameter</u>	Symbol	<u>Unit</u>	<u>Min</u>	<u>Typ.</u>	<u>Max</u>
VDD Rise Time (10% to 90%)	T1	ms	0.5		10
VDD Good to Signal Valid	T2	ms	30	-	90
Signal Valid to Backlight On	Т3	ms	200	I	
Backlight Power On Time	T4	ms	0.5	I	
Backlight LED_VCC Good to System PWM On	T5	ms	10		
System PWM On to Backlight LED_EN On	Т6	ms	10		
Backlight LED_EN Off to System PWM Off	Т7	ms	0		
System PWM Off to B/L Power Disable	Т8	ms	10		
Backlight Power Off Time	Т9	ms	0.5	10	30
Backlight Off to Signal Disable	T10	ms	200	I	
Signal Disable to Power Down	T11	ms	0	1	50
VDD Fall Time	T12	ms	0.5	10	30
Power Off	T13	ms	500		

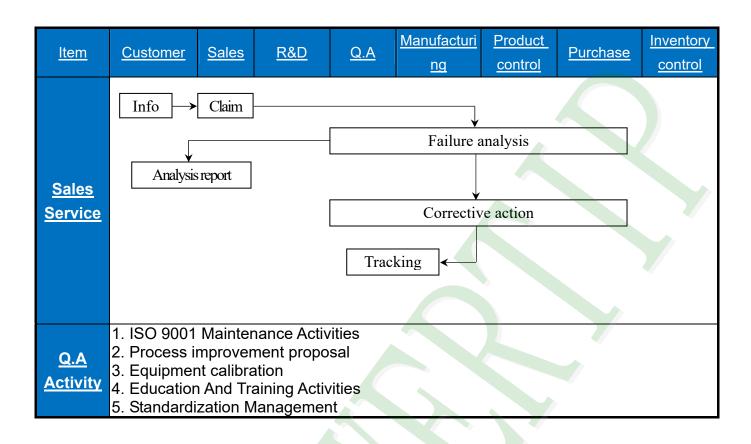


3. Quality Assurance System

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5"-15" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

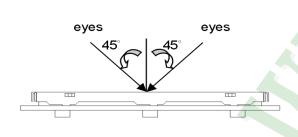
◆OUT Going Defect Level: Sampling

◆Standard of the product appearance test:

a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light(about 300lux \sim 500lux) and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.



5% Brightness

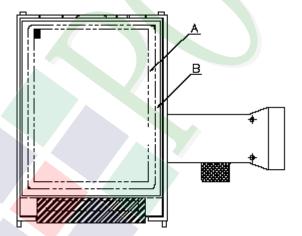
ND fliter

30~40 cm

LCD panel

2.5~3cm

(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module 3.5"~15":

NO	<u>Item</u>	Criterion (ver.	Level				
10	<u> </u>	1.1 The part number is inconsistent with work order of production.	Major				
01	Product condition	1.2 Mixed product types.	Major				
		1.3 Assembled in inverse direction.	Major				
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major				
		4.1 Missing line character and icon.	Major				
		4.2 No function or no display.	Major				
		4.3 Display malfunction.	Major				
04	Electrical Testing	4.4 LCD viewing angle defect.	Major				
		4.5 Current consumption exceeds product specifications.					
		4.6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.	Minor				
		Item Acceptance (Q'ty)					
		Bright Dot ≤ 4					
		Dot Dark Dot ≤ 5					
	Dot defect	Defect Joint Dot ≤ 3					
	Bot dolost	Total ≤ 7					
05	(Bright dot, Dark dot) On -display	 5.1 Inspection pattern: full white, full black, Red, Green and blue screens. 5.2 It is defined as dot defect if defect area > 1/2 dot. 5.3 The distance between two dot defect ≥5 mm. 5.4 Bright dot: Dots appear bright and unchanged in visible with 5% ND filter is defined. 5.5 Tiny bright dot: bright dot area ≤1/2 dot. a. Dots appear bright and unchanged in visible with 5% ND filter is defined defect and is judged in accordance with 6.1 b. Dots invisible with 5% ND Filter is Ignored 	Minor				



◆Specification For TFT-LCD Module 3.5″ ~15″:

NO	<u>ltem</u>	<u>Criterion</u>				Level		
		6.1 Round type (Non-display or display):						
		Dimensio	on (diame	ter: Ф <u>)</u>		otance (Q'1		
			Φ ≦ (<u>A area</u> Ignore		rea	
	Black or white	0.25	<u> </u>		5			
	Dot, scratch,		Φ > 0	0.50	0	Ign	ore	
	contamination		Total		5			
	Round type	6.2 Line type(No	on-display	or displa	y):			
	\rightarrow X	module	Length	- War d	42.010	Acceptar	nce (Q'ty)	
	<u>Y</u>	size	<u>(L)</u>		th (W)	A area	B area	
06	$\Phi = (x+y)/2$		 L ≦		≤ 0.03	Ignore	-	Minor
		3.5" to less	10.0		$N \leq 0.05$	4		
		9"	L ≦5.0	$0.05 < W \le 0.10$ W > 0.10		2 As round	19.10.0	
	Line type			Total	0.10	type 5	-	
	✓ ✓ W				≤ 0.05	Ignore		
	→ L +		L ≦ 10.0	0.05 <	W ≤ 0.10	5		
	_	9" to 15"		W	>0.10	As round	Ignore	
				Total		type 5	-	
		Dimension	<u>(diameter</u>	<u>: Φ)</u>	Accer A area	otance (Q'	area	
	Polarizer		Φ ≤ 0.25	5	Ignore			
07	Bubble		$\Phi \leq 0.50$		4			Minor
		0.50 <	$0.50 < \Phi \le 0.80$ $\Phi > 0.80$		1 lg		gnore	
	X	To	φ >0.80 otal		5			



◆Specification For TFT-LCD Module 3.5″ ~15″

<u>NO</u>	<u>ltem</u>	<u>Criterion</u>		
NO 08	The crack of glass	Symbols: X: The length of crack Z: The thickness of crack W: te	ne width of crack. erminal length CD side length ack between panels: Y ING Minor	
		X Y Crack can't enter	<u>Z</u>	
		≥ a viewing area	≦1/2 t	
		≦a Crack can't exceed the half of SP width.	1/2 t < Z ≦2 t	



◆Specification For TFT-LCD Module 3.5″ ~15″:

NO	<u>Item</u>	<u>Criterion</u>				
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8.1.2 Corner crack:				
		X Y Z				
		≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t				
		\leq 1/5 a Crack can't exceed the half of SP width. 1/2 t $<$ Z \leq 2 t				
08	The crack of glass 8.2 Protrusion over terminal:					
	8.2 Protrusion over terminal: 8.2.1 Chip on electrode pad: X Y X X Y X Y X Y X Y X Y X Y X Y X Y					
4		$\begin{array}{c ccccc} X & Y & Z \\ \hline Front & \leq a & \leq 1/2 W & \leq t \\ \hline Back & \leq a & \leq W & \leq 1/2 t \\ \hline \end{array}$				



◆Specification For TFT-LCD Module 3.5″ ~15″:

<u>NO</u>	<u>ltem</u>	<u>Criterion</u>	Level
08	The crack of glass	Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8.2.2 Non-conductive portion: X X X X X X X X X X X X	Minor



◆Specification For TFT-LCD Module 3.5″ ~15″

NO	<u>ltem</u>	<u>Criterion</u>	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
		10.1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
	General	10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
10	appearance	10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance (PCB or FPC) is \leq 1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

4.1	Reliability Test Condition (ver.bu1)					
<u>NO.</u>	TEST ITEM	TEST CONDITION				
1	High Temperature Storage Test	Keep in 80 ±5°C 240 hrs				
2	Low Temperature Storage Test	Keep in -30 ±5℃ 240 hrs				
3	High Temperature / High Humidity Storage Test	Keep in 60 ℃ / 90% R.H duration for 240 hrs (Excluding the polarizer)				
		-30°C → +25°C	→ 80 °C → +25 °C			
4	Temperature Cycling Storage Test	(30mins) (5mins)	(30mins) (5mins)			
	Storage rest	20 (Cycle			
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15° 2. Humidity relative: 30%~60°				
		 Energy Storage Capacitance Discharge Resistance(Rd): Discharge, mode of operation Single Discharge (time between 	e(Cs+Cd): 150pF±10% 330Ω±10%			
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration: 1.5 mm Each direction (X, Y, Z) duration for 2 hrs 				
7	Drop Test (Packaged)	Packing Weight (Kg 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454	76 61 46			
		Drop Direction : 1 corner / 3 e	dges / 6 sides each 1time			

©Result Evaluation Criteria:

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function.

(Normal operation state) Temperature: +20~30°C

Humidity : 50~70%

Atmospheric pressure: 86~106Kpa



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

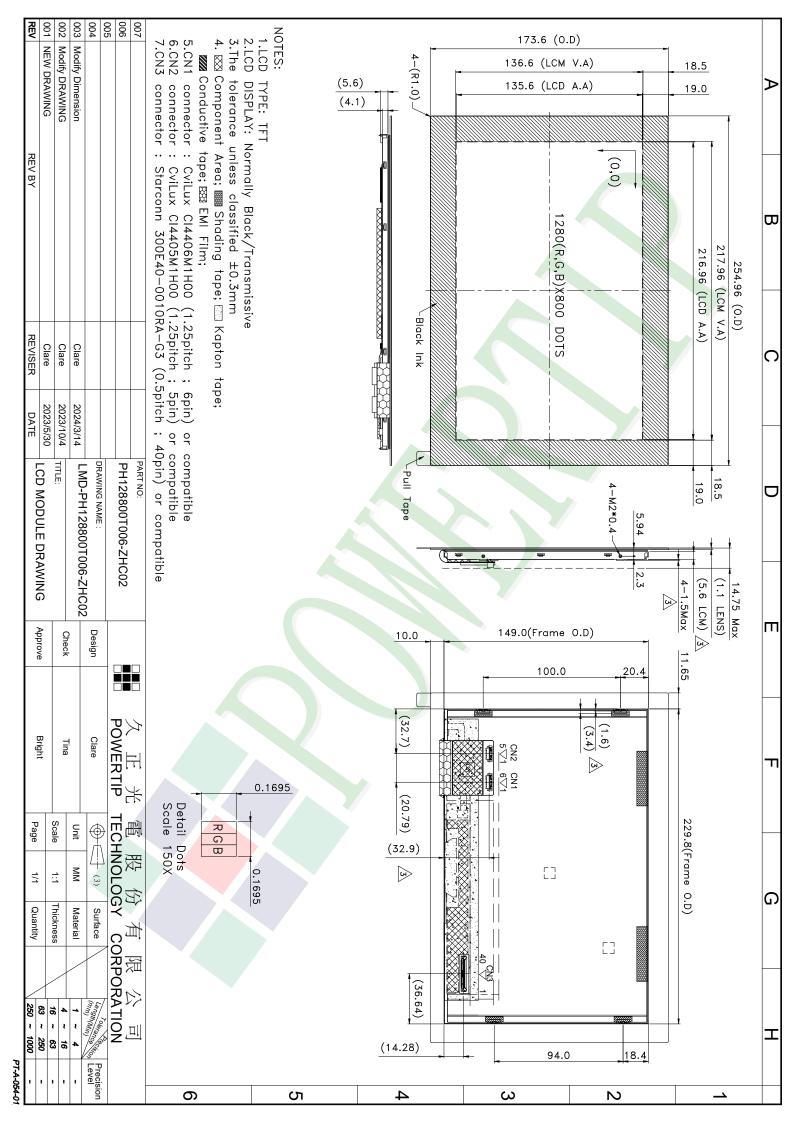
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
 Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Ver.001 Documents NO. PKG-PH128800T006-ZHC02

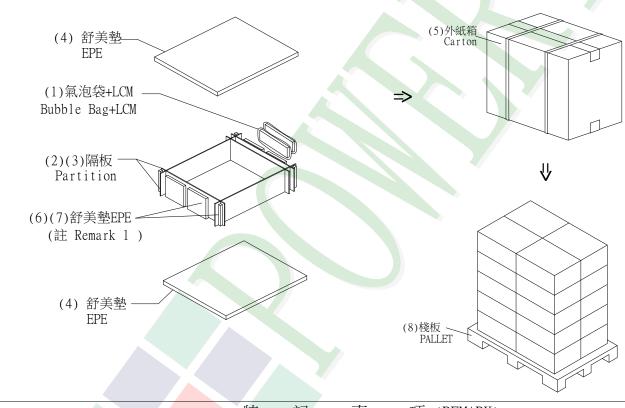
Packaging Specifications

Approve	Check	Contact		
Bright	Tina	Clare		

1.包裝材料規格表(Packaging Material): (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH128800T006-ZHC02	254.96 X 173.6	0.4793	340	162.962
2	氣泡袋(1)Bubble Bag	BAG000000048	330 X 260	0.01	340	3.4
3	A10隔板(2)A10 Partition	BX0000000119	340 X 174 X 7	0.031	360	11.16
4	B10隔板(3)B10 Partition	BX0000000118	500 X 174 X 7	0.037	40	1.48
5	舒美墊(4)EPE	FOAM00000244	500 X 340 X 20	0.036	40	1.44
6	外箱C6(5)Carton	BX0000000120	514 X 354 X 228	0.75	20	15.0
7	舒美墊(6)EPE	FOAM000000245	170 X 130 X 15	0.01	80	0.8
8	舒美墊(7)EPE	FOAM00000246	170 X 23 X 15	0.001	760	0.76
9	棧板(8)PALLET	OTPALLET00007	1200 X 800 X 140	5.5	1	5.5

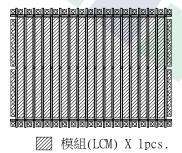
- 2.一 整箱總重量 (Total LCD Weight in carton) : 202.50 Kg±10%
- 3. 單箱數量規格表(Packaging Specifications and Quantity):
 - (1)Quantity Of Spacer: A10隔板 X 18, B10隔板 X 2
 - (2) Total LCM quantity in carton: quantity per box 17 x no of boxes 1 = 17
 - (3) Total LCM quantity in pallet: quantity per carton $\frac{17}{20}$ x no. of cartons $\frac{20}{20}$ = $\frac{340}{20}$



特記事項(REMARK)

of carton

1. LCM排放示意圖(前後間<mark>隔不放置)</mark>: 1. LCM placed as figure show<mark>in</mark>g: (First and last slot should be empty)



- 2.外箱剩餘空間以緩衝材填補 The remaining space of the carton is filled with buffer material
- 3.棧板外箱擺放方式:一層擺放4箱外箱, 共5層,故 4箱 X 5層=20箱外箱 Placement of carton on pallet: 4 boxes of carton on one layer, a total of 5 layers, so 4 boxes X 5 layers = 20 boxes
- 4.外圍加打包帶及外部封塑膠膜 Packing tape and plastic film outside

POWERTIP TECH. CORP.