

COMPLIANT

Standard Recovery Diodes, (Hockey PUK Version), 3800 A



K-PUK (DO-200AC)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	3800 A			
Package	K-PUK (DO-200AC)			
Circuit configuration	Single			

FEATURES

- Wide current range
- High voltage ratings up to 1000 V
- High surge current capabilities
- Diffused junction
- Hockey PUK version
- Case style K-PUK (DO-200AC)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Converters
- Power supplies
- · High power drives
- · Auxiliary system supplies for traction applications

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
		3800	A	
I _{F(AV)}	T _{hs}	55	°C	
I _{F(RMS)}		6230	A	
	T _{hs}	25	°C	
I _{FSM}	50 Hz	35 800	^	
	60 Hz	37 500	A	
l²t	50 Hz	6410	kA ² s	
	60 Hz	5850	KA ² S	
V_{RRM}	Range	400 to 1000	V	
TJ		-40 to +180	°C	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 180 °C mA		
	04	400	500			
VS-SD3000CK	08	800	900	75		
	10	1000	1100			



FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	l=	180° conduction, half sine wave		3800 (1925)	А	
at heatsink temperature	I _{F(AV)}	Double sid	le (single side) o	cooled	55 (85)	°C
Maximum RMS forward current	I _{F(RMS)}	25 °C heat	sink temperatu	re double side cooled	6230	
		t = 10 ms	No voltage	Sinusoidal half wave, initial $T_J = T_J$ maximum	35 800	А
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied		37 500	
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM} reapplied		30 100	
		t = 8.3 ms			31 500	
	l ² t	t = 10 ms	No voltage reapplied		6410	kA ² s
Maximum I ² t for fusing		t = 8.3 ms			5850	
		t = 10 ms	100 % V _{RRM} reapplied		4530	
		t = 8.3 ms			4135	
Maximum I²√t for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied		64 100	kA²√s	
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J = T _J maximum		0.74	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.86	V	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum		0.08	mW	
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			0.07	11100
Maximum forward voltage drop	V _{FM}	$I_{pk} = 6000 \text{ A}, T_J = T_J \text{ maximum}$ $t_p = 10 \text{ ms sinusoidal wave}$		1.22	V	

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating temperature range	TJ		-40 to +180	°C	
Maximum storage temperature range	T _{Stg}		-55 to +200		
Maximum thermal resistance,	R _{thJ-hs}	DC operation single side cooled	0.042	2 K/W	
junction to heatsink		DC operation double side cooled	0.020	IV/VV	
Mounting force, ± 10 %			22 250 (2250)	N (kg)	
Approximate weight			425	g	
Case style	See dimensions - link at the end of datasheet K-PUK (DO-200AC))-200AC)		

△R _{thJ-hs} CONDUCTION						
CONDUCTION ANGLE	SINUSOIDAL C	ONDUCTION	RECTANGULAR CONDUCTION		TEST CONDITIONS	LINITO
CONDUCTION ANGLE	SINGLE SIDE	DOUBLE SIDE	SINGLE SIDE	DOUBLE SIDE	TEST CONDITIONS	UNITS
180°	0.002	0.002	0.001	0.001	T _J = T _J maximum	
120°	0.002	0.002	0.002	0.002		
90°	0.003	0.003	0.003	0.003		K/W
60°	0.004	0.004	0.004	0.004		
30°	0.007	0.007	0.007	0.007		

Note

• The table above shows the increment of thermal resistance R_{thJ-hs} when devices operate at different conduction angles than DC



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180 SD3000C..K Series Heatsink Temperature (°C) (Single Side Cooled) thJ-hs (DC) = 0.042 K/W 140 120 onduction Angle 100 Maxim um Allowable 80 60 180 40 1000 1500 2000 2500 Average Forward Current (A)

Fig. 1 - Current Ratings Characteristics

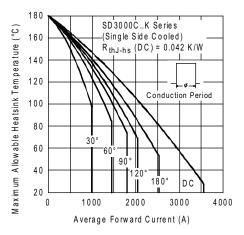


Fig. 2 - Current Ratings Characteristics

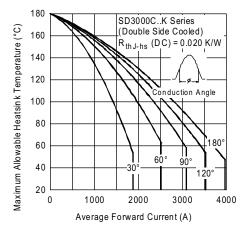


Fig. 3 - Current Ratings Characteristics

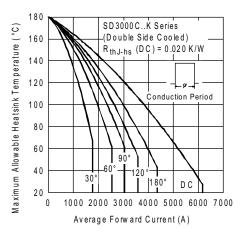


Fig. 4 - Current Ratings Characteristics

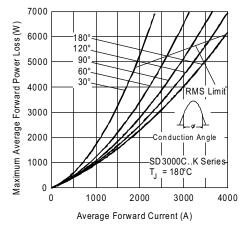


Fig. 5 - Forward Power Loss Characteristics

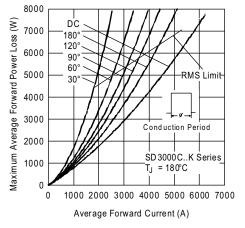


Fig. 6 - Forward Power Loss Characteristics

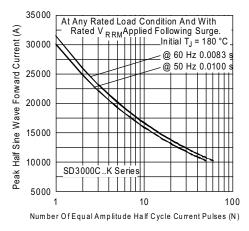


Fig. 7 - Maximum Non-Repetitive Surge Current Single and Double Side Cooled

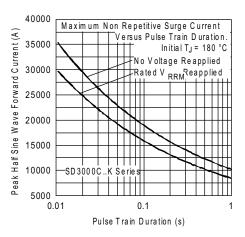


Fig. 8 - Maximum Non-Repetitive Surge Current Single and Double Side Cooled

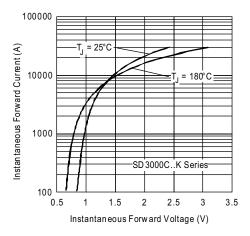


Fig. 9 - Forward Voltage Drop Characteristics

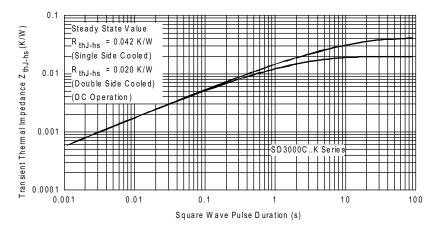
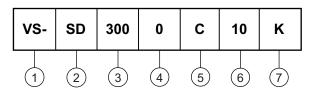


Fig. 10 - Thermal Impedance $Z_{thJ\text{-}hs}$ Characteristics



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Diode

3 - Essential part number

- 0 = standard recovery

5 - C = ceramic PUK

6 - Voltage code x 100 = V_{RRM} (see Voltage Ratings table)

7 - K = PUK case K-PUK (DO-200AC)

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95247			



K-PUK (DO-200AC)

DIMENSIONS in millimeters (inches)

3.5 (0.14) DIA. NOM. x
1.8 (0.07) deep MIN. both ends

Yew Yord (80°1)

1 (0.04) MIN. both ends

2 places

C

Note:
A = Anode
C = Cathode

Quote between upper and lower pole pieces has to be considered after application of mounting force (see Thermal and Mechanical Specifications)



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