SI	PF	CI	FI	CA	IT	<b>O</b>	N	S
UI		VI			<b>1</b> 1 1	v	ıv	J

CUSTOMER : PTC

SAMPLE CODE : SH480272T015-IAB

MASS PRODUCTION CODE : PH480272T015-IAB

SAMPLE VERSION : 01

SPECIFICATIONS EDITION : 003

DRAWING NO. (Ver.) : JLMD-PH480272T015-IAB\_001

PACKAGING NO. (Ver.) : JPKG-PH480272T015-IAB\_002

## **Customer Approved**

Date:

Approved	Checked	Designer
李昀	劉進	陳璐

☐ Preliminary specification for design input

Specification for sample approval

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## **History of Version**

Date	Ver.	Edi.	Description	Page	Design by
06/27/2019	01	001	New Drawing.	-	任健
09/06/2019	01	002	New Sample.	-	任健
12/02/2019	01	003	Modify LCM Packaging	Appendix	陳璐
					4
				<b>→</b>	

Total: 34 Page



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Appendix: 1.LCM Drawing

2.Packaging

Note: For detailed information please refer to IC data sheet: Sitronix --- ST7282-G4



#### 1.1 Features

Item	Standard Value				
Display Type	480 * 3 (RGB) * 272 Dots				
LCD Type	a-si TFT, Normally white, Transmissive Type				
Screen size(inch)	4.3"(Diagonal)				
Viewing Direction	6 O'clock ( Gray scale Inversion )*1				
Viewing Direction	12 O'clock *2				
Color configuration	R,G, B vertical stripe				
Display Interface	Digital 24-bits RGB				
Driver IC	ST7282-G4				
	THIS PRODUCT CONFORMS THE ROHS OF PTC				
ROHS	Detail information please refer web site :				
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/				

<sup>\*1.</sup> For saturated color display content (e.g. pure-red, pure-green, pure-blue or pure-colors -combinations).

## 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W) x 67.2 (L) x 4.0(H)	mm

### LCD panel

Item	Standard Value		
Active Area	95.04 (W) x 53.856 (L)	mm	
Pixel Size	0.198 (W) * 0.198 (H)	mm	

Note: For detailed information please refer to LCM drawing

<sup>\*2. &</sup>quot;For display content based upon multicolor images e.g. photos, RGB defined user interfaces"



## 1.3 Absolute Maximum Ratings

#### Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	$V_{DD}$	GND=0	-0.3	+4.6	V
Input Voltage Range	V <sub>IN</sub>	-	-0.3	V <sub>DD</sub> +0.3	V
Operating Temperature	Top	-	-20	+70	°C
Storage Temperature	T <sub>ST</sub>	-	-30	+80	°C
Storage Humidity	H <sub>D</sub>	Ta ≤ 60 °C	10	90	%RH

## 1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power supply Voltage	$V_{DD}$	-	3.0	3.3	3.6	V
"H" Input Voltage	VIH		0.7*V <sub>DD</sub>	-	$V_{DD}$	V
"L" Input Voltage	VIL	-	GND	-	0.3*V <sub>DD</sub>	V
"H" Output Voltage	Vон	·	V <sub>DD</sub> -0.4	-	$V_{DD}$	V
"L" Output Voltage	Vol	-	GND	-	GND+0.4	V
Supply Current	$I_{DD}$	V <sub>DD</sub> =3.3V	1	20	35	mA



## 1.5 Optical Characteristics

#### **TFT LCD Panel**

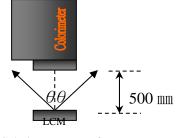
V<sub>DD</sub> =3.3V, Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	
Response time		Tr + Tf	-	-	29	44	ms	Note2
	Тор	θΥ+		-	60	-		
Viewing angle	Bottom	θΥ-	CR ≥ 10	_	60	-	Deg.	Note4
viewing angle	Left	θX-	CIV Z 10	-	60	-	Deg.	NOIE4
	Right	θX+		-	60	-		
Contrast rati	0	CR	-	500	600	-	-	-
	\\/bito	X		0.23	0.28	0.33		
	White	Υ		0.25	0.30	0.35		
	Red	Х		0.55	0.60	0.65		
Color of CIE		Υ	IF= 20 mA	0.30	0.35	0.40		Noto 1
Coordinate (B/L & T/P)	Green	Х	IF- 20 IIIA	0.27	0.32	0.37		Note1
(=====, , ,		Υ		0.50	0.55	0.60		
	Dluc	Х		0.10	0.15	0.20		
	Blue	Υ		0.00	0.05	0.10		
Average Brightness								
Pattern=white display		IV	IF= 20 mA	200	260	-	cd/m <sup>2</sup>	Note1
(B/L & T/P) *1								
Uniformity (B/L & T/P) *	2	△B	IF= 20mA	70	-	-	%	Note1

#### Note1:

- $1 : \triangle B = B(min) / B(max) \times 100\%$
- 2 : Measurement Condition for Optical Characteristics:
  - a : Environment: 25°C±5°C / 60±20%R.H → no wind → dark room below 10 Lux at typical lamp current and typical operating frequency.
  - b : Measurement Distance:  $500 \pm 50 \text{ mm}$  ,  $(\theta = 0^{\circ})$
  - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
  - d : The uncertainty of the C.I.E coordinate measurement  $\pm 0.01$ , Average Brightness  $\pm$  4%





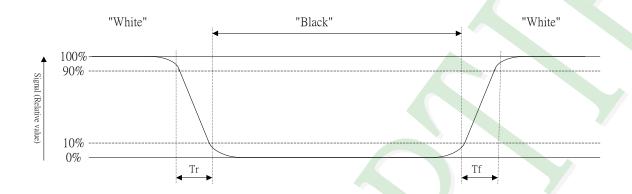
Colorimeter=BM-7 fast



#### Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



#### Note3: Definition of contrast ratio:

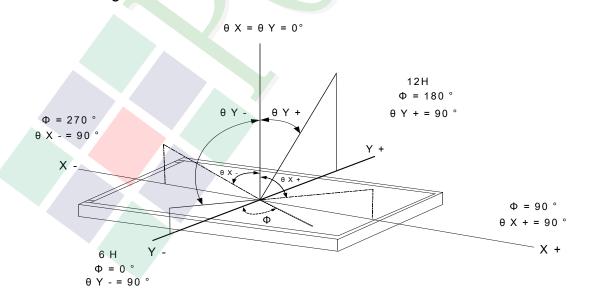
Contrast ratio is calculated with the following formula

Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

# Note4: Definition of viewing angle: Refer to figure as below:





## 1.6 Backlight Characteristics

#### Maximum Ratings

- maximam radingo					
Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25°ℂ		25	mA
(Each one)	II	1a -25 C	_	25	ША
LED Reverse Voltage	VR	Ta =25°ℂ		5	V
(Each one)	VK	1a -25 C	-	ວ	V
Power Dissipation	PD	Ta =25°ℂ	_	595	mW

## **Electrical / Optical Characteristics**

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		19.6	22.4	24.5	V
Average Brightness ((Without LCD &T/P)	IV	IF=20mA	5500	6000	1	cd/m <sup>2</sup>
CIE Color Coordinate	X		0.24	0.27	0.30	
((Without LCD &T/P)	Υ		0.24	0.27	0.30	-
Color			White	7		

<sup>\*1 :</sup> The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and  $I_L$  =20mA. The LED lifetime could be decreased if operating  $I_L$  is larger than 20 mA.

## Internal Circuit Diagram



## Other Description

Item	Conditions	Description
Life Time*1	Ta =25°ℂ	20,000 hrs
Life Time 1	IF= 20mA	20,000 1118



## 1.7 Touch Panel Characteristics

## 1.7.1 Optical Characteristics

Item	Specification
1.Transparency	80% Min

#### 1.7.2 Mechanical Characteristic

Item	Specification
1.Input Method	Finger or stylus pen
2.Hardness of surface	3H -pressure 500g of ,45deg.
3.FPC peeling strength	500gf min(Peeling upward by 90°)
4.Activation Force	50gf~120gf individual point with stylus pen(R0.8)
	Activation force guarantee area: 3.0mm inside of Active Area.
5.Linearity Force	80gf less input with stylus pen(R0.8)
	Activation force guarantee area: 3.0mm inside of Active Area.

#### 1.7.3 Electrical Characteristics

Item	Specification
1.Rated Voltage	DC 5V(DC 7V Max)
2. Resistance Between	Direction X (Glass side): 500Ω~ 1250Ω
Terminals.	Direction Y (Film side): 150Ω~ 450Ω
3.Insulation Resistance	20 MΩ or more (DC 25 V)
4.Linearity	<ul> <li>≤±1.5%.</li> <li>Linearity(%)= ΔV/ (EV-SV) *100.</li> <li>ΔV: The difference between the ideal voltage and measured voltage on the each measuring line.</li> <li>SV: Voltage of starting Points.</li> <li>EV: Voltage of Ending Points.</li> <li>(Test condition refers to 1.7.2 item5)</li> </ul>
5.Bouncing	<10ms



## 1.7.4 Reliability Characteristic

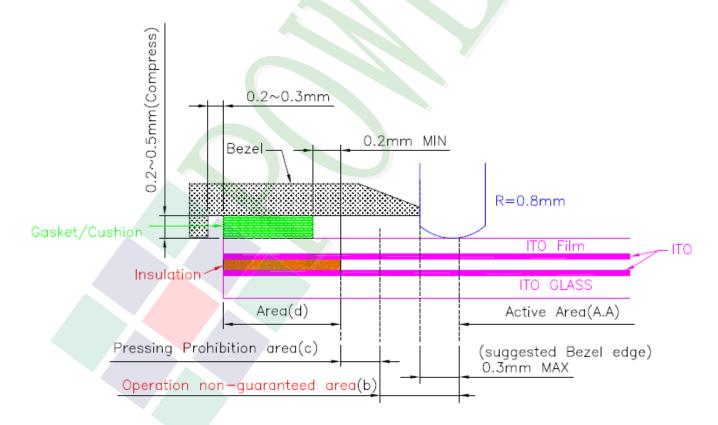
NO	Test Item	Test Condition	Test Result
1	Hitting Durability	1,000,000times min. (R 8 mm Silicon Rubber Hardness 60°250gf 2times/sec).	Follow 1.7.3 item2 and item4.
2	Pen Sliding Durability	100,000 times min (Tip R0.8mm).	Follow 1.7.3 item2 and item4.
3	Impact Resistance	ψ9mm steel ball is dropped on the surface from 30 cm height at 1 time.	No crack
4	Flexible pattern Bending Resistance	Bending 3 times by bending radius R1.0 mm	Follow 1.7.3 item2.
5	Flexible Pattern Insert/Pull Out Resistance	5times at least.	Follow 1.7.3 item2.



#### 1.7.5 Touch Panel Design/Handing Guide

- (1) Keep the gap, for example 0.2 to 0.3mm, between bezel edge and T/P edge.

  The reason is to avoid the bezel edge from contacting T/P surface that may cause "short" with bottom layer
- (2) Insertion a cushion material is recommended.
- (3) The cushion material should be limited on the busbar insulation paste area. If it is over the transparent insulation paste area, a "short" may be occurred.
- (4) Do not to use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- (5) Never expand the T/P top layer (PET Film) like a balloon by internal air pressure. The life of the T/P will extremely decreasing.
- (6) Top layer, PET, dimension is changing base on environment temperature and humidity. Please avoid a stress from housing bezel to top layer, because it may cause "waving".
- (7) The input to the Touch Panel sometimes distorts touch panel itself.
- (8)To use the stylus pen or fingernail sliding at the edge of the housing is prohibited. It would cause the cracking of the ITO coating and damage the touch panel. It also request not to press this area while assembling
- (9) Purpose: In order to prevent accidental use and performance deterioration, please keep the following precautions.



In order to prevent unusual performance degradation and malfunction of a touch panel, please carry out the set case designing and a touch panel assembling method after surely considering the definition of each area illustrated in above figure



### Area(a): Active area

The active area is guaranteed the position data detectable precision, operation force and other operations. it is strongly recommended to place the operation button or menu keys within the active area. Due to structure, the active area is less durable at the edge or close to the edge.

#### Area(b): Operation non-guaranteed area

This area does not guarantee a touch panel operation and its function. When this area is pressed, touch panel shows degradation of its performance and durability such as a pen sliding durability becomes about one-tenth compared with the active area (area-(a) as guaranteed area) and its operation force requires about double. About 0.5 mm outside from a boundary of the active area corresponds to this area.

#### Area(c): Pressing prohibition area

The area which forbids pressing, because an excessive load is applied to a transparent electrode (ITO) and a serious damage is given to a touch panel function by pressing. About 0.5 mm outside from Operation non-guaranteed area.

Area(d): Non-Active area

The area does not activate even if pressed.





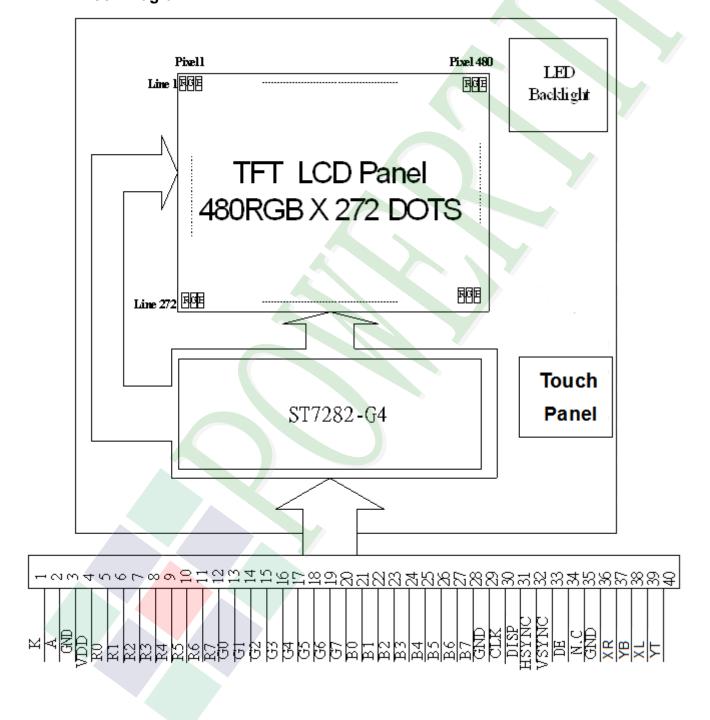
## 2. MODULE STRUCTURE

## 2.1 Counter Drawing

## 2.1.1 LCM Mechanical Diagram

\* See Appendix

## 2.1.2 Block Diagram





## 2.2 Interface Pin Description

Pin No.	Symbol	Function
1	K	Power supply for LED Backlight cathode input
2	Α	Power supply for LED Backlight anode input
3	GND	Ground
4	VDD	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7

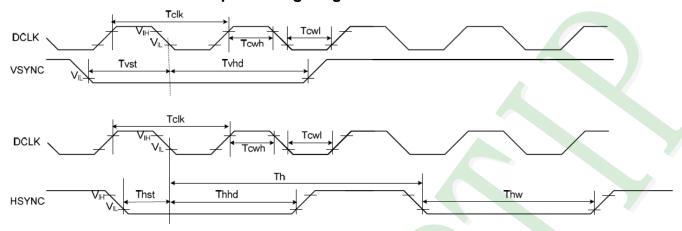


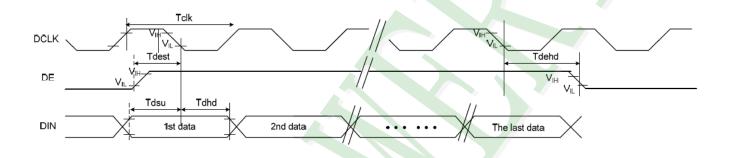
Pin No.	Symbol	Function
21	В0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	В3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	В6	Blue data bit 6
28	В7	Blue data bit 7
29	GND	Ground
30	CLK	Dot data clock
31	DISP	Display control / standby mode selection "High" : Normal display
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data input enable. Active High to enable the data input
35	N.C	Not Connect
36	GND	Ground
37	XR	Right side of touch panel
38	YB	Bottom side of touch panel
39	XL	Left side of touch panel
40	YT	Up side of touch panel



## 2.3 Timing Characteristics

## 2.3.1 Clock and Data Input Timing Diagram



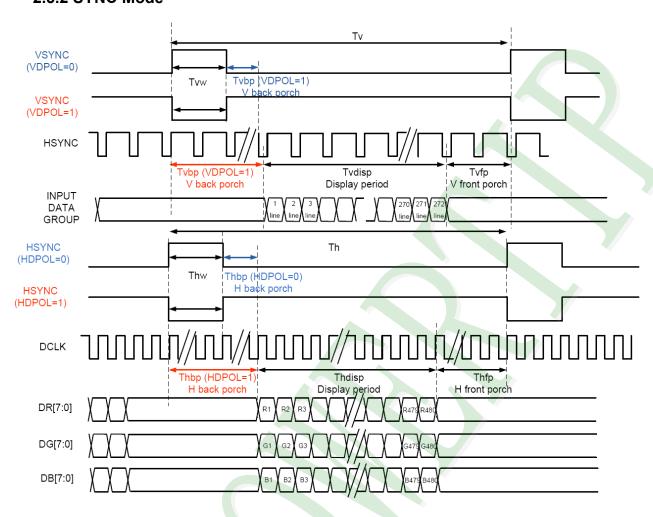




Parameters	Cumbal		Spec		Unit	Conditions			
Parameters	Symbol	Min.	Тур.	Max.	Offic	Conditions			
System operation timing									
VDD power source slew time	Tpor	-	1	20	ms	From 0V to 99% VDD			
GRB pulse width	<b>t</b> RSTw	10	50	1	us	R=10Kohm, C=1uF			
	I	nput/ C	Output	timing					
CLK pulse duty	Tcw	40	50	60	%	-			
Hsync width	Thw	2	-	-	DCLK	-			
HSYNC period	Th	55	60	65	us	-			
VSYNC setup time	Tvst	12	-	-	ns	-			
VSYNC hold time	Tvhd	12	-	-	ns	-			
HSYNC setup time	Thst	12			ns	-			
HSYNC hold time	Thhd	12	-	-	ns	_			
Data setup time	Tdsu	12	-	-	ns	-			
Data hold time	Tdhd	12	1	1	ns	-			
DE setup time	Tdest	10	-	-	ns	-			
DE hold time	Tdehd	10	-	-	ns	-			
SD output stable time	Tst	-	-	12	us	Output settled within +20mV Loading=.6.8k+28.2pF			
GD output rise and fall time	Tgst	-	-	6	us	Output settled (5%~95%), Loading =4.7k+29.8pF			

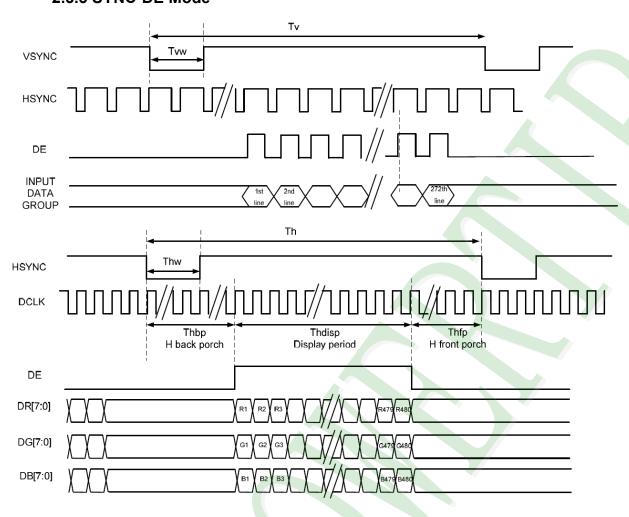


#### 2.3.2 SYNC Mode



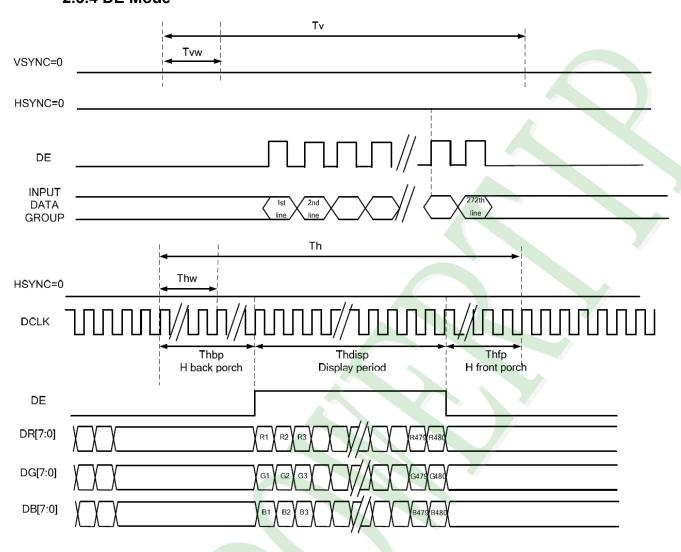


#### 2.3.3 SYNC-DE Mode





#### 2.3.4 DE Mode





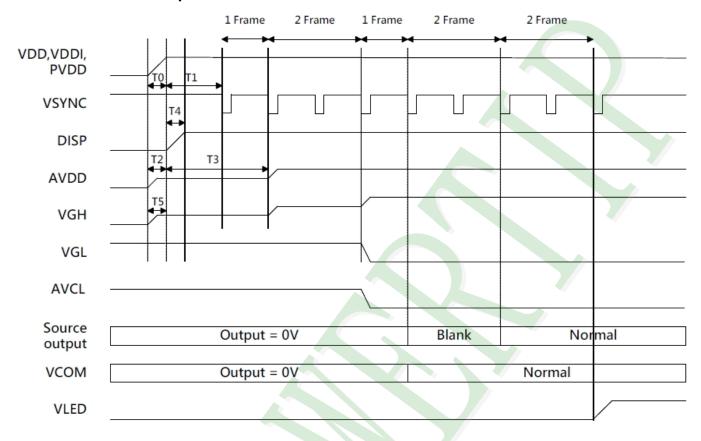
Parallel 24-bit RGB Input Timing Table

Doror	meters	Cymbol		Unit		
Parar	Symbol	Min.	Тур.	Max.	Offic	
DCLK frequency	Fclk	8	9	12	MHz	
DCLK Period		Tclk	83	111	125	nS
	Period Time	Th	485	531	598	DCLK
	Display Period	Thdisp	-	480	-	DCLK
HSYNC	Back Porch	Thbp	3	43	43	DCLK
	Front Porch	Thfp	2	8	75	DCLK
	Pulse Width	Thw	2	4	75	DCLK
	Period Time	Tvdisp	276	292	321	Н
	Display Period	Tvbp	-	272	<u>-</u>	Н
VSYNC	Back Porch	Tvfp	2	12	12	Н
	Front Porch	Tvw	2	8	37	Н
	Pulse Width	Tvdisp	2	4	37	Н



## 2.4 POWER ON/OFF SEQUENCE

#### 2.4.1 Power On Sequence

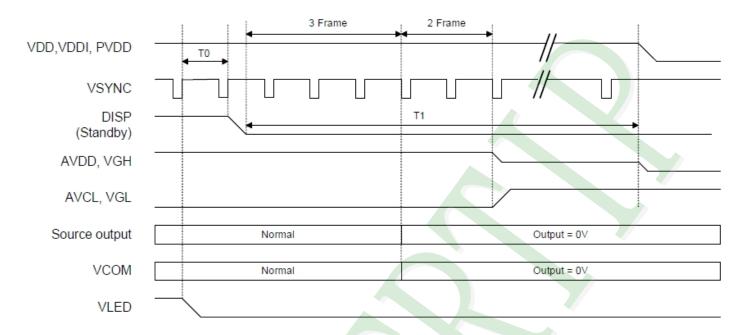


Symbol	Description	Min. Time
T0	Determined by the external power	-
T1	Time from stable VDD, VDDI, PVDD set-up to the first VSYNC	T1=0
T2	Time from AVDD=0V to AVDD=3.3V	T2=T0
Т3	me from AVDD=3.3V to AVDD=6.0V	T3=T1+ (1*Frame)
T4	Time from stable VDD, VDDI, PVDD set-up to DISP asserted	T4=0
T5	Time from VGH=0V to VGH=3.3V	T5=T0

Note: Recommend the LCM power on rise time T0= 0~ 1ms.



## 2.4.2 Power Off Sequence



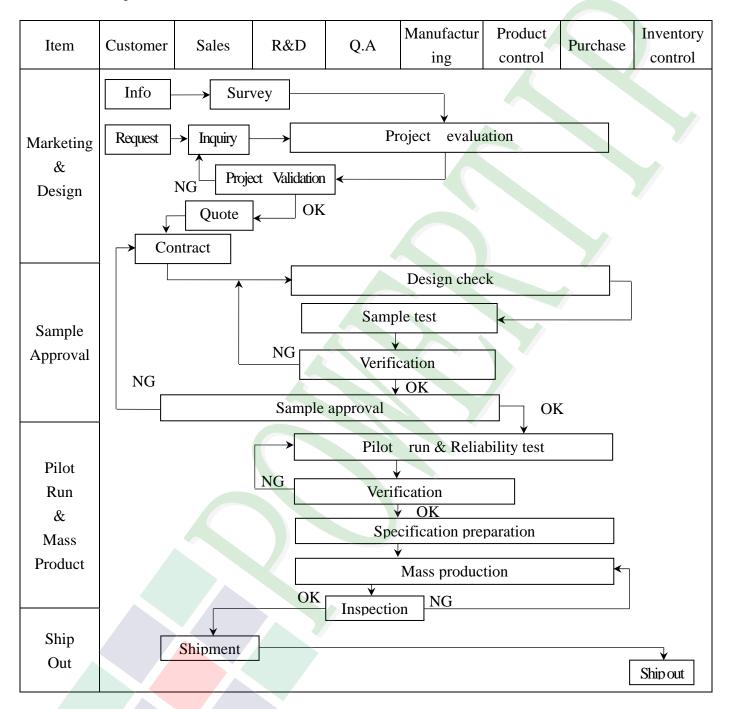
Symbol	Description	Min. Time
T0	Time from backlight power off to DISP="L"	1*Frame
T1	Time from DISP="L" to LCM Power off	5*Frame



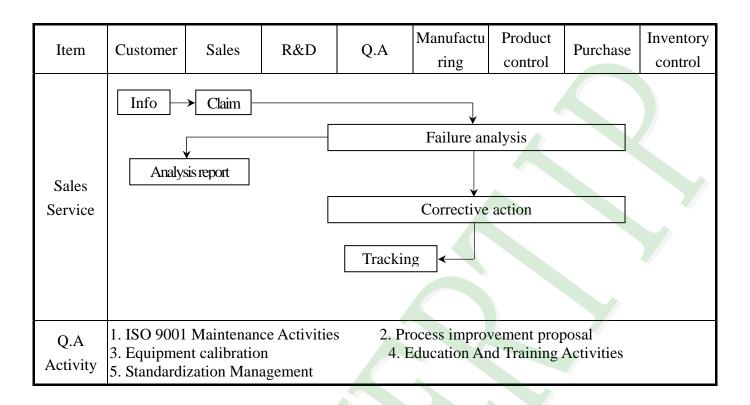


## 3. QUALITY ASSURANCE SYSTEM

## 3.1 Quality Assurance Flow Chart









### 3.2 Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5" ~15" (Ver.B01).

♦ Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.

**◆**Equipment : Gauge · MIL-STD · Powertip Tester · Sample

◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

**♦**OUT Going Defect Level: Sampling.

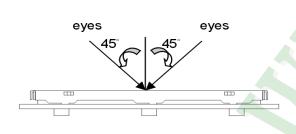
**♦**Standard of the product appearance test:

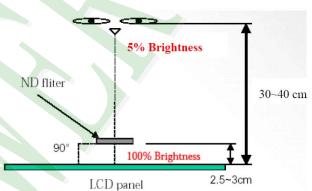
#### a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)

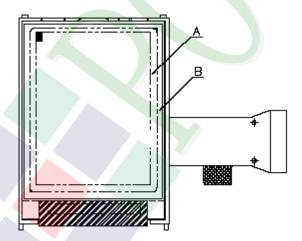
, and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.





(3). Definition of area.



A area: viewing area

**B** area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



## ◆Specification For TFT-LCD Module 3.5″ ~15″:

NO	Item	Criterion	Level							
		1.1The part number is inconsistent with work order of production.								
01	Product condition	1.2 Mixed product types.								
		1.3 Assembled in inverse direction.	Major							
02	Quantity	2.1The quantity is inconsistent with work order of production.								
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major							
		4.1 Missing line character and icon.	Major							
0.4	Electrical Testing	4.2 No function or no display.	Major							
		4.3 Display malfunction.								
04		4.4 LCD viewing angle defect.								
		4.5 Current consumption exceeds product specifications.								
		4. 6Mura can not be seen through 5% ND filter at 50% Gray screen, should be judged by the viewing angle of 90 degree.								
		House Assertance (O2tv)								
		$\begin{array}{c c} Item & Acceptance (Q'ty) \\ \hline Bright Dot & \leq 4 \end{array}$								
		Dork Dot < 5								
	Dot defect	$\begin{array}{c cccc} Dot & & \equiv & 3 \\ Defect & & Joint Dot & & \leq & 3 \end{array}$								
05	(Bright dot \ Dark dot)		Minor							
		$  Total   \leq 7$								
	On -display	5.1 Inspection pattern: full white, full black, Red, Green and								
		blue screens.  5.2 It is defined as dot defect if defect area > 1/2 dot.								
		5.3 The distance between two dot defect ≥5 mm.								
		5.4 Bright dot that can not be seen through 5% ND filter.								



## ◆Specification For TFT-LCD Module 3. 5″ ~15″:

<b>→</b> Phe	THE TOTAL TOTAL	I-DCD I	viouale 0. c	, 10 ,						(ver.bur)		
NO	Item	Criterion							Level			
	Black or white dot \ scratch \ contamination	6. 1 Ro	Dimensio	Non-displa on (diamete $\Phi \leq 0.$ $< \Phi \leq 0.$ $\Phi > 0$	r: Ф) 25 50	Accepta A area Ignore 5	nce (Q'ty) B area					
	Round type		Total			5						
	Round type $\begin{array}{c c}  & X & & \\  & Y & \\ \hline  & Y & \\ \end{array}$	6. 2 Li	ne type( No	on-display (	or displ	ay):						
06	Y	mo	dule size	Length (L)	W	width (W)	Acceptanc A area	e (Q'ty) B area		Minoi		
00	$\Phi = (x+y)/2$					W ≤ 0.03	Ignore			IVIIIIU		
				L ≤10.0	0.03	$<$ W $\leq 0.05$	4					
		3 5"	to less 9"	L <b>≦</b> 5.0	0.05	$<$ W $\leq$ 0.10	2	Ignore				
			olo to less y		W > 0.10 As round	ignore	Ignore					
							type					
				Total			5					
					0.05	$W \le 0.05$	Ignore					
	L	0,,	15"	L ≤10.0	0.05	$<$ W $\leq$ 0.10	5	т				
		9" to	o 15"			W > 0.10	As round type	Ignore				
								Tota	l	5		
							<u> </u>					
						Agganta	naa (O?tv)					
		1	Dimension	(diameter :	Φ)	Accepta A area	nce (Q'ty) B are	29				
				$\Phi \leq 0.25$	I	gnore	Dur					
07	Polarizer		0.25 <	$\Phi \leq 0.50$		4				Mino		
	Bubble		0.50 <	$\Phi \le 0.80$		1	Ignore					
				Φ > 0.80	0	0						
			7	<b>Total</b>		5						



## ◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion				
		Symbols:  X: The length of crack Z: The thickness of crack t: The thickness of glass  X: The width of crack W: terminal length a: LCD side length				
		<ul><li>8. 1 General glass chip:</li><li>8. 1. 1 Chip on panel surface and crack between panels:</li></ul>				
08	The crack of glass	SP SP [NG]	Minor			
	Seal width Z					
		X Y Z				
		$\leq a \qquad \begin{array}{c} \text{Crack can't enter} \\ \text{viewing area} \end{array} \qquad \leq 1/2 \text{ t}$				
		$\leq$ a Crack can't exceed the half of SP width. 1/2 t < Z $\leq$ 2 t				



## ◆Specification For TFT-LCD Module 3. 5″~15″:

NO	Item	Criterion				Level
		Symbols:  X: The leng Z: The thic t: The thic	e width of crack. minal length D side length			
		8.1.2 Corn				
		X	Y		Z	
		≤1/5 a	Crack can't e		Z ≤ 1/2 t	
		≤1/5 a	Crack can't exce		$t < Z \leq 2 t$	
00						Minor
08	The crack of glass	8.2 Protrusion over terminal:				
	8. 2. 1 Chip on electrode pad:					
W						
			X	Y	Z	
		Front	$\leq a$	≤ 1/2 W	<b>≦</b> t	
		Back	$\leq a$	≤ W	$\leq 1/2 t$	



## ◆Specification For TFT-LCD Module 3. 5″ ~15″:



## ◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	10. 2 No short circuits in components on PC  10. 3 Parts on PCB or FPC must be production characteristic chart .The parts, missing parts or excess parts.  General appearance  10. 4 Product packaging must the same as a specification sheet.  10. 5 The folding and peeled off in polarize	10. 1 Pin type \ quantity \ dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts, missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is $\leq 1.5$ mm.	Minor



## 4. RELIABILITY TEST

## 4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION				
1	High Temperature Storage Test	Keep in +80 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.				
2	Low Temperature Storage Test	Keep in −30 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.				
3	High Temperature / High Humidity Storage Test	Keep in +60 °C / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)				
4	Temperature Cycling Storage Test	$-30^{\circ}\mathbb{C} \rightarrow +25^{\circ}\mathbb{C} \rightarrow +80^{\circ}\mathbb{C} \rightarrow +25^{\circ}\mathbb{C}$ $(30^{\text{mins}})  (5^{\text{mins}})  (5^{\text{mins}})$ $20 \text{ Cycle}$ Surrounding temperature, then storage at normal condition 4hrs.				
5	ESD Test	2. Humidity rela 3. Energy Storag 4. Discharge Res 5. Discharge, mo	Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/- ambiance: 15°C ~35°C ative: 30%~60% ge Capacitance(Cs+Cd): 150pF±10% sistance(Rd): 330 Ω±10% ode of operation: successive discharges at least 1 sec) indication: ±5%)			
6	Vibration Test (Packaged)	<ol> <li>Sine wave 10~55 Hz frequency (1 min/sweep)</li> <li>The amplitude of vibration :1. 5 mm</li> <li>Each direction (X \ Y \ Z) duration for 2 Hrs</li> </ol>				
7	Drop Test (Packaged)	Packing Weight (K)  0 ~ 45. 4  45. 4 ~ 90. 8  90. 8 ~ 454  Over 454  Drop Direction: %1 corner / 3 ed	122 76 61 46			



## 5. PRECAUTION RELATING PRODUCT HANDLING

#### **5.1 SAFETY**

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—when working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution! (LCM products with Capacitive Touch Panel)
  Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
  - Therefore, the touch needs to be thoroughly tested inside the target application.

#### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}$ C  $\pm$   $5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

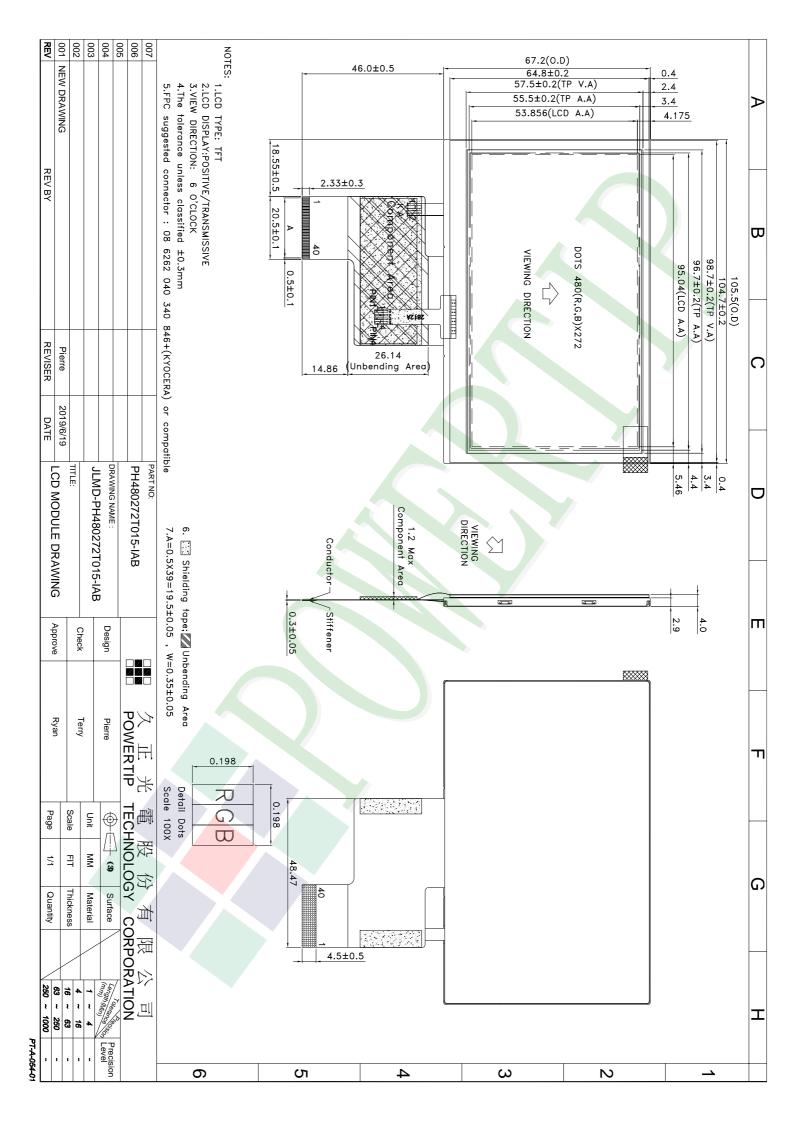
#### **5.4 TERMS OF WARRANTY**

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Ver.002 I CM 与毕坦校士					Approve	Check	Contact
					_		
Documents NO. JPKG-PH480272T015-IAB LCM Packaging Specifications			Terry	Air	Pierre		
	<u>'</u>	— (Fo	r Tray)				
1.包	卫裝材料規格表 (Packaging M	aterial): (per carton)					
No.	Item	Model	Dimensions (mm)	1Pcs V	Weight	Quantity	Total Weight
1	成品 (LCM)	PH480272T015-IAB	105.5 X 67.2X 4.0	0.0	59	120	7.08
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015		-	6	
3	TRAY 盤 (2)Tray	TYSG000000202	352 X 260 X 13.05	0.9	358	36	3.369
4	内盒(3)Product Box	BX36627063ABBA	383 X 270 X 66	0.1	182	6	1.092
5	保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284		2	0.0568
6	外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.0	1.0		1.0
7							
8							
9							
	整箱總重量 (Total LCD Weight		10%				
	箱數量規格表 (Packaging Specifi CM quantity per box : no per tray	ications and Quantity): 4	x no of tray	5		= 20	
	otal LCM quantity in carton: quant	•	x no of boxes	6		= 20	
\		20				120	
			(4)(	呆利龍	板		
Use	empty tray 空盤		Poly	ylon bo	ard	< \	_
:	工盤						$\rightarrow$
		(1)多層薄膜————————————————————————————————————			[a_	$\gg$	
		FOI					
	+						
Dut	products into the tray						
Put.	products into the tray	(2)TRAY 盤 ——	(4)	保利龍	板 >>>		
		Tray		保利龍 ylon bo	oard	$\rightarrow$	
			₩			₩	
						V	
							_
	₩	/ī					
		(3)内盒					$\mathcal{A}$
Tra	y stacking	Product Box					
,							
_			17.21	, e.e			
]	B/		(5)外 Car				
Carton							
特 記 事 項 (REMARK)							
1/1 □L → △G (INTIMULY)							
A 斜角 Detail B							
							Tray 2
圓角 Tray 1							
	AY盤相疊時,需旋轉180度,請詳見B視						
Rotate tray 180 degrees and place on top of stack.  Check the tray stack using Fig. B.							
_ cne	ck are may stack using Fig. D.						