

SPECIFICATIONS

CUSTOMER	:	CBE005
SAMPLE CODE	:	SH128800T005-ZFC
MASS PRODUCTION CODE	:	PH128800T005-ZFC
SAMPLE VERSION	:	02
SPECIFICATIONS EDITION	:	004
DRAWING NO. (Ver.)	:	LMD-PH128800T005-ZFC (Ver.001)
PACKAGING NO. (Ver.)	:	PKG-PH128800T005-ZFC (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
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- ☐ Preliminary specification for design input
- ☒ Specification for sample approval



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PH128800T005-ZFC

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1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Screen size(inch)	12.1(Diagonal)
Resolution	1280* (R 、 G 、 B) * 800 Dots
Display mode	Full Viewing Angle, Normally Black
Touch panel	Projective Capacitive Touch Panel 10 Points touch
Polarizer Surface treatment	AG type
Color arrangement	RGB-stripe
Interface	LVDS(6Bit / 8Bit)
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	305.0(W) * 200.0 (L) * 14.05 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	261.12 (W) * 163.2 (L)	mm

Note : For detailed information please refer to LCM drawing.

1.3 Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	VCC	-0.3	+4.0	V	-
Operating Temperature	T _{OP}	-20	+70	°C	(1)
Storage Temperature	T _{ST}	-30	+80	°C	(1)

Note 1:

(a) 90%RH Max. (Ta≤39°C)

(b) Wet-bulb temperature should be 39°C Max.

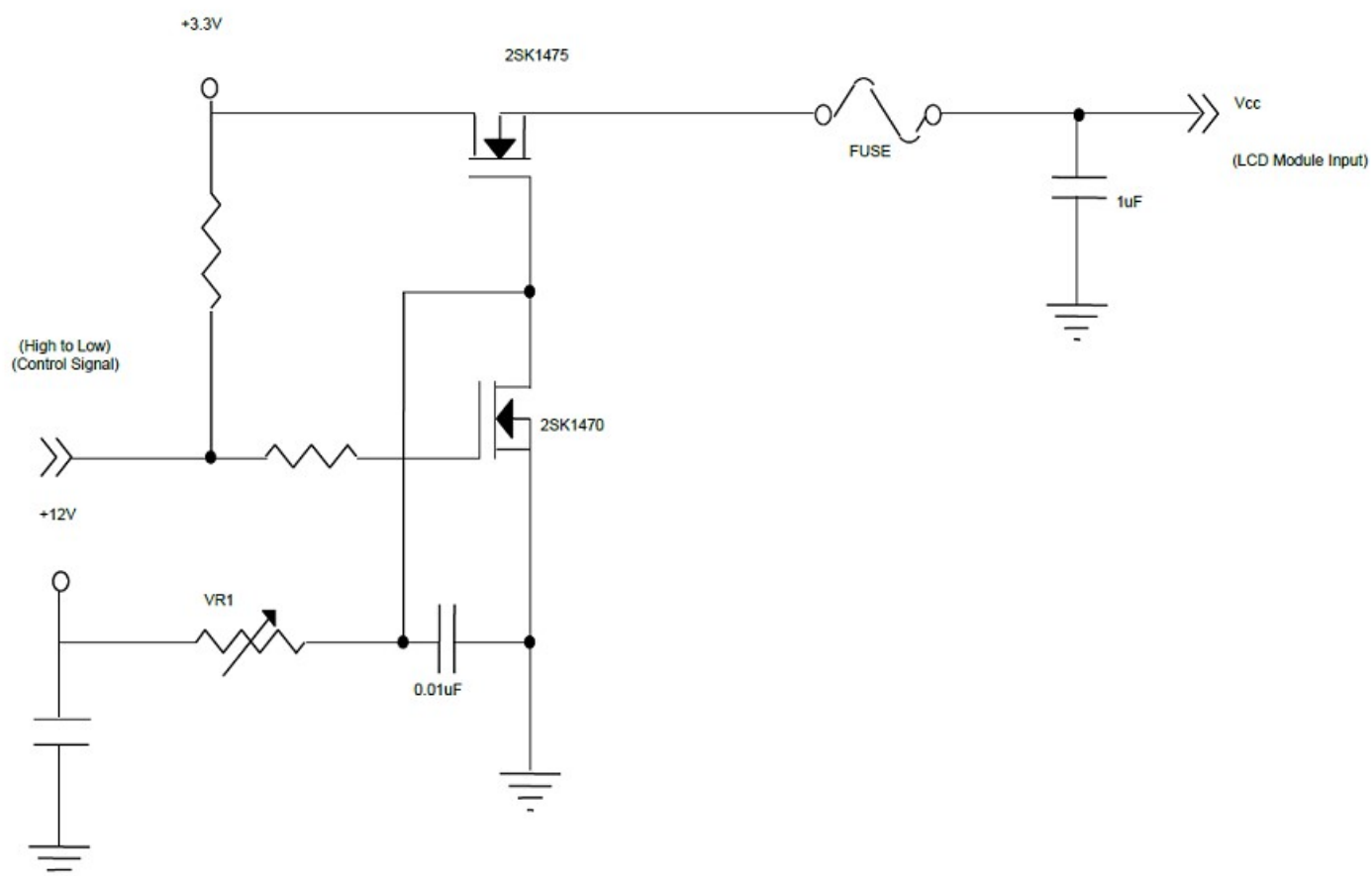
(c) No condensation.

1.4 DC Electrical Characteristics

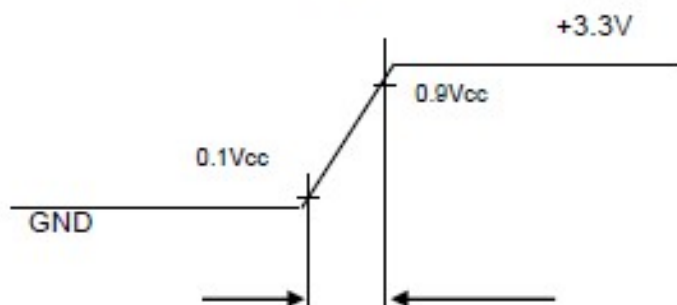
Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		V _{CC}	3.0	3.3	3.6	V	-
Ripple Voltage		V _{RP}	-	50	-	mVp-p	
Inrush Current		I _{INRUSH}	1.5			A	(2)
Power Supply Current	White	I _{CC}	-	560	675	mA	(3)a
	Black		-	360	430	mA	(3)b
LVDS differential input voltage		V _{id}	100	-	600	mV	
LVDS common input voltage		V _{ic}	1.125	1.2	1.375	V	
Differential Input Voltage for LVDS Receiver Threshold	“H” Level	V _{IH}	100	-	-	mV	-
	“L” Level	V _{IL}	-	-	-100	mV	-
Terminating Resistor		R _T	-	100	-	Ohm	-

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:



VCC rising time is 470us



1.5 Optical Characteristics

TFT LCD Panel

Ta=25°C

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	-
Response time		Tr	-	-	12	17	ms	Note2
		Tf		-	8	13		
Viewing angle	Top	ΘY+	CR ≥ 10	80	85	-	Deg.	Note4
	Bottom	ΘY-		80	85	-		
	Left	ΘX-		80	85	-		
	Right	ΘX+		80	85	-		
Contrast ratio		CR	-	800	1000	-	-	Note3
Color of CIE Coordinate (With B/L& Touch Panel)	White	X		0.263	0.313	0.363	-	Note1
		Y		0.279	0.329	0.379		
	Red	X		0.602	0.652	0.702		
		Y		0.288	0.338	0.385		
	Green	X		0.276	0.326	0.376		
		Y		0.558	0.608	0.658		
	Blue	X		0.100	0.150	0.200		
		Y		0.003	0.053	0.103		
Average Brightness Pattern=white display		IV		-	400	500	-	cd/m2
Luminance uniformity		YU	-	75	-	-	%	Note1

Note1:

1 : $\Delta B = B(\min) / B(\max) \times 100\%$

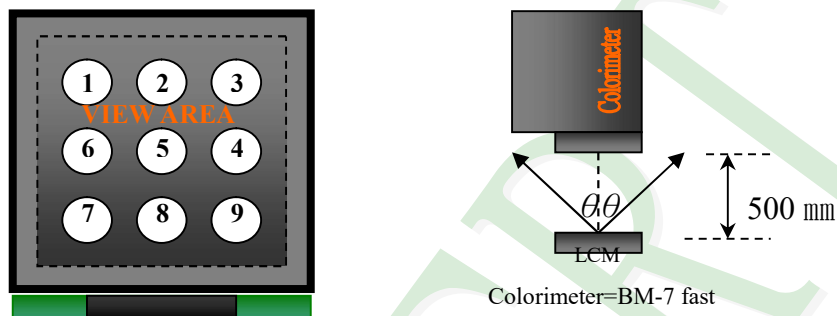
2 : Measurement Condition for Optical Characteristics:

a : Environment: $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ / $60 \pm 20\% \text{R.H}$, no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: $500 \pm 50 \text{ mm}$, ($\theta = 0^{\circ}$)

c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.

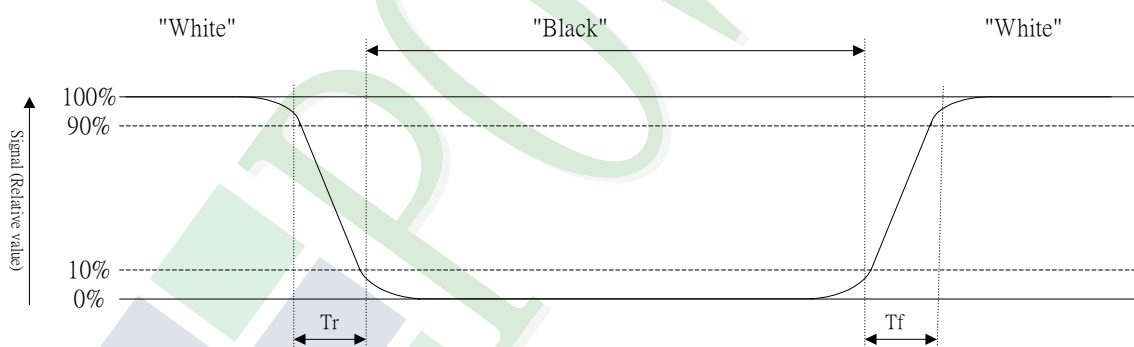
d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

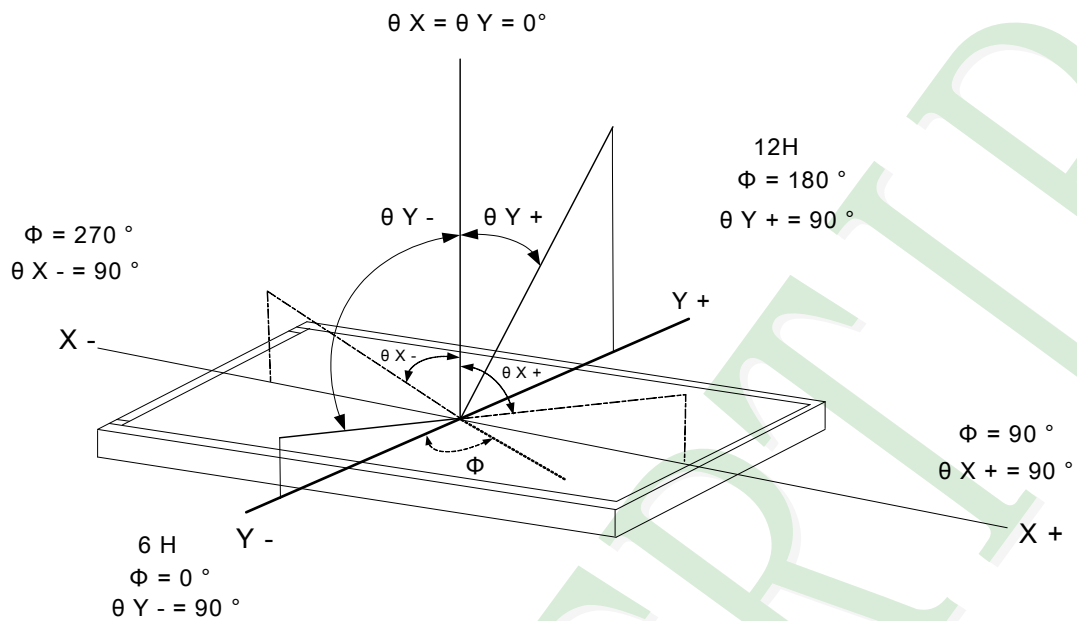


Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

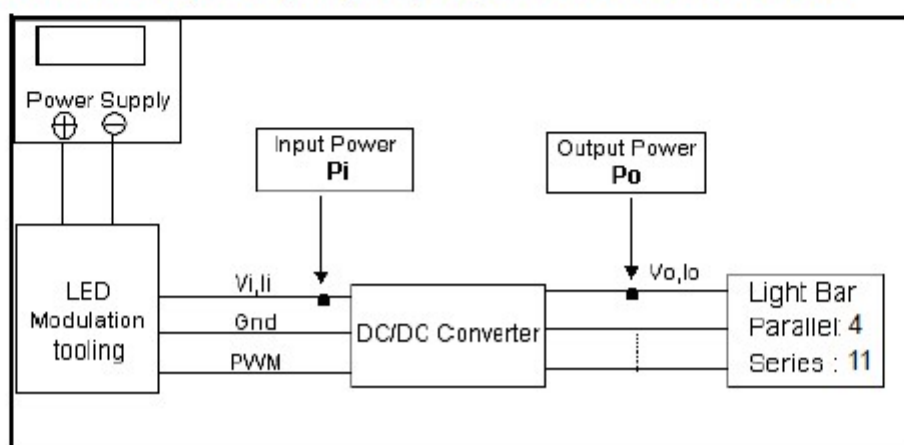
Note4: Definition of viewing angle:
Refer to figure as below:



1.6 Backlight Characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Converter Input Voltage		V_i	10.8	12.0	13.2	V_{DC}	(Duty 100%)
Converter Input Ripple Voltage		V_{IRP}	-	-	350	mV	
Converter Input Current		I_i	-	0.8	1.0	A_{DC}	@ $V_i = 12V$ (Duty 100%)
Converter Inrush Current		I_{IRUSH}	-	-	3.0	A	@ V_i rising time=20ms ($V_i=12V$)
Input Power Consumption		P_i	-	9.6	12	W	(1)
EN Control Level	Backlight on	ENLED (BLON)	2.5	3.3	5.0	V	
	Backlight off		0	-	0.3	V	
PWM Control Level	PWM High Level	Dimming (E_PWM)	2.5	-	5.0	V	
	PWM Low Level		0	-	0.15	V	
PWN Noise Range		V_{Noise}	-	-	0.1	V	
PWM Control Frequency		f_{PWM}	190	200	20k	Hz	(2)
PWM Dimming Control Duty Ratio		-	5	-	100	%	(2), Suggestion @ 190Hz < f_{PWM} < 1kHz
			20	-	100	%	(2), @ 1kHz $\leq f_{PWM}$ < 20kHz
LED Life Time		L_{LED}	50,000		-	Hrs	(3)

Note (1) LED current is measured by utilizing a high frequency current meter as shown below:



Note (2) At 190 ~1kHz PWM control frequency, duty ratio range is restricted from 5% to 100%.

1K ~20kHz PWM control frequency, duty ratio range is restricted from 20% to 100%.

If PWM control frequency is applied in the range from 1KHz to 20KHZ, The “non-linear” phenomenon on the Backlight Unit may be found. So It’s a suggestion that PWM control frequency should be less than 1KHz.

Note (3) The lifetime of LED is estimated data and defined as the time when it continues to operate under the conditions at $T_a = 25 \pm 2^\circ C$ and Duty 100% until the brightness becomes $\leq 50\%$ of its original value. Operating LED at high temperature condition will reduce life time and lead to color shift.

1.7 Touch Panel Characteristics

Features

Item	Standard Value
Touch Panel Size	12.1
Touch type	Projective capacitive touch panel
Input Method	Finger / 10 Points touch
Output Interface	USB
Touch IC	Atmel#mXT1066T2
I ² C Address	0x4A

I²C Address

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0	1	0	0	1	0	1	R/W

Bit 0: 0 for Write / 1 for Read

Mechanical Specifications

Item	Standard Value	Unit
Viewing Area	262.6 (W) * 164.7 (L)	mm

Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Supply voltage	VBUS	-	-0.3	+6.0	V
Operating Temperature	T _{OP}	-	-20	+70	°C
Storage Temperature	T _{ST}	-	-30	+80	°C

DC Electrical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage	VBUS	-	-	5.0	-	V

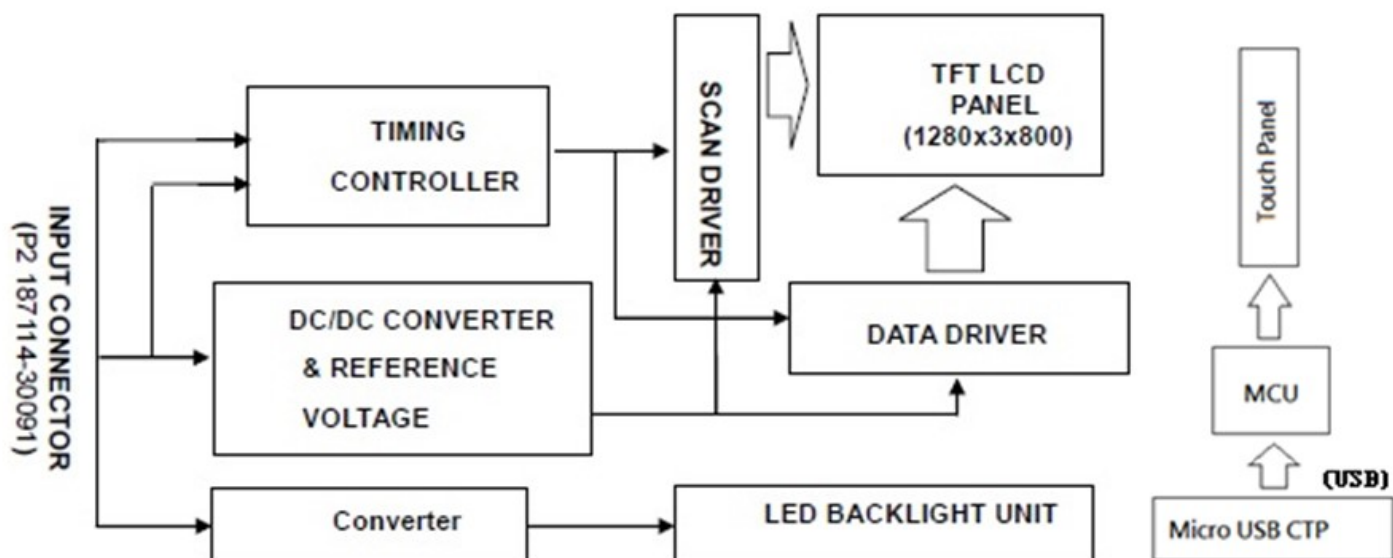
Touch Panel IC Read/Write description & Register Mapping

Reference: Atmel Touch Driver Porting Reference Guide.

2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram



2.2 Interface Pin Description

P2:

Pin No.	Symbol	Description
1	Vi	LED power.
2	Vi	LED power.
3	Vi	LED power.
4	Vi	LED power.
5	ENLED(BLON)	Enable pin. Shown below.
6	Dimming(E_PWM)	Backlight Adjust. Shown below.
7	NC	No Connection or Ground.
8	NC	No Connection or Ground.
9	VCC	Power supply: +3.3V.
10	VCC	Power supply: +3.3V.
11	GND	Ground.
12	GND	Ground.
13	RX0-	Negative transmission data of pixel 0.
14	RX0+	Positive transmission data of pixel 0.
15	GND	Ground.
16	RX1-	Negative transmission data of pixel 1.
17	RX1+	Positive transmission data of pixel 1.
18	GND	Ground.
19	RX2-	Negative transmission data of pixel 2.
20	RX2+	Positive transmission data of pixel 2.
21	GND	Ground.
22	RXC-	Negative of clock.
23	RXC+	Positive of clock.
24	GND	Ground.
25	RX3-	Negative transmission data of pixel 3.

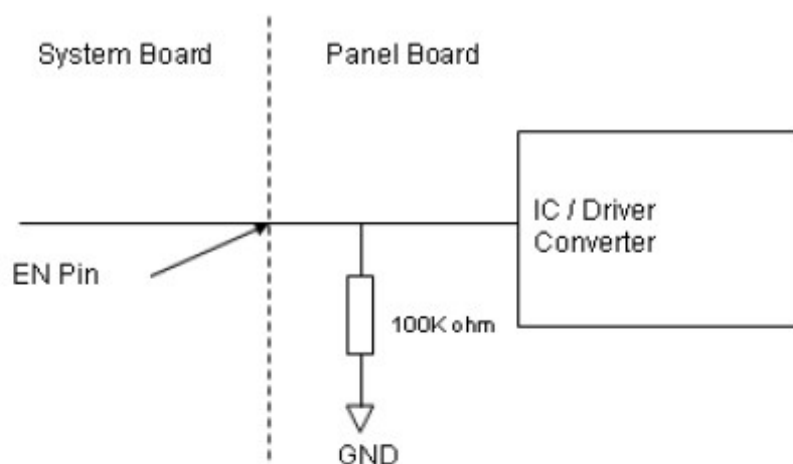
Pin No.	Symbol	Description
26	RX3+	Positive transmission data of pixel 3.
27	GND	Ground.
28	SEL 6/8	LVDS 6/8 bit select function control. Low → 6bit Input Mode. High → 8bit Input Mode. “Low” stands for 0V.”High stands for 3.3V
29	GND	Ground.
30	NC	No Connection or Ground.

Note: ENLED (BLON), Dimming (E_PWM), SEL 6/8 as shown below:

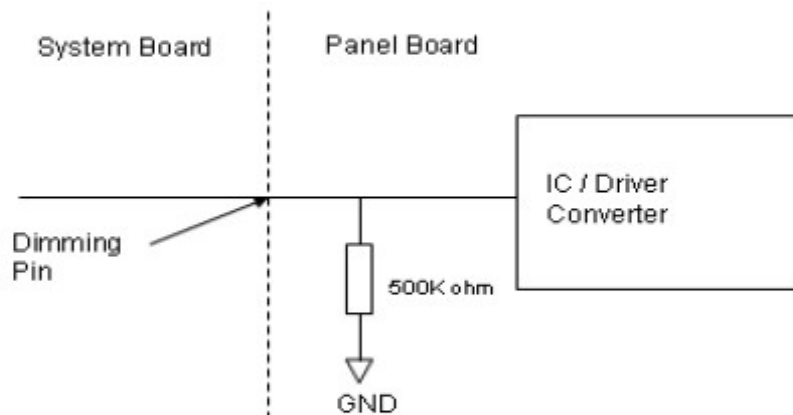
USB1: USB Capacitive Touch Panel Interface)

Pin#	Name	Description
1	VBUS	4.4V-5.25V
2	D-	Data-
3	D+	Data+
4	ID	No connection
5	GND	Power Ground.

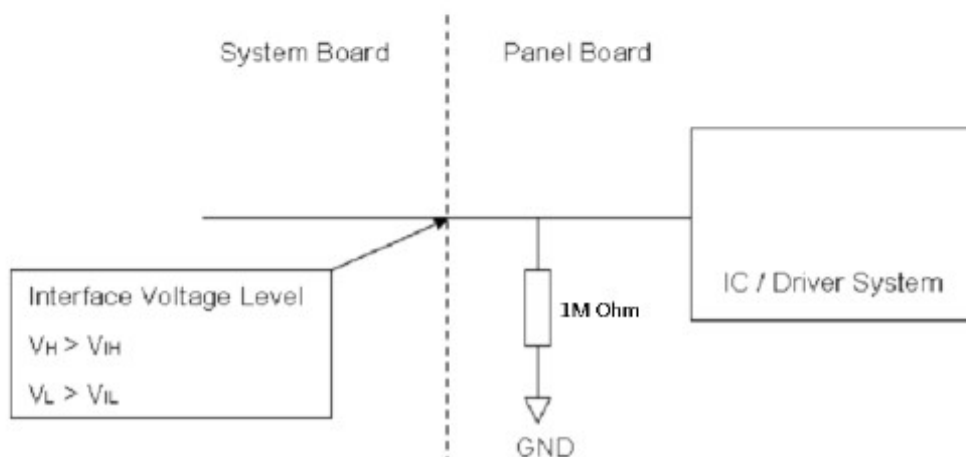
ENLED (BLON)



Dimming (E_PWM)



SEL(6/8)



2.3 Timing Characteristics

2.3.1 Color Data Input Assignment

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color.

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color.



Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note: 0: Low Level Voltage, 1: High Level Voltage

2.3.2 Display Timing Specifications

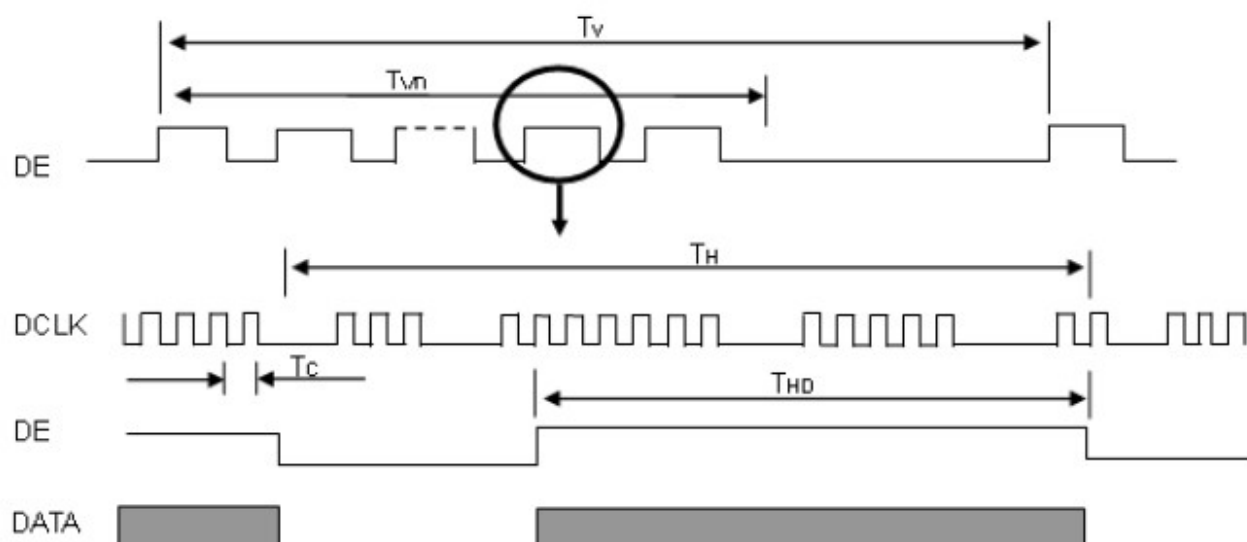
The input signal timing specifications are shown as the following table and timing diagram.

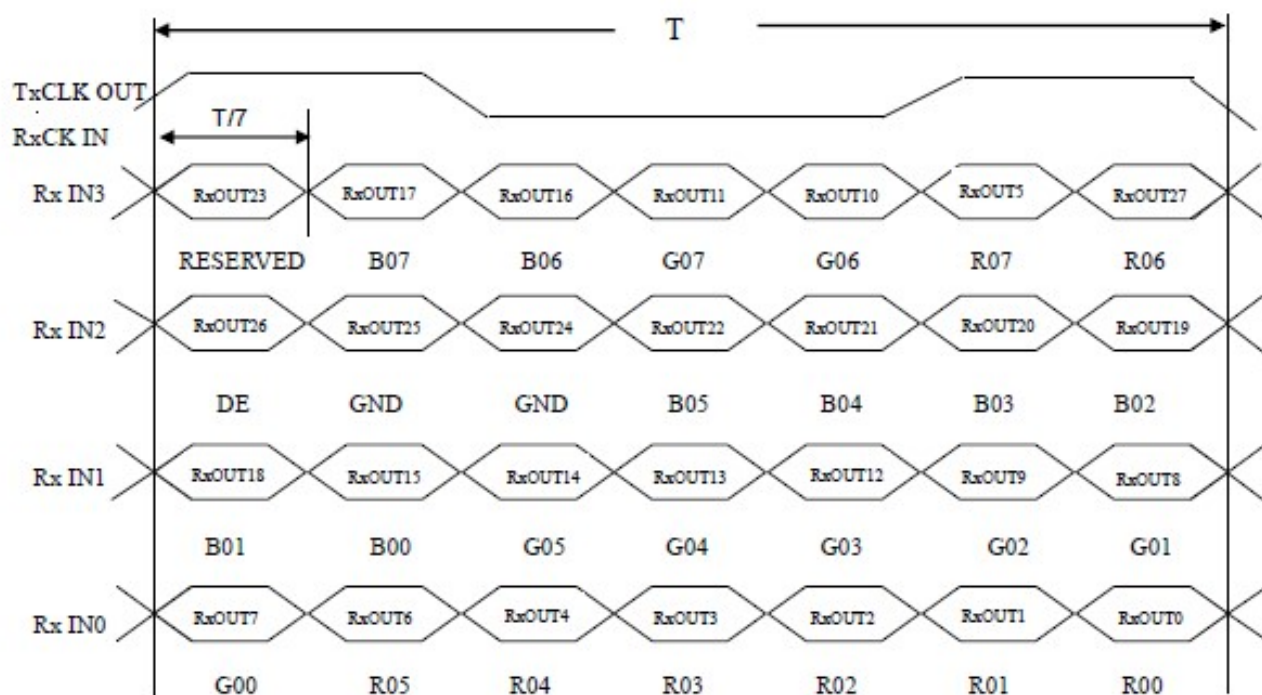
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Clock	Frequency	F_r	65.9	71	85	MHz	-
	Period	T_c	13.4	14.1	15.2	ns	-
	Input cycle to cycle jitter	T_{rel}	---	---	200	ns	(a)
	Input Clock to data skew	TLVCCS	$-0.02 \cdot T_c$	---	$0.02 \cdot T_c$	ps	(b)
	Spread spectrum modulation range	F_{clk_mod}	$0.987 \cdot F_c$	---	$1.013 \cdot F_c$	MHz	(c)
	Spread spectrum modulation frequency	F_{SSM}	---	---	200	KHz	
Vertical Display Term	Frame Rate	F_r	---	60	---	Hz	$T_V = T_{vd} + T_{vb}$
	Total	T_v	808	823	885	T_h	-
	Active Display	T_{vd}	800	800	800	T_h	-
	Blank	T_{vb}	8	23	85	T_h	-
Horizontal Display Term	Total	T_h	1360	1440	1600	T_c	$T_h = T_{hd} + T_{hb}$
	Active Display	T_{hd}	1280	1280	1280	T_c	-
	Blank	T_{hb}	80	160	320	T_c	-

Note (1) Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

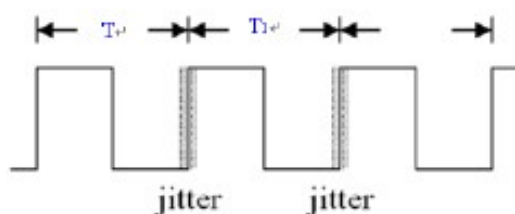
Note (2) The $T_v(T_{vd}+T_{vb})$ must be integer, otherwise, the module would operate abnormally.

INPUT SIGNAL TIMING DIAGRAM

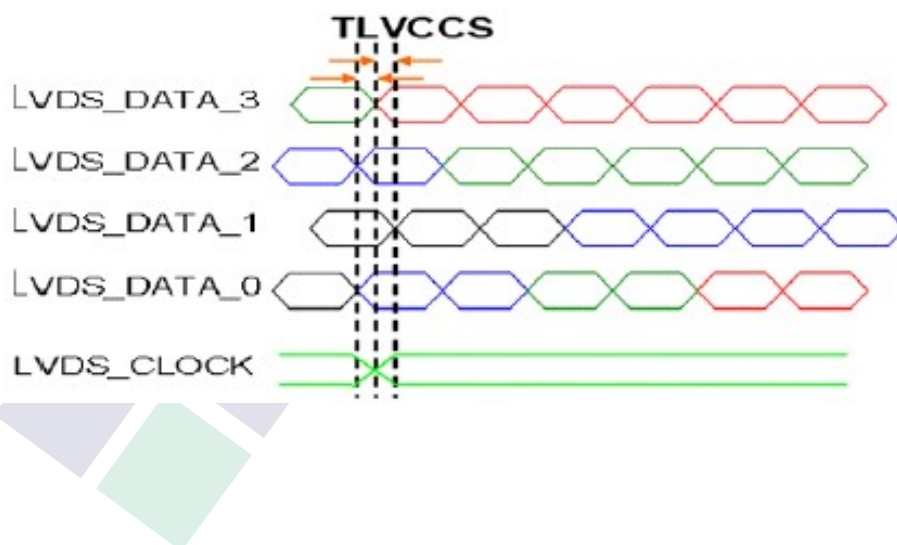




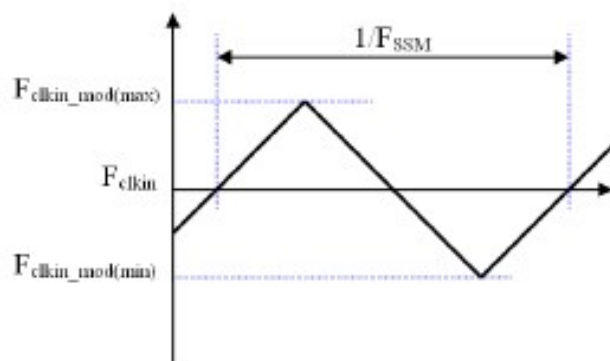
Note (a) The input clock cycle-to-cycle jitter is defined as below figures. $Trcl = |T_1 - T_1|$



Note (b) Input Clock to data skew is defined as below figures.

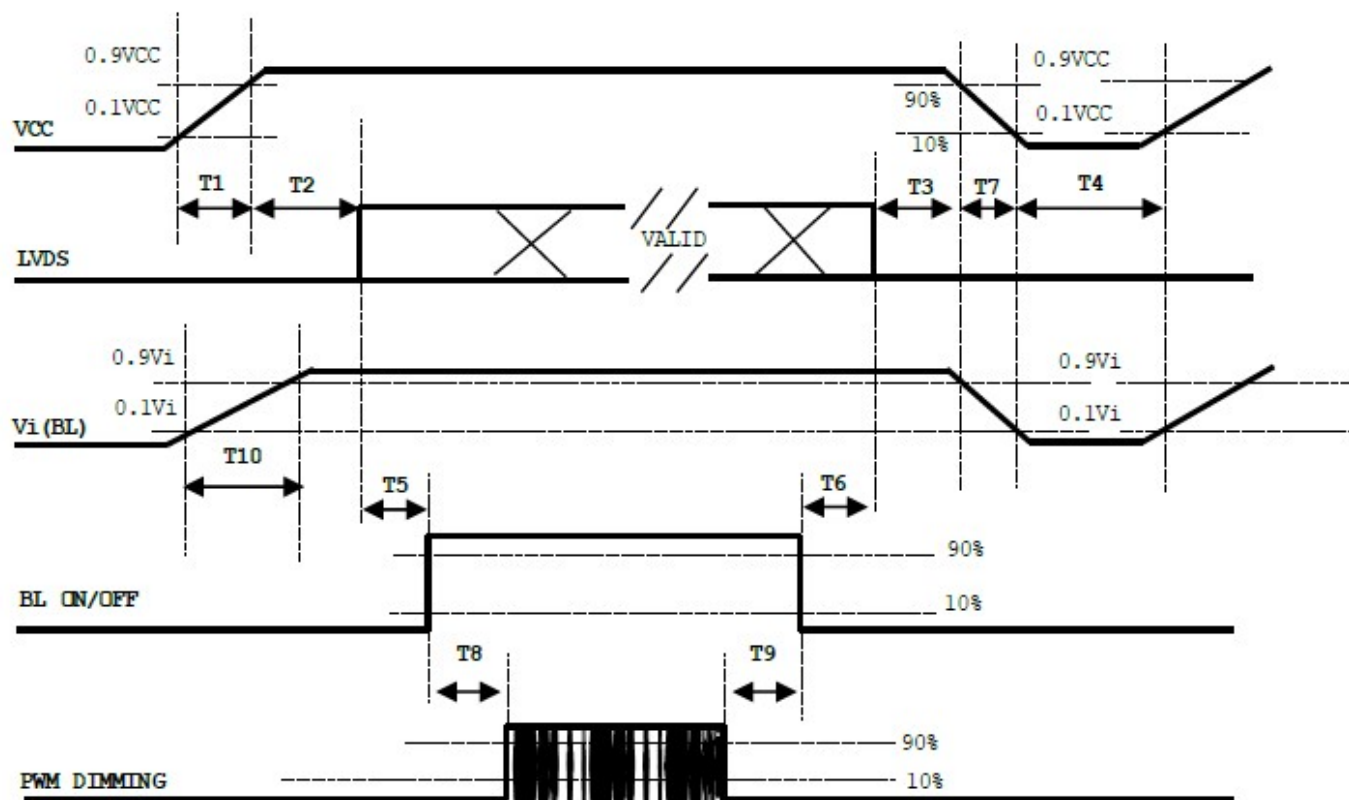


Note (c) The SSCG (Spread spectrum clock generator) is defined as below figures.



2.3.3 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD assembly, the power on/off sequence should be as the diagram below.



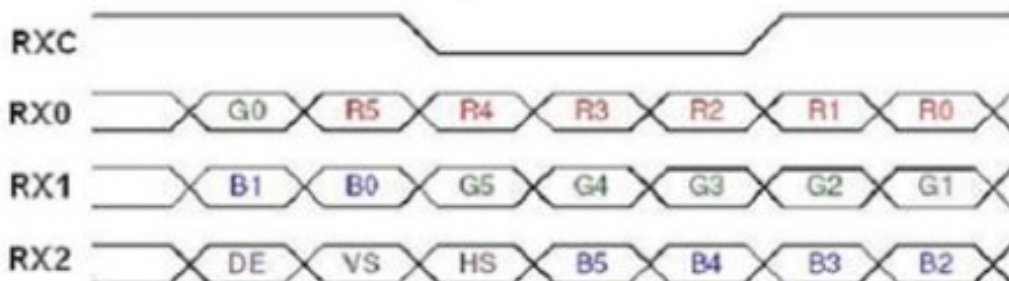
Parameter	Value			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0	-	50	ms
T3	0	-	50	ms
T4	500	-	-	ms
T5	450	-	-	ms
T6	200	-	-	ms
T7	10	-	100	ms
T8	10	-	-	ms
T9	10	-	-	ms
T10	20	-	50	ms

Note:

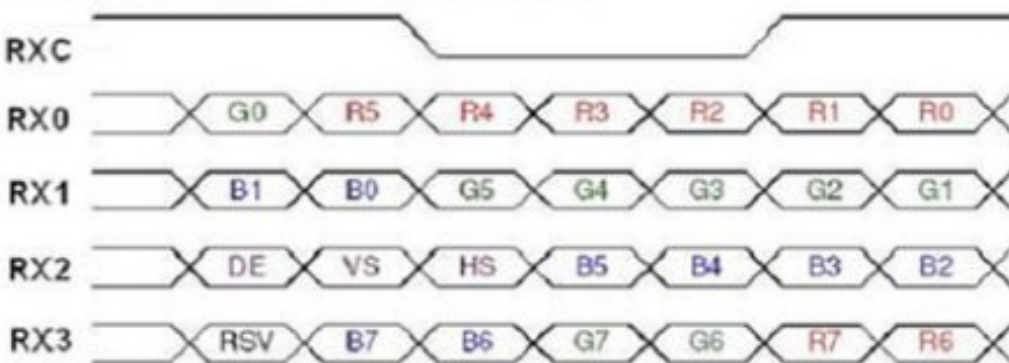
- (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- (2) When the backlight turns on before the LCD operation of the LCD turns off, the display may momentarily become abnormal screen.
- (3) In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.
- (6) INX won't take any responsibility for the products which are damaged by the customers not following the Power Sequence.
- (7) There might be slight electronic noise when LCD is turned off (even backlight unit is also off). To avoid this symptom, we suggest "Vcc falling timing" to follow "T7 spec".

LVDS Data Input Format

SEL 6/8="Low" for 6 Bits LVDS



SEL 6/8="High" for 8 Bits LVDS



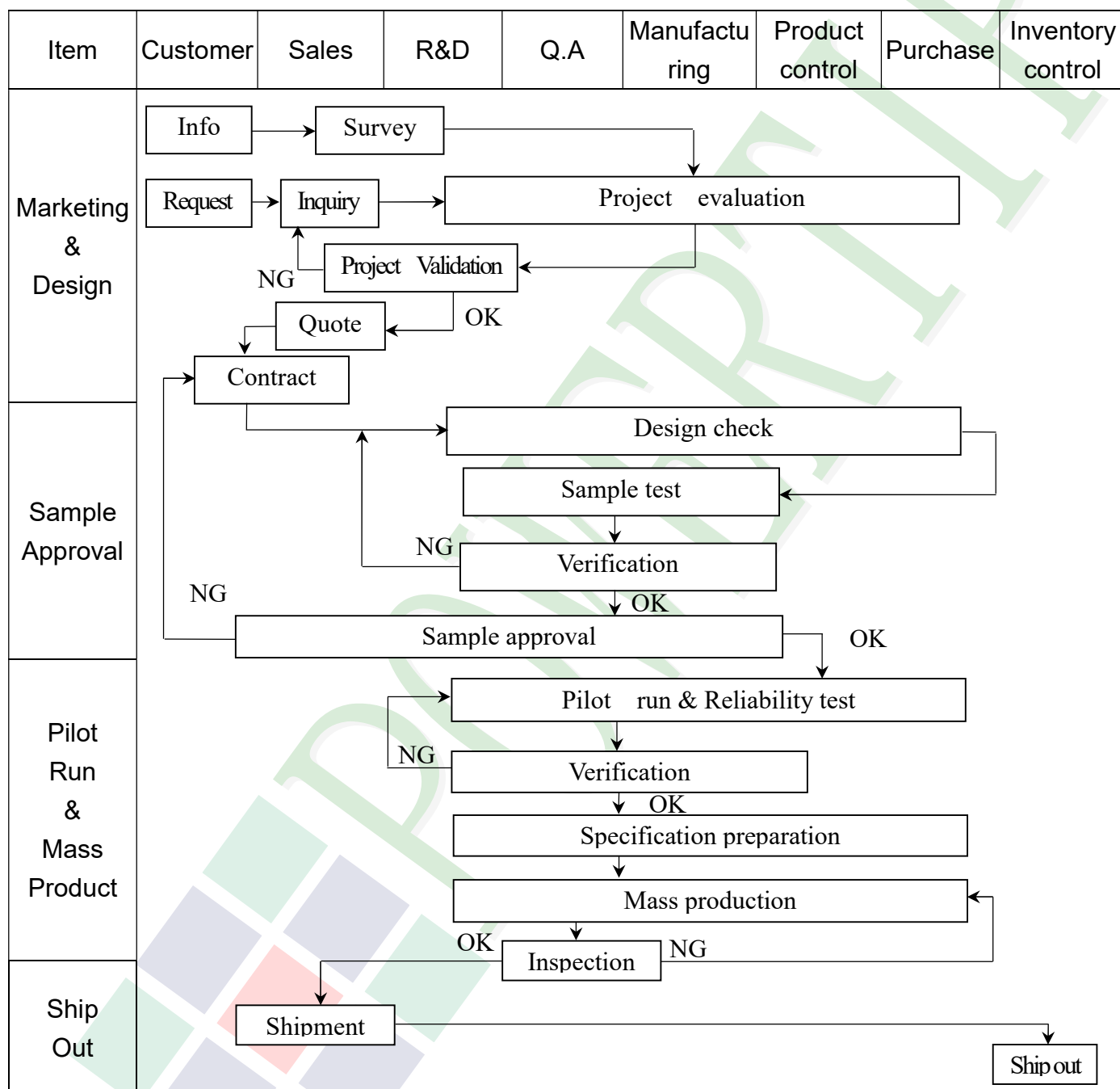
Note (1) R/G/B data 7: MSB, R/G/B data 0: LSB

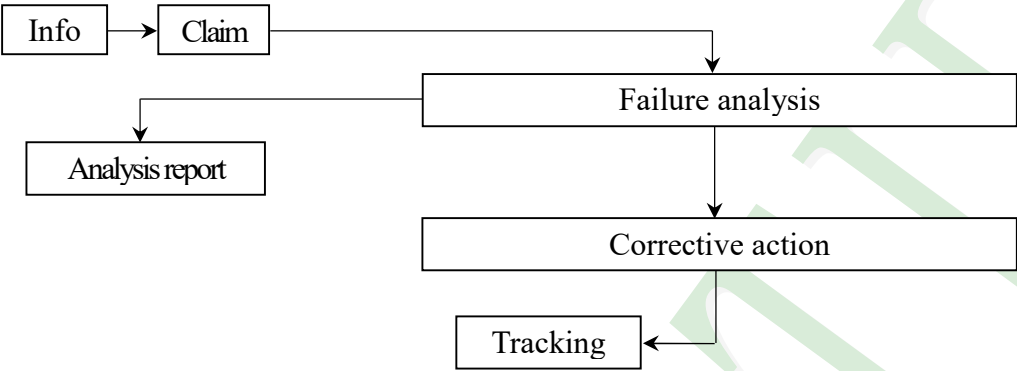
Note (2) Please follow PSWG

Signal Name	Description	Remark
R7	Red Data 7 (MSB)	Red-pixel Data Each red pixel's brightness data consists of these 8 bits pixel data.
R6	Red Data 6	
R5	Red Data 5	
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
G7	Green Data 7 (MSB)	Green-pixel Data Each green pixel's brightness data consists of these 8 bits pixel data.
G6	GreenData 6	
G5	GreenData 5	
G4	GreenData 4	
G3	GreenData 3	
G2	GreenData 2	
G1	GreenData 1	
G0	GreenData 0 (LSB)	
B7	Blue Data 7 (MSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 8 bits pixel data.
B6	Blue Data 6	
B5	Blue Data 5	
B4	Blue Data 4	
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
RXCLKIN+ RXCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



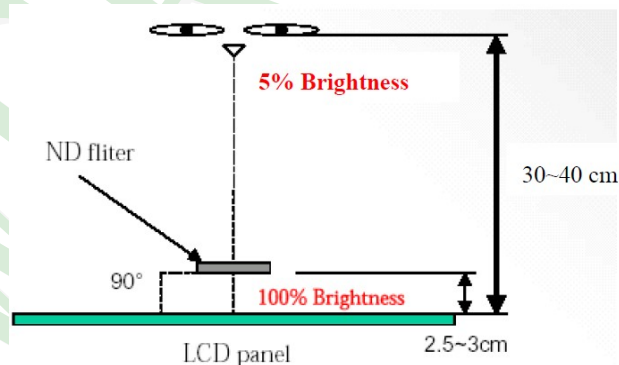
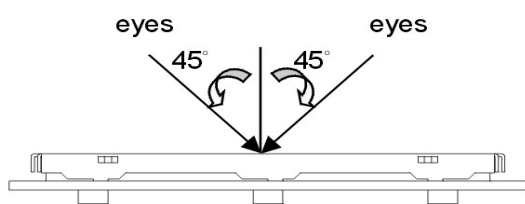
Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD Info[Info] --> Claim[Claim] Claim --> Failure[Failure analysis] Failure --> Analysis[Analysis report] Failure --> Corrective[Corrective action] Corrective --> Tracking[Tracking] </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

3.2. Inspection Specification

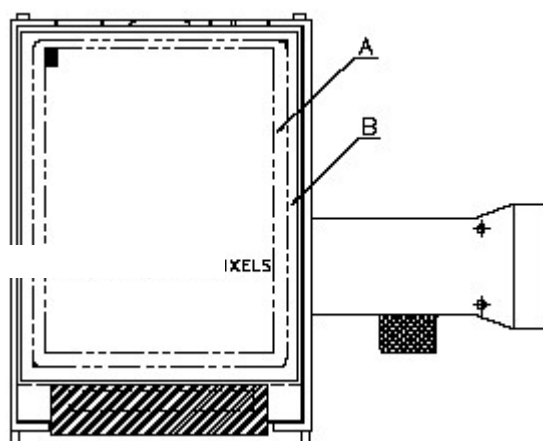
- ◆Scope : The document shall be applied to TFT-LCD Module for 3.5" ~15" (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II.
- ◆Equipment : Gauge 、MIL-STD 、Powertip Tester 、Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ◆OUT Going Defect Level : Sampling.
- ◆Standard of the product appearance test :

a. Manner of appearance test :

- (1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)
 , and distance of view must be at 30~40 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)

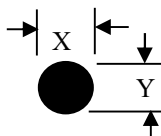
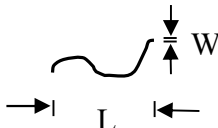
◆Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level												
01	Product condition	1.1The part number is inconsistent with work order of production.	Major												
		1.2 Mixed product types.	Major												
		1.3 Assembled in inverse direction.	Major												
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major												
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major												
04	Electrical Testing	4. 1 Missing line character and icon.	Major												
		4. 2 No function or no display.	Major												
		4. 3 Display malfunction.	Major												
		4. 4 LCD viewing angle defect.	Major												
		4. 5 Current consumption exceeds product specifications.	Major												
		4. 6 Mura can not be seen through 5% ND filter at 50% Gray screen , should be judged by the viewing angle of 90 degree.	Minor												
05	Dot defect (Bright dot 、 Dark dot) On -display	<table><tr><th colspan="2">Item</th><th>Acceptance (Q'ty)</th></tr><tr><td rowspan="4">Dot Defect</td><td>Bright Dot</td><td>≤ 4</td></tr><tr><td>Dark Dot</td><td>≤ 5</td></tr><tr><td>Joint Dot</td><td>≤ 3</td></tr><tr><td>Total</td><td>≤ 7</td></tr></table>	Item		Acceptance (Q'ty)	Dot Defect	Bright Dot	≤ 4	Dark Dot	≤ 5	Joint Dot	≤ 3	Total	≤ 7	Minor
		Item		Acceptance (Q'ty)											
Dot Defect	Bright Dot	≤ 4													
	Dark Dot	≤ 5													
	Joint Dot	≤ 3													
	Total	≤ 7													
5. 1 Inspection pattern : full white , full black , Red , Green and blue screens. 5. 2 It is defined as dot defect if defect area >1/2 dot. 5. 3 The distance between two dot defect ≥5 mm. 5. 4 Bright dot that can not be seen through 5% ND filter.															

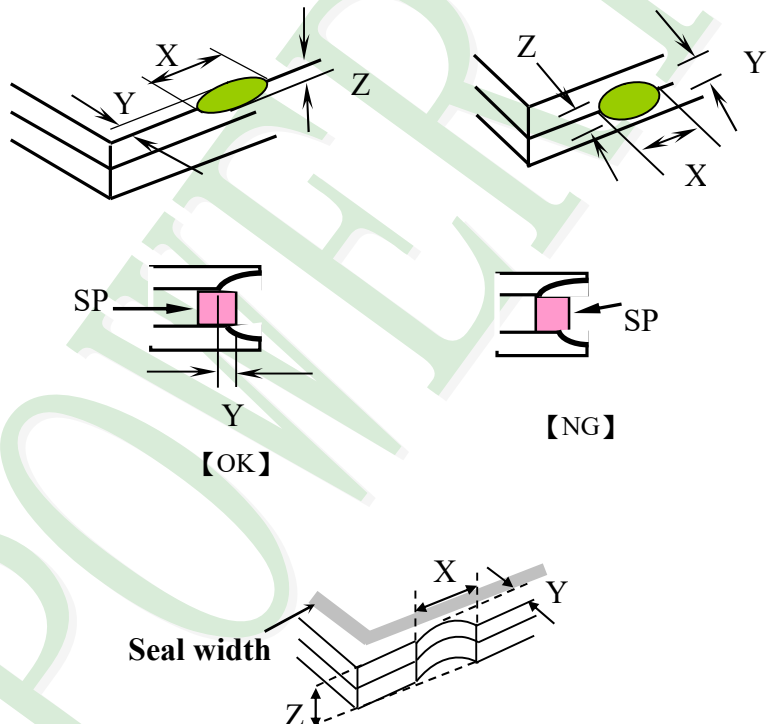
◆Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level																																																						
06	Black or white dot、scratch、contamination Round type  Φ=(x+y) / 2 Line type 	6. 1 Round type (Non-display or display) : <table><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≤ 0.25</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>0.25 < Φ ≤ 0.50</td><td>5</td></tr><tr><td>Φ > 0.50</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table> 6. 2 Line type(Non-display or display) : <table><tr><th rowspan="2">module size</th><th rowspan="2">Length (L)</th><th rowspan="2">Width (W)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td rowspan="5">3.5” to less 9”</td><td>---</td><td>W ≤ 0.03</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>L ≤ 10.0</td><td>0.03 < W ≤ 0.05</td><td>4</td></tr><tr><td>L ≤ 5.0</td><td>0.05 < W ≤ 0.10</td><td>2</td></tr><tr><td>---</td><td>W > 0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td><td></td></tr><tr><td rowspan="4">9” to 15”</td><td>---</td><td>W ≤ 0.05</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>L ≤ 10.0</td><td>0.05 < W ≤ 0.10</td><td>5</td></tr><tr><td>---</td><td>W > 0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td><td></td></tr></table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	Φ ≤ 0.25	Ignore	Ignore	0.25 < Φ ≤ 0.50	5	Φ > 0.50	0	Total	5	module size	Length (L)	Width (W)	Acceptance (Q'ty)		A area	B area	3.5” to less 9”	---	W ≤ 0.03	Ignore	Ignore	L ≤ 10.0	0.03 < W ≤ 0.05	4	L ≤ 5.0	0.05 < W ≤ 0.10	2	---	W > 0.10	As round type	Total		5		9” to 15”	---	W ≤ 0.05	Ignore	Ignore	L ≤ 10.0	0.05 < W ≤ 0.10	5	---	W > 0.10	As round type	Total		5		Minor
		Dimension (diameter : Φ)		Acceptance (Q'ty)																																																					
			A area	B area																																																					
		Φ ≤ 0.25	Ignore	Ignore																																																					
0.25 < Φ ≤ 0.50	5																																																								
Φ > 0.50	0																																																								
Total	5																																																								
module size	Length (L)	Width (W)	Acceptance (Q'ty)																																																						
			A area	B area																																																					
3.5” to less 9”	---	W ≤ 0.03	Ignore	Ignore																																																					
	L ≤ 10.0	0.03 < W ≤ 0.05	4																																																						
	L ≤ 5.0	0.05 < W ≤ 0.10	2																																																						
	---	W > 0.10	As round type																																																						
	Total		5																																																						
9” to 15”	---	W ≤ 0.05	Ignore	Ignore																																																					
	L ≤ 10.0	0.05 < W ≤ 0.10	5																																																						
	---	W > 0.10	As round type																																																						
	Total		5																																																						
07	Polarizer Bubble	<table><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≤ 0.25</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>0.25 < Φ ≤ 0.50</td><td>4</td></tr><tr><td>0.50 < Φ ≤ 0.80</td><td>1</td></tr><tr><td>Φ > 0.80</td><td>0</td></tr><tr><td colspan="2">Total</td><td>5</td><td></td></tr></table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	Φ ≤ 0.25	Ignore	Ignore	0.25 < Φ ≤ 0.50	4	0.50 < Φ ≤ 0.80	1	Φ > 0.80	0	Total		5		Minor																																				
		Dimension (diameter : Φ)		Acceptance (Q'ty)																																																					
			A area	B area																																																					
		Φ ≤ 0.25	Ignore	Ignore																																																					
		0.25 < Φ ≤ 0.50	4																																																						
		0.50 < Φ ≤ 0.80	1																																																						
Φ > 0.80	0																																																								
Total		5																																																							

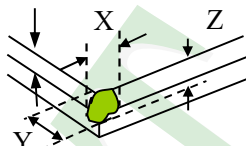
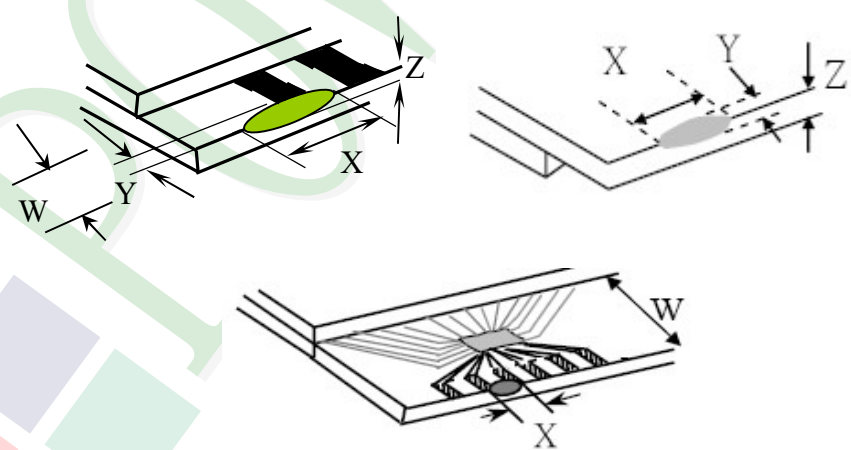
◆Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level						
08	The crack of glass	Symbols : X : The length of crack Z : The thickness of crack t : The thickness of glass Y : The width of crack. W : terminal length a : LCD side length 8.1 General glass chip : 8.1.1 Chip on panel surface and crack between panels: 	Minor						
		<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$\leq a$</td><td>Crack can't enter viewing area</td><td>$\leq 1/2 t$</td></tr><tr><td>$\leq a$</td><td>Crack can't exceed the half of SP width.</td><td>$1/2 t < Z \leq 2 t$</td></tr></table>		X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$
X	Y	Z							
$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$							
$\leq a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$							

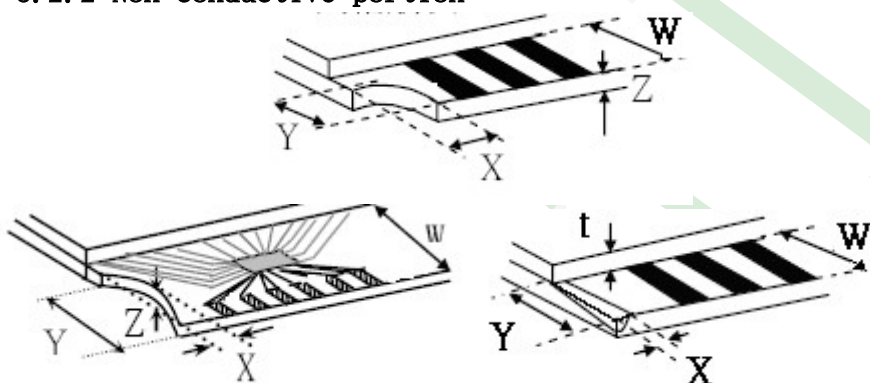
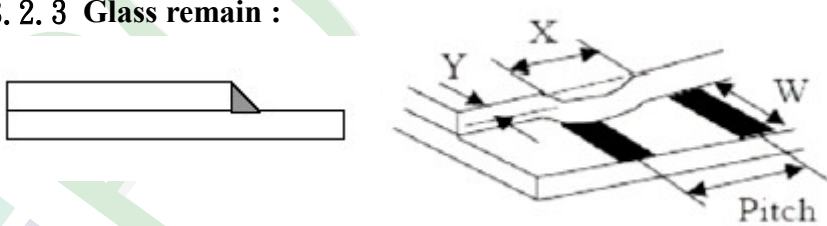
◆Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level									
08	The crack of glass	Symbols : X : The length of crack Z : The thickness of crack t : The thickness of glass Y : The width of crack. W : terminal length a : LCD side length 8.1.2 Corner crack :  <table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$\leq 1/5 a$</td><td>Crack can't enter viewing area</td><td>$Z \leq 1/2 t$</td></tr><tr><td>$\leq 1/5 a$</td><td>Crack can't exceed the half of SP width.</td><td>$1/2 t < Z \leq 2 t$</td></tr></table>	X	Y	Z	$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$	$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	Minor
		X	Y	Z								
$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$										
$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$										
8.2 Protrusion over terminal : 8.2.1 Chip on electrode pad :  <table><tr><th></th><th>X</th><th>Y</th><th>Z</th></tr><tr><td>Front</td><td>$\leq a$</td><td>$\leq 1/2 W$</td><td>$\leq t$</td></tr><tr><td>Back</td><td>$\leq a$</td><td>$\leq W$</td><td>$\leq 1/2 t$</td></tr></table>		X	Y	Z	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	$\leq a$	$\leq W$	$\leq 1/2 t$
	X	Y	Z									
Front	$\leq a$	$\leq 1/2 W$	$\leq t$									
Back	$\leq a$	$\leq W$	$\leq 1/2 t$									

◆Specification For TFT-LCD Module 3. 5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level												
08	The crack of glass	Symbols : X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length 8.2.2 Non-conductive portion :  <table> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>$\leq 1/3 a$</td> <td>$\leq W$</td> <td>$\leq t$</td> </tr> </table> ⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8.2.3 Glass remain :  <table> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>$\leq a$</td> <td>$\leq 1/3 W$</td> <td>$\leq t$</td> </tr> </table> 8.2.4 Cracking  Not Allowed	X	Y	Z	$\leq 1/3 a$	$\leq W$	$\leq t$	X	Y	Z	$\leq a$	$\leq 1/3 W$	$\leq t$	Minor
X	Y	Z													
$\leq 1/3 a$	$\leq W$	$\leq t$													
X	Y	Z													
$\leq a$	$\leq 1/3 W$	$\leq t$													

◆Specification For TFT-LCD Module 3.5" ~15" :
(Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General appearance	10. 1 Pin type 、 quantity 、 dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION											
1	High Temperature Storage Test	Keep in +80 ±2℃ 240 hrs Surrounding temperature, then storage at normal condition 4hrs.											
2	Low Temperature Storage Test	Keep in -30 ±2℃ 240 hrs Surrounding temperature, then storage at normal condition 4hrs.											
3	High Temperature / High Humidity Storage Test	Keep in +60℃ / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)											
4	Temperature Cycling Storage Test	-30℃ → +25℃ → +80℃ → +25℃ (30mins) (5mins) (30mins) (5mins) ← 20 Cycle → Surrounding temperature, then storage at normal condition 4hrs.											
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/-	Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/-										
		1.Temperature ambience : 15℃ ~35℃ 2.Humidity relative : 30%~60% 3.Energy Storage Capacitance(Cs+Cd) : 150pF±10% 4.Discharge Resistance(Rd) : 330Ω±10% 5.Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)											
6	Vibration Test (Packaged)	1.Sine wave 10~55 Hz frequency (1 min) 2.The amplitude of vibration :1.5 mm 3. Each direction (X、Y、Z) duration for 2 Hrs											
7	Drop Test (Packaged)	<table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table> Drop direction :※1 corner / 3 edges / 6 sides each 1times		Packing Weight (Kg)	Drop Height (cm)	0 ~ 45.4	122	45.4 ~ 90.8	76	90.8 ~ 454	61	Over 454	46
Packing Weight (Kg)	Drop Height (cm)												
0 ~ 45.4	122												
45.4 ~ 90.8	76												
90.8 ~ 454	61												
Over 454	46												

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

Ver.001

Documents NO.

PKG-PH128800T005-ZFC

LCM包裝規格書

LCM Packaging Specifications

Approve	Check	Contact
Oliver	Oliver	Stone

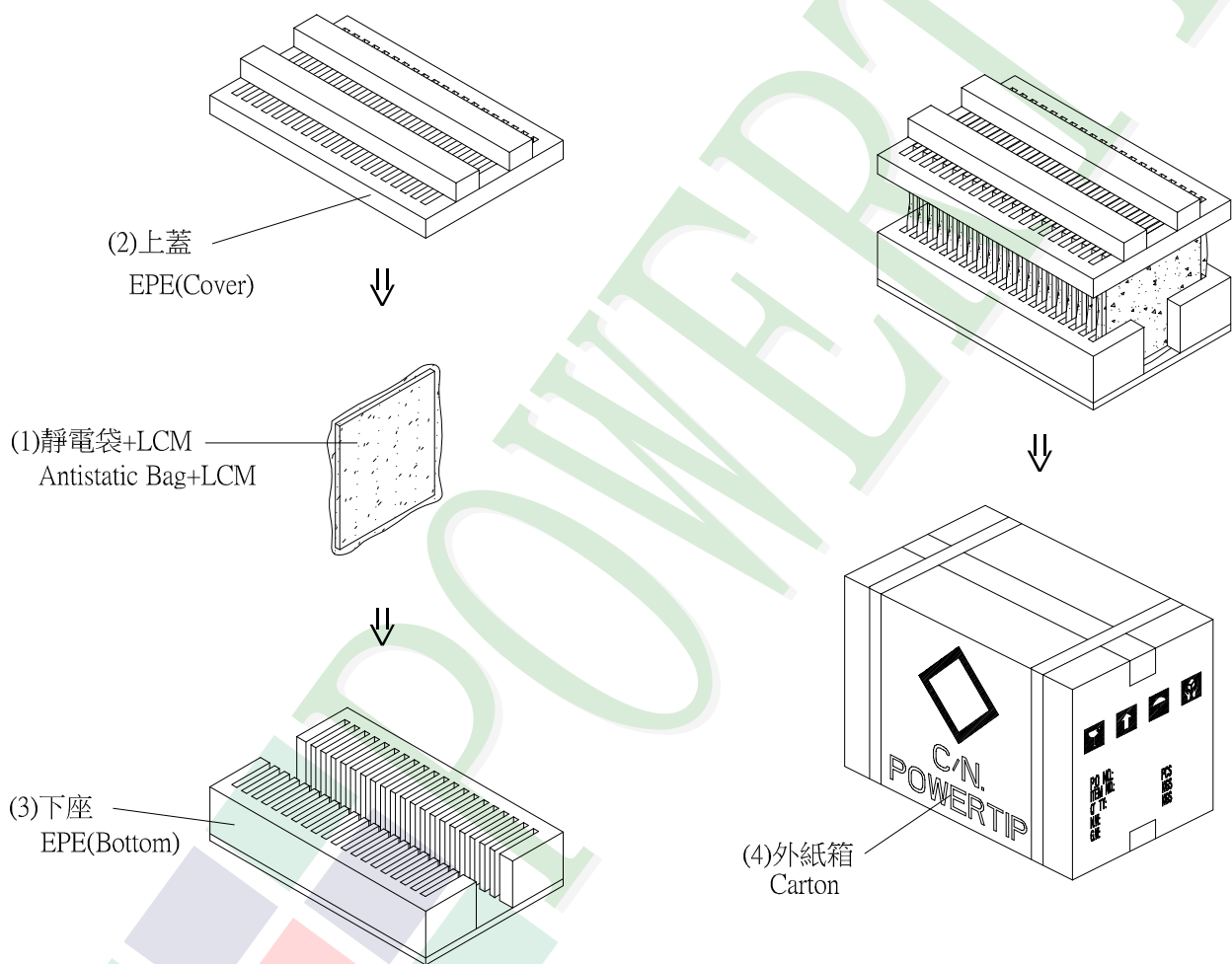
1.包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH128800T005-ZFC	305.0 X 200.0	0.955	18	17.19
2	靜電袋(1)Antistatic Bag	--	--	--	18	--
3	上蓋(2)EPE(Cover)	FOAM000000197	510 X 355 X 55	0.16	1	0.16
4	下座(3)EPE(Bottom)	FOAM000000198	510 X 355 X 100	0.34	1	0.34
5	外紙箱(4)Carton	BX53537327CCBA	535 X 373 X 265	1.1624	1	1.1624
6						

2.一 整箱總重量 (Total LCD Weight in carton) : 18.85 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

Total LCM quantity in carton : quantity per EPE 18 x no of EPE 1 = 18



特 記 事 項 (REMARK)

4. 使用供應商原包裝靜電袋。