

SEMITOP® 3

Half-Bridge (MOSFET module)

SK280MB10

Features*

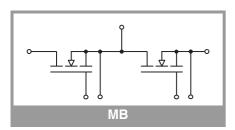
- One screw mounting module
- · Low inductive design
- Heat transfer and isolation through direct copper bonded aluminium oxide ceramic (DCB)
- 100V Trench MOS technology
- UL recognized file no. E 63 532

Typical Applications

• Switched Mode Power Supplies

Remarks

Recommended driving for optimal switching performances: V_{GS} =0/10V



Absolute Maximum Ratings						
Symbol	Conditions		Values	Unit		
MOSFET	1			'		
V_{DSS}			100	V		
I_D	T _j = 175 °C	$T_s = 25 ^{\circ}\text{C}$ $T_s = 70 ^{\circ}\text{C}$	335	Α		
		T _s = 70 °C	280	Α		
I _{DM}			960	Α		
I _{DRM}			320	Α		
V_{GS}			-20 20	V		
Tj			-40 175	°C		
Integrated	d body diode					
I _{FM}			960	Α		
I _{FRM}			320	Α		

Absolute Maximum Ratings					
Symbol	Conditions	Values	Unit		
Module					
I _{t(RMS)}	ΔT _{terminal} at PCB joint = 30 K, per pin	60	Α		
T _{stg}		-40 125	°C		
V _{isol}	AC, sinusoidal, t = 1 min	2500	٧		

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
MOSFET 1							
V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_j = 25 ^{\circ}\text{C}$		100			V	
V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 0.55 \text{ mA}, T_i = 25 ^{\circ}\text{C}$		2	2.7	3.5	V	
I _{DSS}	V _{GS} = 0 V, V _{DS} = 100 V, T _i = 25 °C				0.2	mA	
I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}, T_j = 25 ^{\circ}\text{C}$				200	nA	
R _{DS(on)}	V _{GS} = 10 V I _D = 200 A chiplevel	T _j = 25 °C		1.15	1.35	mΩ	
		T _j = 150 °C		2.1		mΩ	
C _{iss}	$V_{GS} = 0 \text{ V}, V_{DS} = 50 \text{ V}, f = 1 \text{ MHz}$			22200		pF	
Coss	$V_{GS} = 0 \text{ V}, V_{DS} = 50$	V, f = 1 MHz		3880		pF	
C _{rss}	$V_{GS} = 0 \text{ V}, V_{DS} = 50$	V, f = 1 MHz		138		pF	
R _{Gint}	T _j = 25 °C			4		Ω	
Q_{G}	V _{GS} = 0+15 V, V _D	_D = 50 V, I _D = 200 A		530		nC	
t _{d(on)}	$V_{DD} = 50 \text{ V}$	T _j = 150 °C		190		ns	
t _{d(off)}	_iui/ui _{off} = 1.0 k/4/µS	T _j = 150 °C		1000		ns	
t _r		T _j = 150 °C		133		ns	
t _f		T _j = 150 °C		97		ns	
E _{on}		T _j = 150 °C		0.2		mJ	
E _{off}	dv/dt = 637 kV/μs	T _j = 150 °C		2.1		mJ	
$R_{th(j-s)}$	per MOSFET, λ_{paste} =0.8 W/(mK)		0.47			K/W	
Integrated	l body diode						
$V_F = V_{SD}$	-I _D = 200 A	T _j = 25 °C		0.88		V	
	V _{GS} = 0 V chiplevel	T _j = 150 °C		0.77		V	
$V_{F0} = V_{SD0}$	chiplevel	T _j = 25 °C		0.71		V	
		T _j = 150 °C		0.53		V	
$r_F = r_{SD}$	chiplevel	T _j = 25 °C		0.85		mΩ	
		T _j = 150 °C		1.20		mΩ	
t _{rr}	V _{DD} = 50 V	T _j = 150 °C		90		ns	
Q _{rr}	$-I_D = 200 \text{ A}$ di/dt _{off} = 1.4 kA/ μ s	T _j = 150 °C	2.7			μС	
I _{rr}		T _j = 150 °C		60		Α	
E _{rr}	$V_{GS} = 0 V$	T _j = 150 °C		0.1		mJ	



Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Module						
L _{CE}			5		nΗ	
Ms	to heatsink	2.25		2.5	Nm	
W	weight		29		g	

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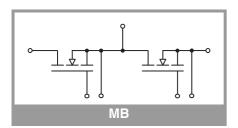
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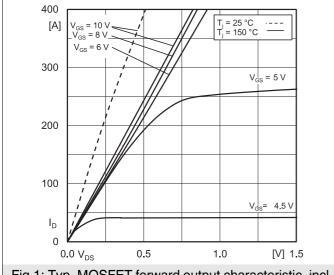
Typical Applications

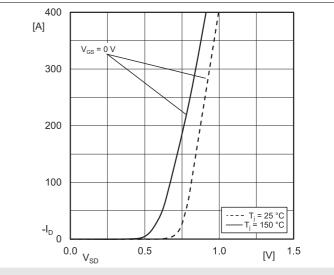
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Remarks

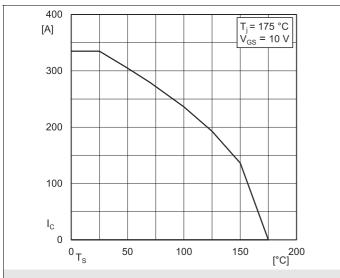
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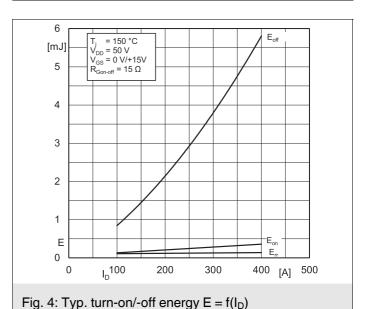


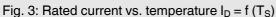












T_j = 150 °C V_{DD} = 50 V V_{GS} = 0V/+15V I_D = 200 A

[mJ]

3

2

Ε

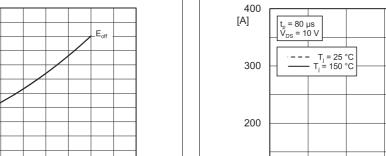


Fig. 5: Typ. turn-on /-off energy E= f (R_G)

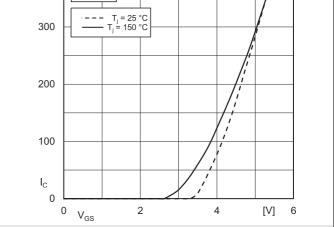
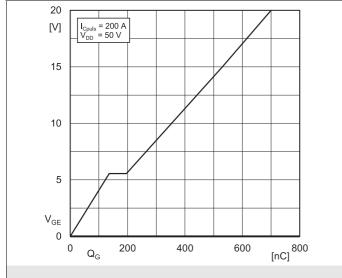


Fig. 6: Typ. MOSFET transfer characteristic

Err

[Ω]



Flg. 7: Typ. MOSFET gate charge characteristic

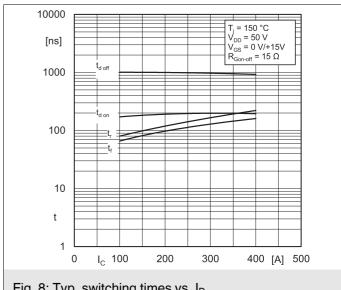


Fig. 8: Typ. switching times vs. I_D

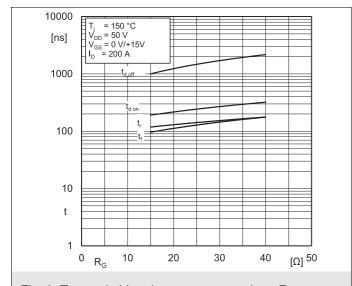


Fig. 9: Typ. switching times vs. gate resistor R_G

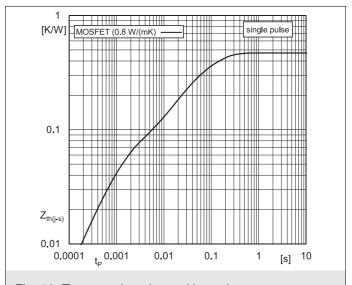
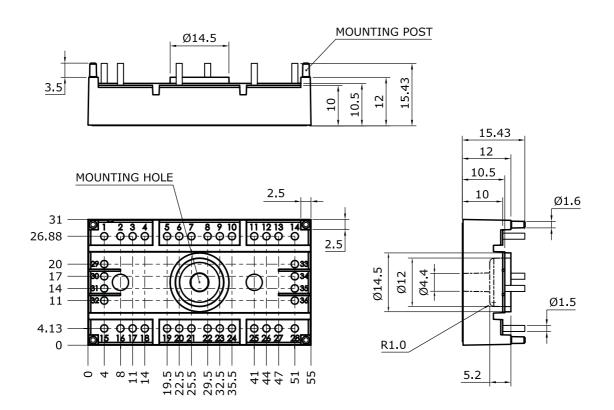


Fig. 10: Typ. transient thermal impedance

Dimensions: mm

Tolerance system: ISO 2768-m



Suggested hole diameter for solder pins in the circuit board:

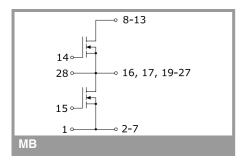
• 2.0 mm

Suggested hole diameter for the mounting post in the circuit board:

• 2.0 mm

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SEMITOP®3



This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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