



RAYSTAR

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曜凌光電股份有限公司



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RFH700A8-AYW-LNL

SPECIFICATION

CUSTOMER:

APPROVED BY	
PCB VERSION	
DATE	

FOR CUSTOMER USE ONLY

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

Release DATE:

TFT Display Inspection Specification: <https://www.raystar-optronics.com/download/products.htm>

Precaution in use of TFT module: <https://www.raystar-optronics.com/download/declaration.htm>

Revision History

VERSION	DATE	REVISED PAGE NO.	Note
0	2022/04/29		First issue

RAYSTAR OPTRONICS

Contents

1.Module Classification Information

2.Summary

3.General Specifications

4.Interface

5.Contour Drawing

6.Absolute Maximum Ratings

7.Electrical Characteristics

8.Data Input Format For LVDS

9.Timing Characteristic

10.Optical Characteristics

11.Reliability

12.Touch Panel Information

13.Other

1.Module Classification Information

R	F	H	70	0A8	-	A	Y	W	-	L	N	L
1	2	3	4	5	-	6	7	8	-	9	10	11

Item	Description			
1	R : Raystar Optronics Inc.			
2	Display Type : F→TFT Type, J→ Custom TFT			
3	Solution: A: 128x160 B:320x234 C:320x240 D:480x234 E:480x272 F:800x480 G:640x480 H:1024x600 I:320x480 J:240x320 K:1280x800 L:240x400 M:1024x768 N:128x128 O:480x800 P:640x320 Q:800x600 S:480x128 T:800x320			
4	Display Size : 7.0” TFT			
5	Version Code: V: Raspberry pi 3B+ X:Raspberry pi			
6	Model Type: A : TFT LCD E : TFT+FR+CONTROL BOARD J : TFT+FR+A/D BOARD N : TFT+FR+A/D BOARD+CONTROL BOARD S : TFT+FR+POWER BOARD (DC TO DC)		6 : TFT+FR H : TFT+D/V BOARD I : TFT+FR+D/V BOARD B : TFT+POWER BD 1 : TFT+CONTROL BOARD	
7	Polarizer Type, Temperature range, View direction	I→Transmissive, W. T, 6:00 ; C→Transmissive, N. T, 6:00 L→Transmissive, W.T,12:00 ; F→Transmissive, N.T,12:00 Y→Transmissive,W.T, IPS TFT ; W→Transmissive, Super W.T, IPS TFT A→Transmissive, N.T, IPS TFT V→Transmissive, Super W.T, VA TFT Z→Transmissive, W.T, O-TFT R→Transmissive, Super W.T, O-TFT N→Transmissive, Super W.T, 6:00; Q→Transmissive, Super W.T, 12:00		
8	Backlight	W : LED, White	H : LED, High Light White	
		F : CCFL, White		
9	Driver Method	D: Digital A: Analog L : LVDS M:MIPI		
10	Interface	N : without control board A : 8Bit B : 16Bit S:SPI Interface R: RS232 U:USB I: I2C H: HDMI		
11	TS	N : Without TS S : resistive touch panel C : capacitive touch panel capacitive touch panel (G-F-F) G : capacitive touch panel(G-G) B: CTP+GG+USB L: CTP+GG Hovering Touch		

2.Summary

TFT 7.0" is a IPS transmissive type color active matrix TFT liquid crystal display that use amorphous silicon TFT as switching devices. This module is composed of a TFT LCD module, It is usually designed for industrial application and this module follows RoHs.

RAYSTAR OPTRONICS

3.General Specifications

- Size: 7.0 inch
- Dot Matrix: 1024 x RGBx600(TFT) dots
- Module dimension: 169.9(W) x 103.4(H) x 8.0(D) mm
- Active area: 154.2144 x 85.92 mm
- Dot pitch: 0.1506 x 0.1432 mm
- LCD type: TFT, Normally Black, Transmissive
- Viewing Angle: 85/85/85/85
- TFT Interface: LVDS
- Aspect Ratio: 16:9
- Driver IC: ST5021 + ST5651 or equivalent
- Backlight Type: LED, Normally White
- CTP IC: N/A or equivalent
- CTP Interface: I2C
- CTP FW Version: 0x10AA
- CTP Resolution: 1024*600
- Support touch point: Contact : five-finger / Hover : One-finger , Hover high : 10mm
- With /Without TP: With CTP
- Surface: Glare

*Color tone slight changed by temperature and driving voltage.

4.Interface

4.1. LCM PIN Definition

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	P	Common Voltage	
2	VDD	P	Digital circuit	
3	VDD	P	Digital circuit	
4	NC	---	No connection	
5	RESET	I	Global reset pin	
6	STBYB	I	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z	
7	GND	P	Ground	
8	RXIN0-	I	Negative LVDS differential data input	
9	RXIN0+	I	Positive LVDS differential data input	
10	GND	P	Ground	
11	RXIN1-	I	Negative LVDS differential data input	
12	RXIN1+	I	Positive LVDS differential data input	
13	GND	P	Ground	
14	RXIN2-	I	Negative LVDS differential data input	
15	RXIN2+	I	Positive LVDS differential data input	
16	GND	P	Ground	
17	RXCLKIN-	I	Negative LVDS differential clock input	
18	RXCLKIN+	I	Positive LVDS differential clock input	
19	GND	P	Ground	
20	RXIN3-	I	Negative LVDS differential data input	
21	RXIN3+	I	Positive LVDS differential data input	
22	GND	P	Ground	
23	NC	---	No connection	
24	NC	---	No connection	
25	GND	P	Ground	
26	NC	---	No connection	
27	NC	---	No connection	
28	SELB	I	6bit/8bit mode select H:6bit / L:8bit	

29	AVDD	P	Power for Analog Circuit	
30	GND	P	Ground	
31	LED-	P	LED Cathode	
32	LED-	P	LED Cathode	
33	L/R	I	Horizontal inversion	
34	U/D	I	Vertical inversion	
35	VGL	P	Negative power for TFT	
36	NC	---	No connection	
37	NC	---	No connection	
38	VGH	P	Positive power for TFT	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

I:input ,O:output,P:power

Note:

When L/R="0",set right to left scan direction.

When L/R="1",set left to right scan direction.

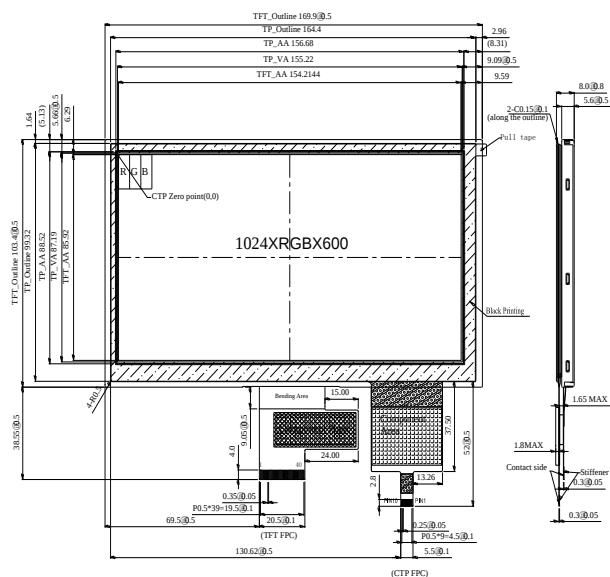
When U/D="0",set top to bottom scan direction.

When U/D="1",set bottom to top scan direction.

4.2. CTP PIN Definition

Pin	Symbol	Function	Remark
1	VSS	Ground for analog circuit	
2	VDDT	Power Supply : +3.3V	
3	SCL	I2C clock input	
4	NC	No connect	
5	SDA	I2C data input and output	
6	NC	No connect	
7	/RST	External Reset, Low is active	
8	NC	No connect	
9	/INT	External interrupt to the host	
10	VSS	Ground for analog circuit	

5. Contour Drawing



CTP FFC		TFT FFC	
PIN NO.	SYMBOL	PIN NO.	SYMBOL
1	VSS	1	VCOM
2	VDD1	2	VDD1
3	SCL	3	VDD1
4	NC	4	NC
5	SDA	5	RESE
6	NC	6	STBY
7	/RST	7	GND
8	NC	8	RX1IN
9	/INT	9	RX1IN
10	VSS	10	GND
		11	RX1IN
		12	RX1IN
		13	GND
		14	RX2IN
		15	RX2IN
		16	GND
		17	EXCLKIN
		18	EXCLKIN
		19	GND
		20	RX3IN
		21	RX3IN
		22	GND
		23	NC
		24	NC
		25	GND
		26	NC
		27	NC
		28	SELB
		29	AVDD
		30	GND
		31	LED+
		32	LED-
		33	LR
		34	UD
		35	VGL
		36	NC
		37	NC
		38	VGH
		39	LED+
		40	LED+

The non-specified tolerance of dimension is $\pm 0.3\text{mm}$.

6. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

- Temp. ≤ 60°C, 90% RH MAX. Temp. > 60°C, Absolute humidity shall be less than 90% RH at 60°C

7. Electrical Characteristics

7.1. Typical Operation Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	VDD	3.0	3.3	3.6	V	
Supply CTP	VDDT	3.0	3.3	3.6	V	
	I _{CTP}	—	TBD	TBD	mA	
Analog Power	AVDD	8.9	9.0	9.1	V	
TFT Gate ON Voltage	VGH	17	18	19	V	Note1
TFT Gate OFF Voltage	VGL	-6.5	-6.0	-5.5	V	Note2
TFT Common Voltage	Vcom	3.0	3.15	3.3	V	Note3
Current for Driver	IDD	-	14	21	mA	VDD=3.3V
Power Current	IAVDD	-	25	-	mA	AVDD=9V
TFT Gate ON Current	IVGH	-	1	-	mA	VGH=18V
TFT Gate OFF Current	IVGL	-	1	-	mA	VGL=6V
TFT Common Current	IVCOM	-	1	-	mA	VCOM=3.15V

Note:

Note 1. VGH is TFT Gate operating Voltage.

Note 2. VGL is TFT Gate operating Voltage.

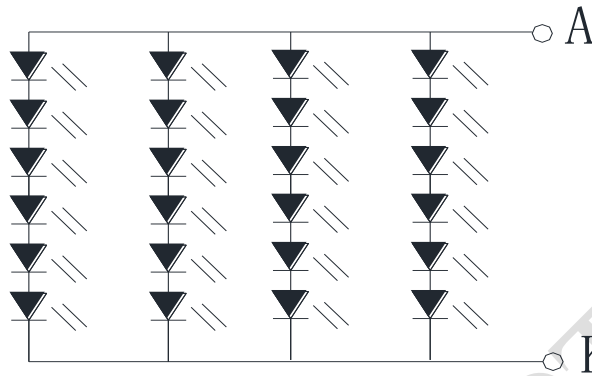
The storage structure of this model is CST (Storage on Common)

Note 3. Vcom must be adjusted to optimize display quality Crosstalk, Contrast Ratio and etc.

7.2. Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED backlight	VL	16.8	19.2	21.0	V	Note 1
Current for LED backlight	IL	--	240	--	mA	
LED life time	-	-	50,000	-	Hr	Note 2

Note 1 : There are 1 Groups LED



Backlight 24LED Circuit

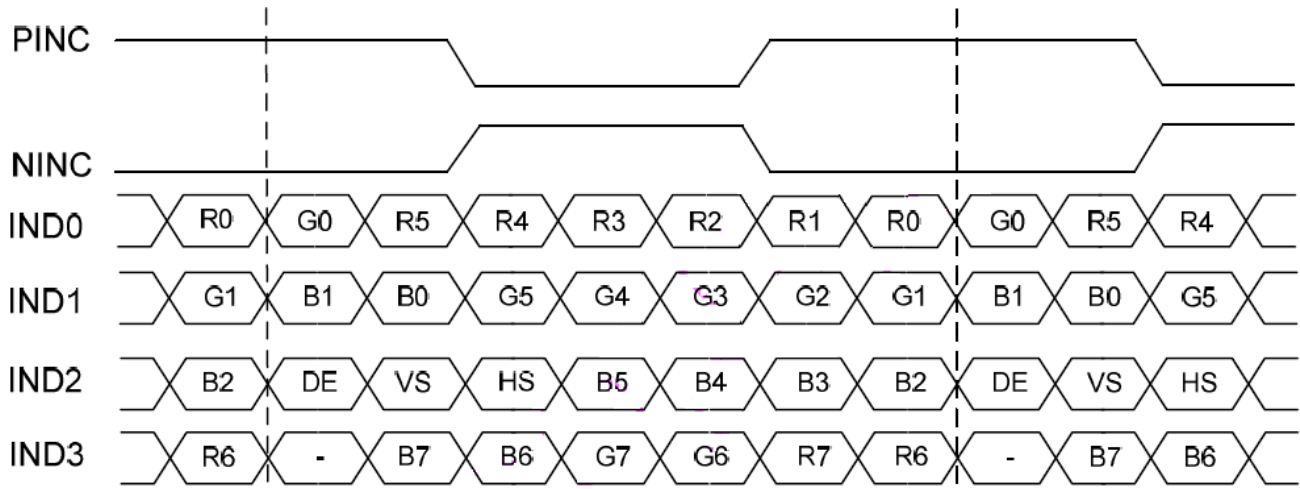
Note 2 : $T_a = 25\text{ }^{\circ}\text{C}$

Note 3 : Brightness to be decreased to 50% of the initial value

Note 4 : The single LED lamp case

8.Data Input Format For LVDS

8 bit LVDS input(HSD="L")



9. Timing Characteristic

9.1. For 1024x600 panel

MODE="H" : DE mode

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
CLKIN frequency@ Frame rate = 60Hz	fclk	40.8	51.2	67.2	MHz
Horizontal display area	thd	1024			CLKIN
1 Horizontal Line	th	1114	1344	1400	
HSD Blanking	thb+thfp	90	320	376	
Vertical display area	tvd	600			H
1 vertical Line	tv	610	635	800	
VSD Blankking	tvb+tvfp	10	35	200	

MODE="L" : SYNC mode

Horizontal input timing

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
CLKIN frequency@ Frame rate = 60Hz	fclk	44.9	51.2	63	MHz
Horizontal display area	thd	1024			CLKIN
1 Horizontal Line	th	1200	1344	1400	
HSD pulse width	thpw	1	-	140	
HSD Blanking	thb	160			
HSD Front Porch	thfp	16	160	216	

Vertical input timing

Parameter	Symbol	Value			Uint
		Min.	Typ.	Max.	
Vertical display area	tvd	600			H
VSD period time	tv	624	635	750	
VSD pulse width	tvpw	1	-	20	
VSD Blanking	tvb	23			
VSD Front Porch	tvfp	1	12	127	

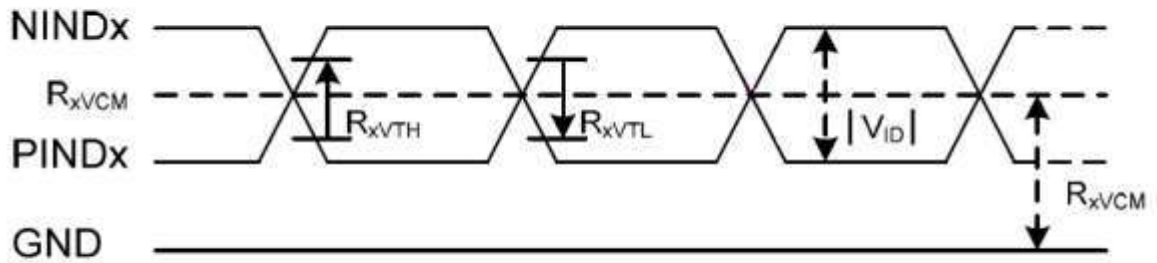
9.2. DC Electrical Characteristics

(VDD=2.3~3.6V, VDDA=8~13.5V, GND=GNDA=0V, TA=-20~85°C)

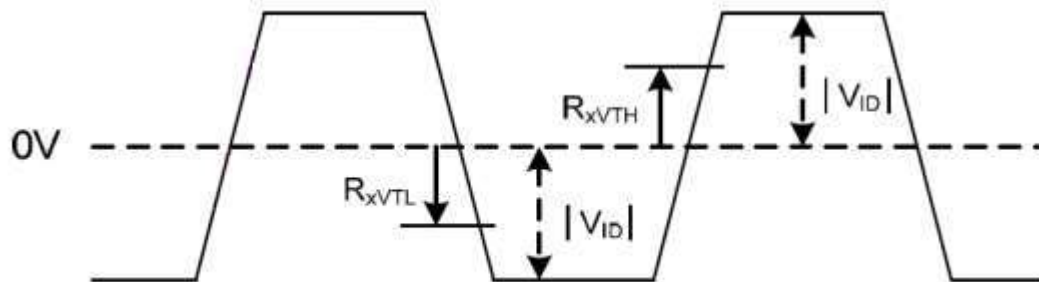
LVDS mode(Receiver Differential Input : PIND0~PIND3 , NIND0~NIND3 , PINC , NINC)

Parameter	Symbol	Min	Typ.	Max.	Unit	Conditions
Differential input high threshold voltage	R_{xVTH}			0.1	V	$R_{xVCM} = 1.2V$
Differential input low threshold voltage	R_{xVTL}	-0.1			V	
Input voltage range (singled-end)	R_{xVIN}	0		2.4	V	
Differential input common mode voltage	R_{xVCM}	$ V_{ID} /2$		$2.4 - V_{ID} /2$	V	
Differential input voltage	$ V_{ID} $	0.2		0.6	V	
Differential input leakage current	R_{VxIIZ}	-10		10	uA	
LVDS Digital Operating Current	I_{ddlvds}	-	40	50	mA	Fclk=65 MHz , VDD=3.3V
LVDS Digital Stand-by Current	I_{stlvds}	-	10	50	uA	Clock & all functions are stopped

Single end signals



Differential signals



Power Circuit

Parameter	Symbol	Min	Typ.	Max.	Unit	Conditions
VCOM buffer input voltage	VCOMI	1.5		VDDA-0.15	V	
VCOM buffer output voltage	VCOMO	VCOMI-0.2	VCOMI	VCOMI+0.2	V	
VCOM buffer output current	IVCOM	-	-	10	mA	VCOMO = 5V VS 4.9V

10. Optical Characteristics

Item	Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time	Tr	$\theta=0^\circ$ 、 $\Phi=0^\circ$	-	13	20	.ms	Note 3
	Tf		-	15	25		
Contrast ratio	CR	At optimized viewing angle	600	800	-	-	Note 4
Color Chromaticity	White	$\theta=0^\circ$ 、 $\Phi=0$	0.269	0.319	0.369	-	Note 2,5,6
	Wy		0.291	0.341	0.391	-	
Viewing angle	Hor.	Θ_R	80	85	-	Deg.	Note 1
		Θ_L	80	85	-		
	Ver.	Φ_T	80	85	-		
		Φ_B	80	85	-		
Brightness	-	-	400	450	-	cd/m ²	Center of display
Uniformity	(U)	-	75	-	-	%	Note 5

Ta=25±2°C,

Note 1: Definition of viewing angle range

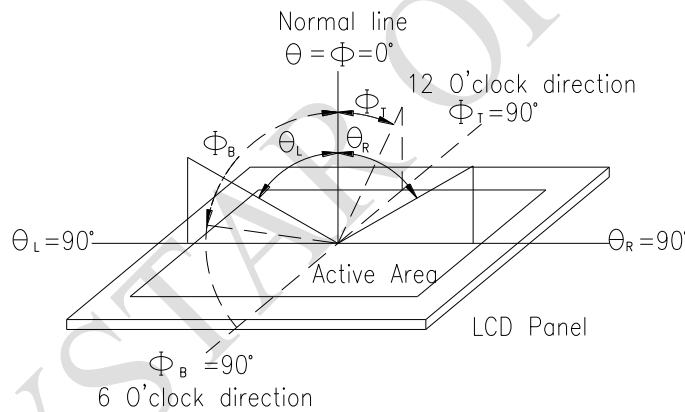


Fig. 10.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

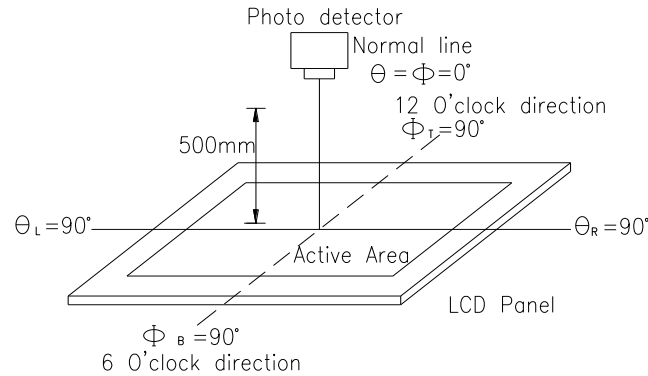
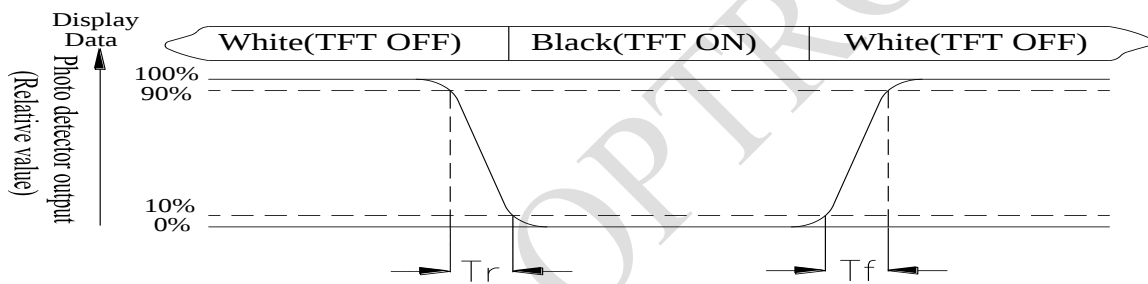


Fig. 10.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, T_r , is the time between photo detector output intensity changed from 90% to 10%. And fall time, T_f , is the time between photo detector output intensity changed from 10% to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (reference the picture in below). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = $L_{min}/L_{max} \times 100\%$

L = Active area length

W = Active area width

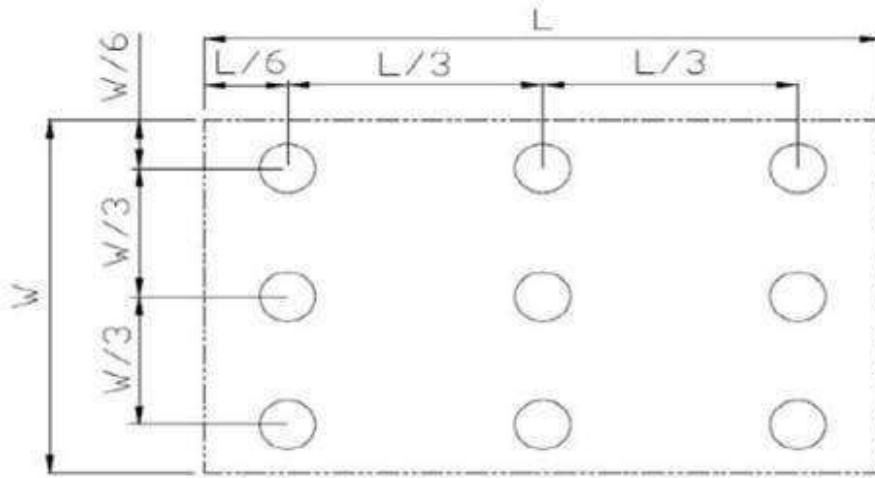


Fig 10.3. Definition of uniformity

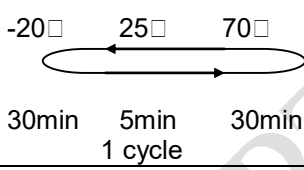
Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

11. Reliability

Content of Reliability Test (Wide temperature, -20℃~70℃)

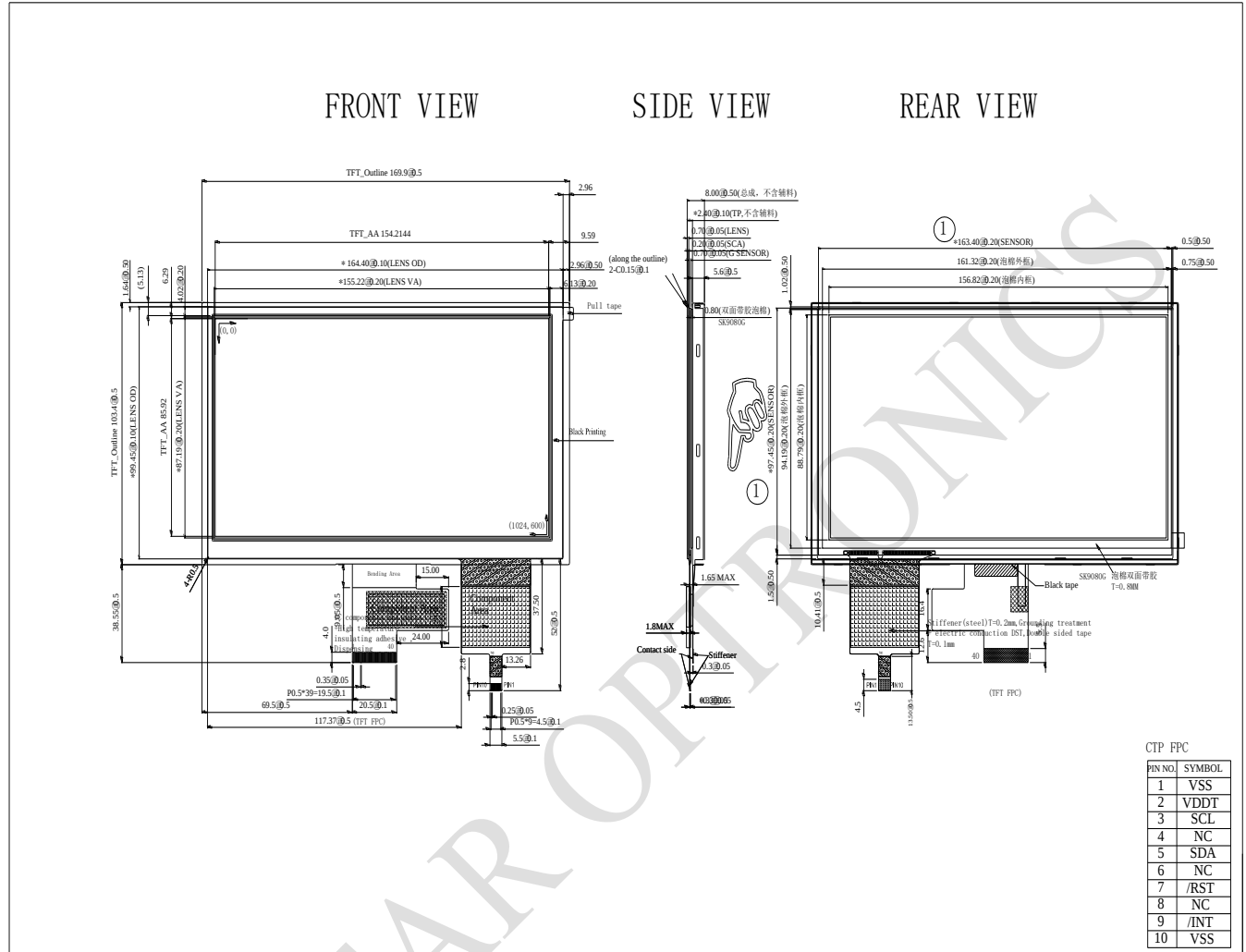
Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30℃ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/Humidity Operation	The module should be allowed to stand at 60℃,90%RH max	60℃,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation 	-20℃/70℃ 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact) ,±800v(air), RS=330Ω CS=150pF 10 times	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

12.Touch Panel Information



LCM Sample Estimate Feedback Sheet

Module Number : _____

1 、 Panel Specification :

1. Panel Type :	☐ Pass	☐ NG , _____
2. View Direction :	☐ Pass	☐ NG , _____
3. Numbers of Dots :	☐ Pass	☐ NG , _____
4. View Area :	☐ Pass	☐ NG , _____
5. Active Area :	☐ Pass	☐ NG , _____
6. Operating Temperature :	☐ Pass	☐ NG , _____
7. Storage Temperature :	☐ Pass	☐ NG , _____
8. Others : _____		

2 、 Mechanical Specification :

1. PCB Size :	☐ Pass	☐ NG , _____
2. Frame Size :	☐ Pass	☐ NG , _____
3. Material of Frame :	☐ Pass	☐ NG , _____
4. Connector Position :	☐ Pass	☐ NG , _____
5. Fix Hole Position :	☐ Pass	☐ NG , _____
6. Backlight Position :	☐ Pass	☐ NG , _____
7. Thickness of PCB :	☐ Pass	☐ NG , _____
8. Height of Frame to PCB :	☐ Pass	☐ NG , _____
9. Height of Module :	☐ Pass	☐ NG , _____
10. Others :	☐ Pass	☐ NG , _____

3 、 Relative Hole Size :

1. Pitch of Connector :	☐ Pass	☐ NG , _____
2. Hole size of Connector :	☐ Pass	☐ NG , _____
3. Mounting Hole size :	☐ Pass	☐ NG , _____
4. Mounting Hole Type :	☐ Pass	☐ NG , _____
5. Others :	☐ Pass	☐ NG , _____

4 、 Backlight Specification :

1. B/L Type :	☐ Pass	☐ NG , _____
2. B/L Color :	☐ Pass	☐ NG , _____
3. B/L Driving Voltage (Reference for LED Type) : ☐ Pass ☐ NG , _____		
4. B/L Driving Current :	☐ Pass	☐ NG , _____
5. Brightness of B/L :	☐ Pass	☐ NG , _____
6. B/L Solder Method :	☐ Pass	☐ NG , _____
7. Others :	☐ Pass	☐ NG , _____

>> **Go to page 2** <<

Module Number : _____

5 、 Electronic Characteristics of Module :

1.Input Voltage :	☐ Pass	☐ NG , _____
2.Supply Current :	☐ Pass	☐ NG , _____
3.Driving Voltage for LCD :	☐ Pass	☐ NG , _____
4.Contrast for LCD :	☐ Pass	☐ NG , _____
5.B/L Driving Method :	☐ Pass	☐ NG , _____
6.Negative Voltage Output :	☐ Pass	☐ NG , _____
7.Interface Function :	☐ Pass	☐ NG , _____
8.LCD Uniformity :	☐ Pass	☐ NG , _____
9.ESD test :	☐ Pass	☐ NG , _____
10.Others :	☐ Pass	☐ NG , _____

6 、 Summary :
Sales signature : _____

Customer Signature : _____

Date : / /