

30V 80A N-Channel Enhancement Mode Power MOSFET

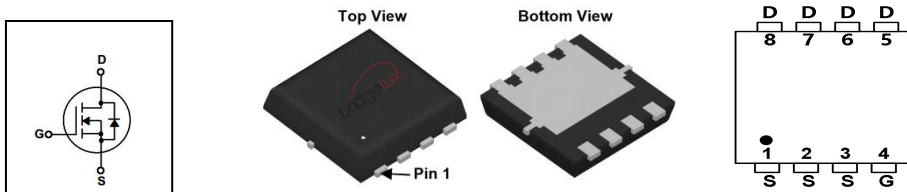
Features

- $R_{DS(on)} \leq 4m\Omega$ @ $V_{GS}=10V$, $I_D=20A$
- Advanced trench technology
- Excellent $R_{DS(on)}$ and Low Gate Charge
- Lead free product is acquired

Application

- Load Switch
- PWM Application
- Power management

SYMBOL



PDFN5*6

ASSEMBLY MESSAGE

Product Name	Package	Packaging
BXT040N03C	PDFN5*6	Reel

ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Rating	Unit
			PDFN5*6	
Drain-Source Voltage		V_{DS}	30	V
Drain Current	Continuous ($T_C = 25^\circ\text{C}$)	I_D	80	A
	Continuous ($T_C = 100^\circ\text{C}$)		51	A
Drain Current	Pulsed (Note1)	I_{DM}	320	A
Single Pulsed Avalanche Energy		EAS	121	mJ
Gate-Source Voltage		V_{GS}	± 20	V
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	44	W
Maximum Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	-55 to 150	$^\circ\text{C}$

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

THERMAL CHARACTERISTICS

Parameter	Symbol	Max.	Unit
		PDFN5*6	
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.84	$^\circ\text{C} / \text{W}$

ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise Noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V, ID=250μA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	VDS=30V, VGS=0V			1	uA
Gate-Body Leakage Current, Forward	I _{GSS}	VGS=20V			100	nA
Gate-Body Leakage Current, Reverse		VGS=-20V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	VDS=VGS, ID=250μA	1.0	1.5	3.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	VGS=10V, ID=20A		2.9	4	mΩ
		VGS=4.5V, ID=10A		4.2	6.5	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	VDS=15V, VGS=0V, f=1.0MHz		2612		pF
Output Capacitance	C _{OSS}			396		pF
Reverse Transfer Capacitance	C _{RSS}			336		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	t _{D(ON)}	VDD=15V, ID=30A, VGS = 10V, RG=3Ω		21		ns
Turn-ON Rise Time	t _R			29		ns
Turn-OFF Delay Time	t _{D(OFF)}			70		ns
Turn-OFF Fall-Time	t _F			33		ns
Total Gate Charge(Note2)	Q _G	VDS =15V, VGS =10V, ID =30A		30		nC
Gate Source Charge	Q _{GS}			8		nC
Gate Drain Charge	Q _{GD}			10		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage	V _{SD}	I _S =30A, VGS=0V			1.4	V
Diode Continuous Forward Current	I _S				80	A
Maximum Pulsed Drain to Source Diode Forward Current	I _{SM}				320	A
Body Diode Reverse Recovery Time	t _{rr}	I _F =20A, dI/dt=100A/μs		25		ns
Body Diode Reverse Recovery Charge	Q _{rr}			20		nC

Note: 2. Essentially independent of operating temperature

TYPICAL CHARACTERISTICS

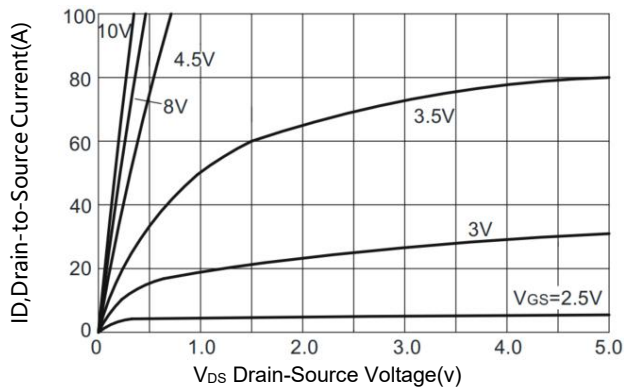


Figure1. Typical Output Characteristics

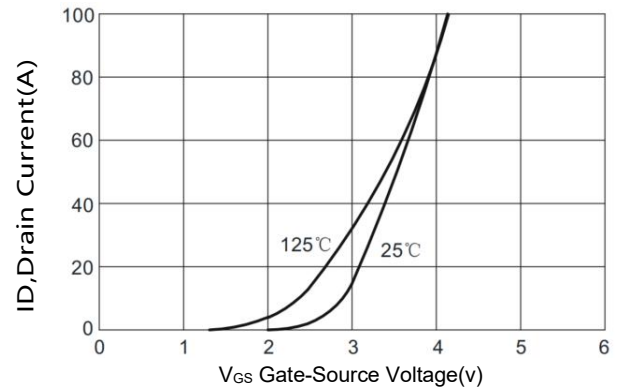


Figure2. Typical Transfer Characteristics

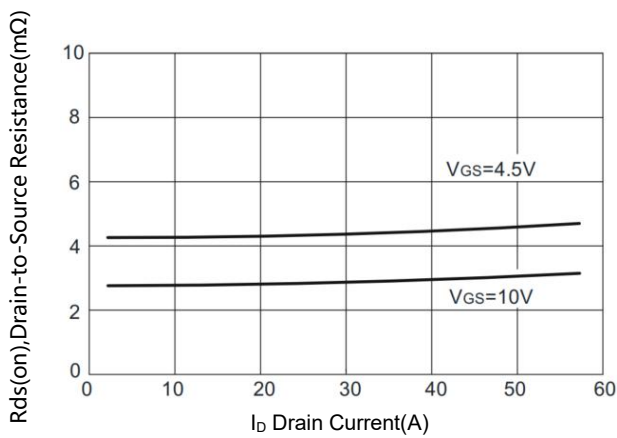


Figure3. On-Resistance versus Drain Current

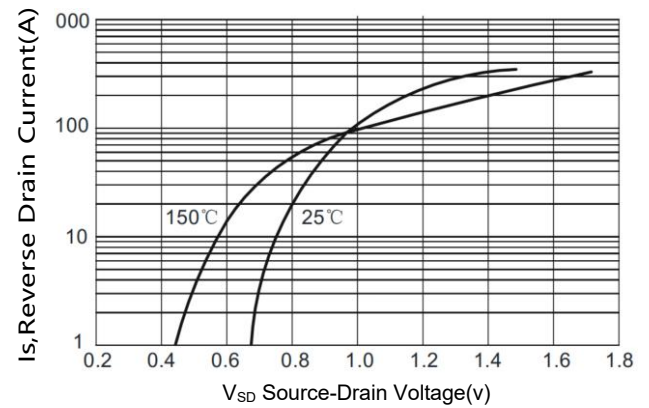


Figure4. Diode forward voltage versus Current

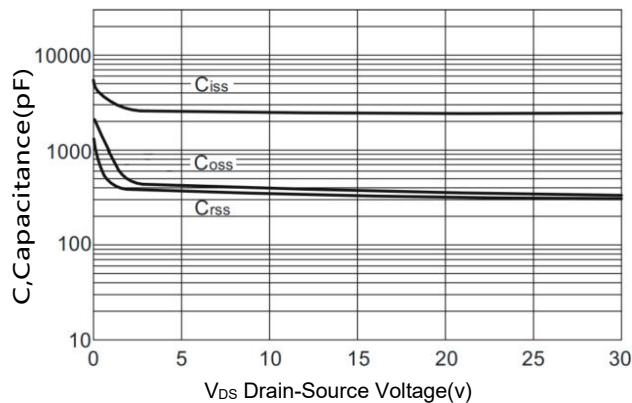


Figure5. Typical Capacitance versus V_{DS}

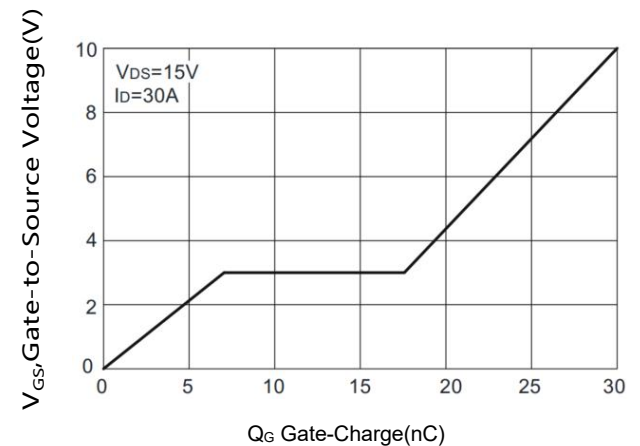


Figure6. Typical Gate Charge versus V_{GS}

TYPICAL CHARACTERISTICS(Cont.)

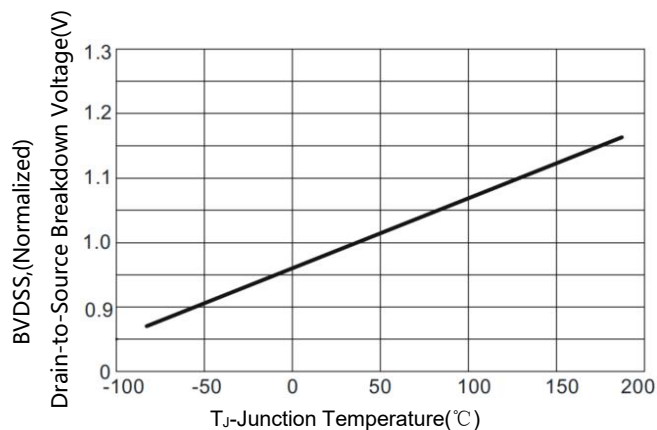


Figure7. BV_{DSS} Variation with Temperature

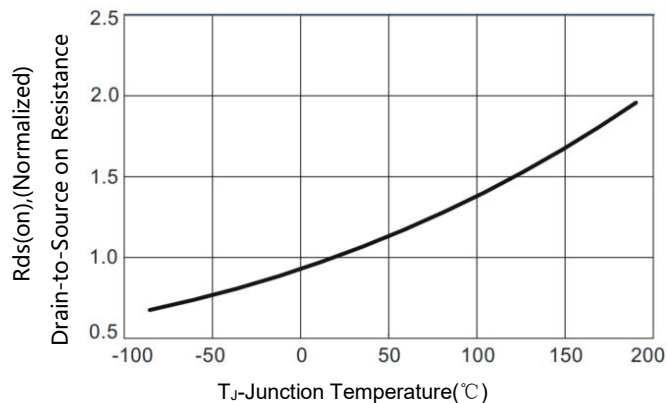


Figure8. On-Resistance Variation with Temperature

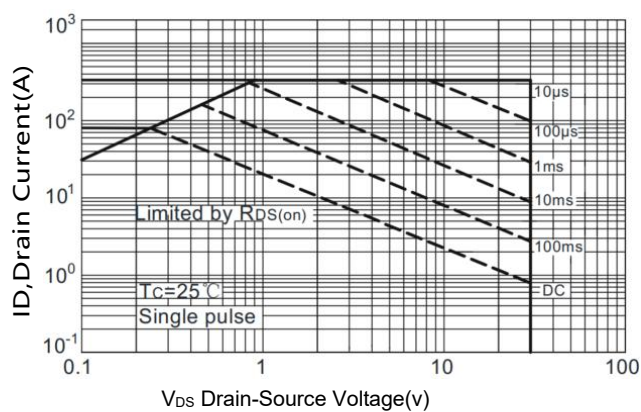


Figure9. Maximum Safe Operating Area

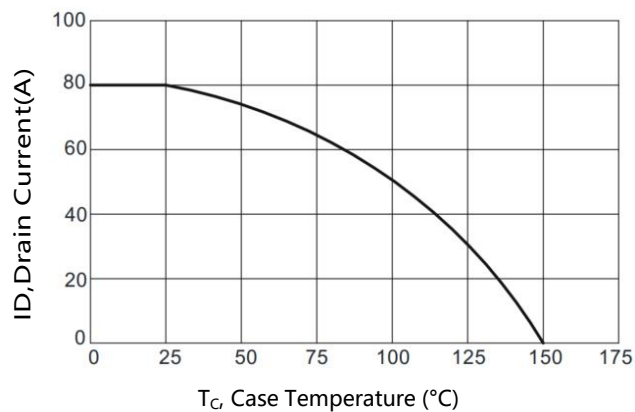
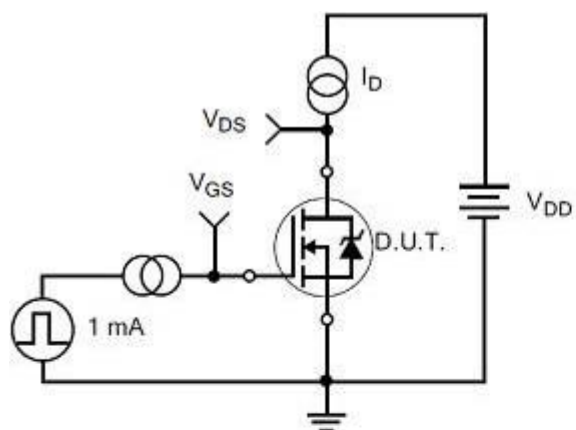
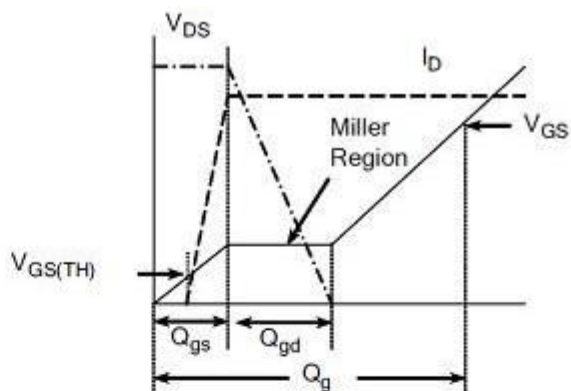


Figure10. Maximum Continuous Drain Current versus Case Temperature

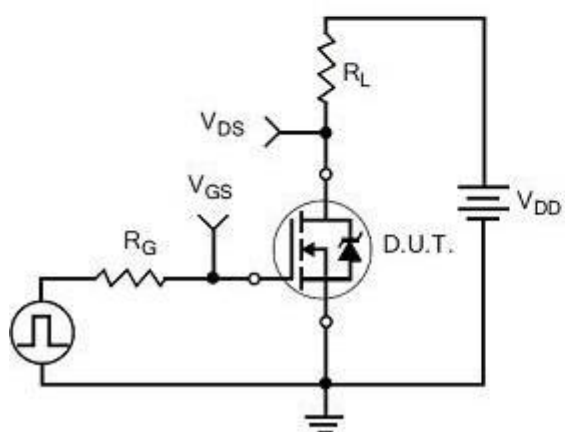
TEST CIRCUITS AND WAVEFORMS



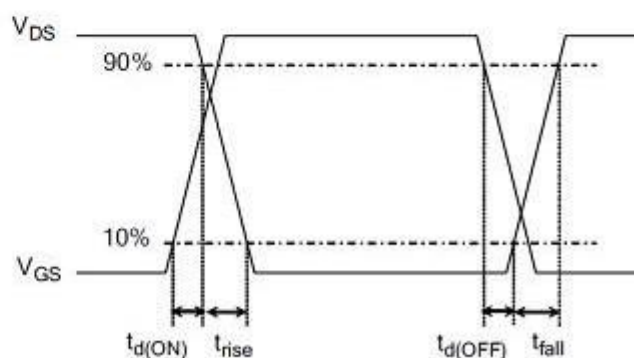
Gate Charge Test Circuit



Gate Charge Waveform

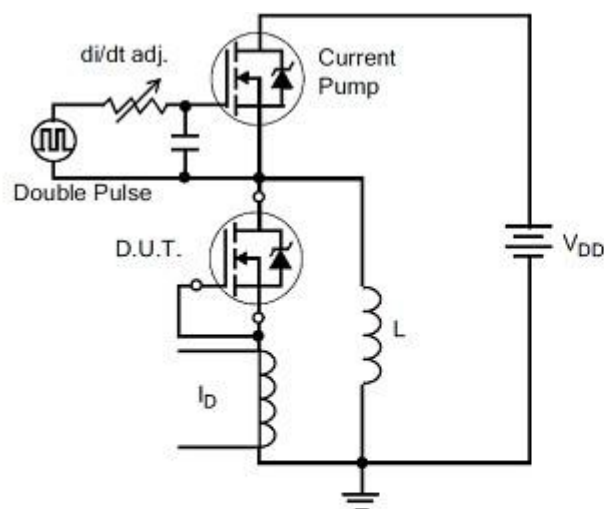


Resistive Switching Test Circuit

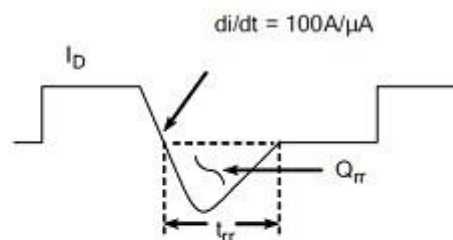


Resistive Switching Waveforms

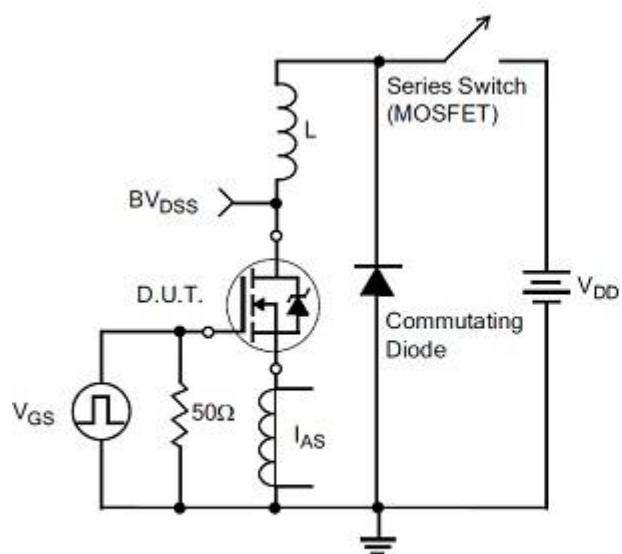
TEST CIRCUITS AND WAVEFORMS(Cont.)



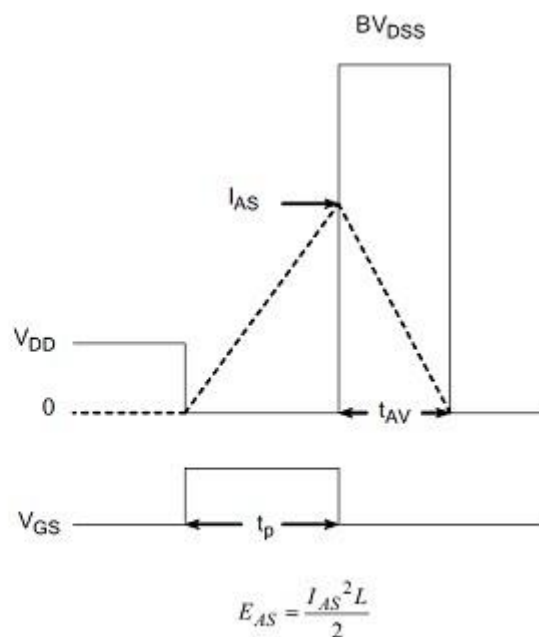
Diode Reverse Recovery Test Circuit



Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

Revision history

Document revision history

Date	Revision	Changes
15-Nov-2021	1.0	First release

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