HEF40244B

Octal buffers with 3-state outputs Rev. 4 — 29 June 2018

Product data sheet

General description

The HEF40244B is an octal non-inverting buffer with 3-state outputs. It features output stages with high current output capability suitable for driving highly capacitive loads.

The 3-state outputs are controlled by the output enable inputs EOA and EOB. A HIGH on EOA or EOB causes the outputs to assume a high impedance OFF-state. The device also features hysteresis on all inputs to improve noise immunity.

Schmitt-trigger action in the inputs makes the circuit highly tolerant to slower input rise and fall times.

Features and benefits

- · Octal bus interface
- 3-state buffers
- Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- · Standardized symmetrical output characteristics
- Specified from -40 °C to +85 °C

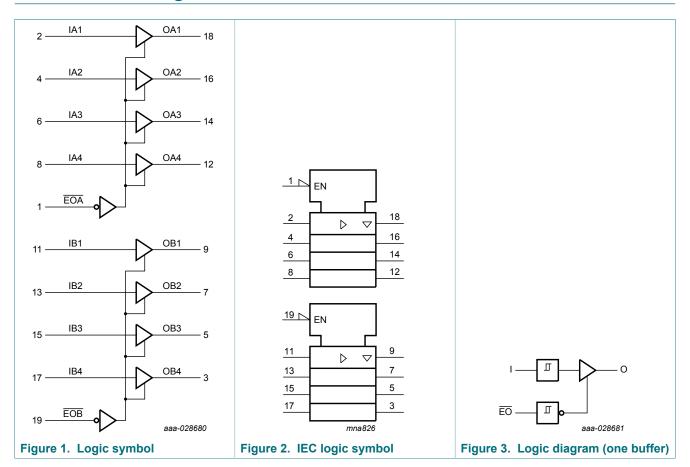
Ordering information

Table 1. Ordering information

| Type number | Package | | | |
|-------------|-------------------|------|---|----------|
| | Temperature range | Name | Description | Version |
| HEF40244BT | -40 °C to +85 °C | SO20 | plastic small outline package; 20 leads; body width 7.5 mm | SOT163-1 |

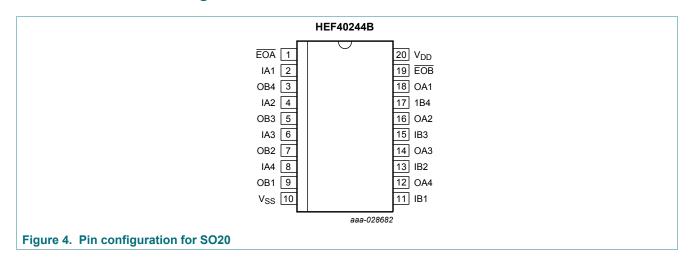


4 Functional diagram



5 Pinning information

5.1 Pinning



HEF40244B

5.2 Pin description

Table 2. Pin description

| Symbol | Pin Description | |
|--------------------|-----------------|-----------------------------------|
| EOA, EOB | 1, 19 | output enable inputs (active low) |
| IA1, IA2, IA3, IA4 | 2, 4, 6, 8 | data inputs |
| OA1, OA2, OA3, OA4 | 18, 16, 14, 12 | data outputs |
| IB1, IB2, IB3, IB4 | 11, 13, 15, 17 | data inputs |
| OB1, OB2, OB3, OB4 | 9, 7, 5, 3 | data outputs |
| V_{SS} | 10 | ground supply voltage |
| V_{DD} | 20 | supply voltage |

6 Functional description

Table 3. Function table [1]

| Control | Input | Output |
|------------|------------|------------|
| EOA or EOB | IAn or IBn | OAn or OBn |
| L | L | L |
| L | Н | Н |
| Н | X | Z |

^[1] H = HIGH voltage level;

7 Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|-------------------------|------------------------------|------|-----------------------|------|
| V_{DD} | supply voltage | | -0.5 | +18 | V |
| VI | input voltage | | -0.5 | V _{DD} + 0.5 | V |
| I _{DD} | supply current | | - | ±100 | mA |
| I _{IK} | input clamping current | | - | ±10 | mA |
| I _{OK} | output clamping current | | - | ±25 | mA |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| T _{amb} | ambient temperature | | -40 | +85 | °C |
| P _{tot} | total power dissipation | T_{amb} = -40 °C to +85 °C | | | |
| | | SO20 package [1] | - | 500 | mW |
| Р | power dissipation | per output | - | 100 | mW |

^[1] For SO20 package: P_{tot} derates linearly with 8 mW/K above 70 °C.

HEF40244B

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L = LOW voltage level;

X = don't care;

Z = high-impedance OFF-state.

8 Recommended operating conditions

Table 5. Operating conditions

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|-------------------------------------|--|-----|----------|------|
| V_{DD} | supply voltage | referenced to V _{SS} (usually ground) | 3 | 15 | V |
| VI | input voltage | | 0 | V_{DD} | V |
| T _{amb} | ambient temperature | in free air | -40 | +85 | °C |
| Δt/ΔV | input transition rise and fall rate | V _{DD} = 5 V | - | 3.75 | μs/V |
| | | V _{DD} = 10 V | - | 0.5 | μs/V |
| | | V _{DD} = 15 V | - | 0.08 | μs/V |

9 Static characteristics

Table 6. Static characteristics

 $V_{SS} = 0 \ V$; $V_I = V_{SS}$ or V_{DD} unless otherwise specified.

| Symbol | Parameter | Conditions | | T _{amb} = | -40 °C | Ta | _{mb} = 25 | °C | T _{amb} = | T _{amb} = 85 °C | |
|-----------------|---------------------------|----------------------------------|----------|--------------------|--------|-------|--------------------|------|--------------------|--------------------------|----|
| | | | V_{DD} | Min | Max | Min | Тур | Max | Min | Max | |
| V_{IH} | HIGH-level | I _O < 1 μA | | | | | | | | | |
| | input voltage | V _O = 0.5 V or 4.5 V | 5 V | 3.5 | - | 3.5 | - | - | 3.5 | - | V |
| | | V _O = 1.0 V or 9.0 V | 10 V | 7.0 | - | 7.0 | - | - | 7.0 | - | V |
| | | V _O = 1.5 V or 13.5 V | 15 V | 11.0 | - | 11.0 | - | - | 11.0 | - | V |
| V _{IL} | LOW-level | I _O < 1 μA | | | | | | | | | |
| | input voltage | V _O = 0.5 V or 4.5 V | 5 V | - | 1.5 | - | - | 1.5 | - | 1.5 | V |
| | | V _O = 1.0 V or 9.0 V | 10 V | - | 3.0 | - | - | 3.0 | - | 3.0 | V |
| | | V _O = 1.5 V or 13.5 V | 15 V | - | 4.0 | - | - | 4.0 | - | 4.0 | V |
| V _{OH} | HIGH-level output voltage | I _O < 1 μA | 5 V | 4.95 | - | 4.95 | - | - | 4.95 | - | V |
| | | | 10 V | 9.95 | - | 9.95 | - | - | 9.95 | - | V |
| | | | 15 V | 14.95 | - | 14.95 | - | - | 14.95 | - | V |
| V _{OL} | LOW-level | I _O < 1 μA | 5 V | - | 0.05 | - | - | 0.05 | - | 0.05 | V |
| | output voltage | | 10 V | - | 0.05 | - | - | 0.05 | - | 0.05 | V |
| | | | 15 V | - | 0.05 | - | - | 0.05 | - | 0.05 | V |
| I _{OH} | HIGH-level output current | see Figure 5 and Figure 6 | | | | | | | | | |
| | | V _{OH} = 3.6 V | 5 V | -9.3 | - | -10 | -24 | - | -10.7 | - | mA |
| | | V _{OH} = 4.6 V | 5 V | -0.75 | - | -0.6 | -1.2 | - | -0.45 | - | mA |
| | | V _{OH} = 8.4 V | 10 V | -14.4 | - | -15 | -46 | - | -15 | - | mA |
| | | V _{OH} = 9.5 V | 10 V | -1.85 | - | -1.5 | -3.0 | - | -1.1 | - | mA |
| | | V _{OH} = 13.2 V | 15 V | -19.5 | - | -20 | -62 | - | -19.8 | - | mA |
| | | V _{OH} = 13.5 V | 15 V | -14.5 | - | -15 | -50 | - | -15.5 | - | mA |

Nexperia HEF40244B

Octal buffers with 3-state outputs

| Symbol | Parameter | Conditions | | T _{amb} = | -40 °C | Ta | _{mb} = 25 | °C | T _{amb} = 85 °C | | Unit |
|-----------------|--------------------------|----------------------------------|----------|--------------------|--------|-----|--------------------|------|--------------------------|-------|------|
| | | | V_{DD} | Min | Max | Min | Тур | Max | Min | Max | |
| I _{OL} | LOW-level | V _{OL} = 0.4 V | 5 V | 2.9 | - | 2.3 | 5.4 | - | 1.75 | - | mA |
| | output current | V _{OL} = 0.5 V | 10 V | 9.5 | - | 7.6 | 17 | - | 5.5 | - | mA |
| | | V _{OL} = 1.5 V | 15 V | 30.0 | - | 25 | 45 | - | 19.0 | - | mA |
| I ₁ | input leakage current | [1] | 15 V | - | ±0.3 | - | - | ±0.3 | - | ±1.0 | μΑ |
| I _{OZ} | OFF-state output current | $V_O = V_{DD}$ | 15 V | - | 1.6 | - | - | 1.6 | - | 12.0 | μΑ |
| | | V _O = V _{SS} | 15 V | - | -1.6 | - | - | -1.6 | - | -12.0 | μΑ |
| I _{DD} | supply current | I _O = 0 A | 5 V | - | 4.0 | - | - | 4.0 | - | 30 | μΑ |
| | | | 10 V | - | 8.0 | - | - | 8.0 | - | 60 | μΑ |
| | | | 15 V | - | 16.0 | - | - | 16.0 | - | 120 | μΑ |
| V_{H} | hysteresis | | 5 V | - | - | - | 220 | - | - | - | mV |
| | voltage | | 10 V | - | - | - | 250 | - | - | - | mV |
| | | | 15 V | - | - | - | 320 | - | - | - | mV |
| Cı | input capacitance | | , | - | - | - | 7.5 | - | - | - | pF |

^[1] Unused inputs must be connected to $V_{DD},\,V_{SS}$ or another input.

10 Dynamic characteristics

Table 7. Dynamic characteristics

 $V_{SS} = 0 \text{ V}$; $T_{amb} = 25 \text{ °C}$; unless otherwise specified; for waveform and test circuit, see Figure 10.

| Symbol | Parameter | Conditions | Extrapolation formula | Min | Тур | Max | Unit |
|------------------|-------------------------------|---------------------------------------|------------------------------------|-----|-----|-----|------|
| t _{PHL} | HIGH to LOW propagation delay | IAn to OAn; IBn to OBn; see Figure 7. | | | | | |
| | | V _{DD} = 5 V | 83 ns + (0.24 ns/pF)C _L | - | 95 | 190 | ns |
| | | V _{DD} = 10 V | 35 ns + (0.10 ns/pF)C _L | - | 40 | 80 | ns |
| | | V _{DD} = 15 V | 26 ns + (0.07 ns/pF)C _L | - | 30 | 60 | ns |
| t _{PLH} | LOW to HIGH propagation delay | IAn to OAn; IBn to OBn; see Figure 7. | | | | | |
| | | V _{DD} = 5 V | 82 ns + (0.06 ns/pF)C _L | - | 85 | 170 | ns |
| | | V _{DD} = 10 V | 38 ns + (0.03 ns/pF)C _L | - | 40 | 80 | ns |
| | | V _{DD} = 15 V | 29 ns + (0.02 ns/pF)C _L | - | 30 | 60 | ns |

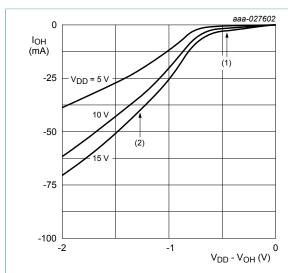
| Symbol | Parameter | Conditions | Extrapolation formula | Min | Тур | Max | Unit |
|--|-------------------------------------|---------------------------------------|-----------------------|-----|-----|-----|------|
| t _{PZH} | OFF-state to HIGH propagation delay | EOA to OAn; EOB to OBn; see Figure 9. | | | | | |
| | | V _{DD} = 5 V | | - | 80 | 160 | ns |
| | | V _{DD} = 10 V | | - | 35 | 70 | ns |
| | | V _{DD} = 15 V | | - | 30 | 60 | ns |
| t _{PZL} | OFF-state to LOW propagation delay | EOA to OAn; EOB to OBn; see Figure 9. | | | | | |
| | | V _{DD} = 5 V | | - | 90 | 180 | ns |
| | | V _{DD} = 10 V | | - | 40 | 80 | ns |
| | | V _{DD} = 15 V | | - | 30 | 60 | ns |
| t _{PHZ} HIGH to OFF-state propagation delay | | EOA to OAn; EOB to OBn; see Figure 9. | | | | | |
| | | V _{DD} = 5 V | | - | 70 | 140 | ns |
| | | V _{DD} = 10 V | | - | 35 | 70 | ns |
| | | V _{DD} = 15 V | | - | 30 | 60 | ns |
| t _{PLZ} | LOW to OFF-state propagation delay | EOA to OAn; EOB to OBn; see Figure 9. | | | | | |
| | | V _{DD} = 5 V | | - | 75 | 150 | ns |
| | | V _{DD} = 10 V | | - | 40 | 80 | ns |
| | | V _{DD} = 15 V | | - | 30 | 60 | ns |
| t _{THL} | HIGH to LOW output transition time | OAn; OBn; see Figure 7 and Figure 8. | | | | | |
| | | V _{DD} = 5 V | | - | 40 | 80 | ns |
| | | V _{DD} = 10 V | | - | 20 | 40 | ns |
| | | V _{DD} = 15 V | | - | 15 | 30 | ns |
| t _{TLH} | LOW to HIGH output transition time | OAn; OBn; see Figure 7 and Figure 8. | | | | | |
| | | V _{DD} = 5 V | | - | 30 | 60 | ns |
| | | V _{DD} = 10 V | | - | 20 | 40 | ns |
| | | V _{DD} = 15 V | | - | 15 | 30 | ns |

^[1] The typical values of the propagation delay are calculated from the extrapolation formulas shown (C_L in pF).

Table 8. Dynamic power dissipation

| Symbol | Parameter | V_{DD} | Typical formula | where: |
|--------|---------------|----------|---|--|
| P_D | dynamic power | 5 V | . (** = 2, = 2 (; *) | f _i = input frequency in MHz; |
| | dissipation | 10 V | $P_D = 17000 \times f_i + \Sigma(f_0 \times C_L) \times V_{DD}^2 (\mu W)$ | f _o = output frequency in MHz; C _L = output load capacitance in pF; |
| | | 15 V | | $\Sigma(f_0 \times C_L)$ = sum of the outputs; V_{DD} = supply voltage in V. |

10.1 Waveforms and test circuit



- (1) P-channel MOS transistor conducting.
- (2) P-channel MOS transistor and bipolar n-p-n transistor conducting.

Figure 5. Typical output source current characteristic.

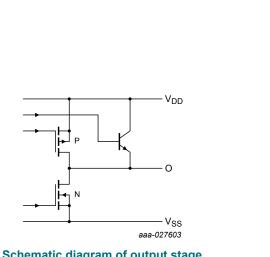
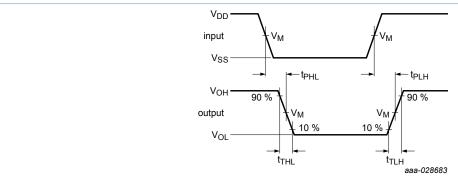


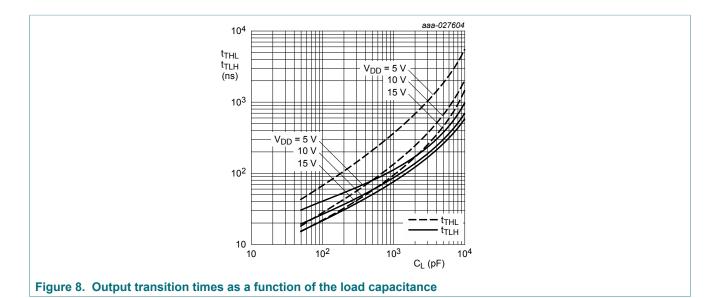
Figure 6. Schematic diagram of output stage.

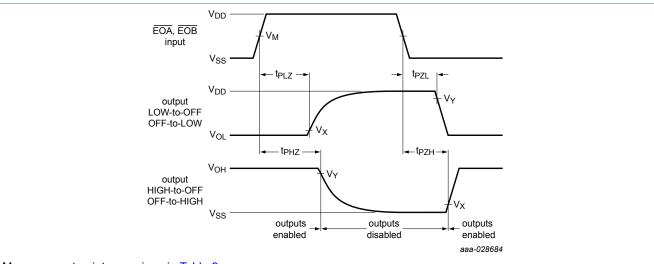


Measurement points are given in Table 9.

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Figure 7. Input (IAn; IBn) to output (OAn; OBn) propagation delays and output transition time.





Measurement points are given in Table 9.

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

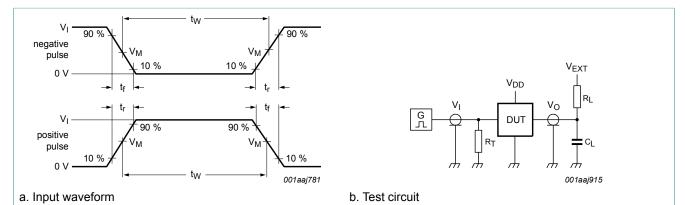
Figure 9. 3-state enable and disable times

Table 9. Measurement points

| Supply voltage | Input | Output | | | | |
|----------------|--------------------|--------------------|--------------------|--------------------|--|--|
| V_{DD} | V_{M} | V _M | V _X | V_{Y} | | |
| 5 V to 15 V | 0.5V _{DD} | 0.5V _{DD} | 0.1V _{DD} | 0.9V _{DD} | | |

Nexperia HEF40244B

Octal buffers with 3-state outputs



Test and measurement data is given in <u>Table 10</u>.

Definitions test circuit:

R_L = Load resistance.

 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator.

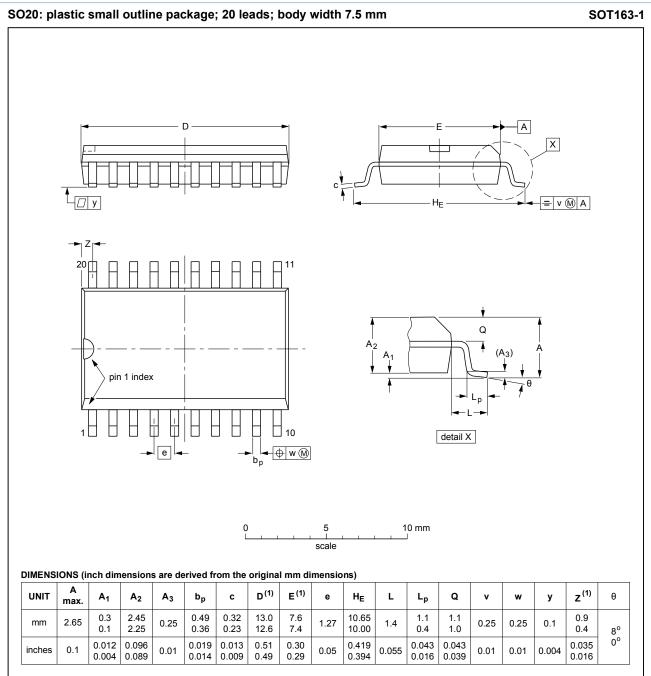
 C_L = Load capacitance including jig and probe capacitance.

Figure 10. Test circuit for measuring switching times

Table 10. Test data

| Supply voltage | Input | | Load | | V _{EXT} | | |
|-----------------|----------|---------------------------------|-------|----------------|-------------------------------------|-------------------------------------|-------------------------------------|
| V _{DD} | VI | t _r , t _f | CL | R _L | t _{PLH} , t _{PHL} | t _{PHZ} , t _{PZH} | t _{PLZ} , t _{PZL} |
| 5 V to 15 V | V_{DD} | ≤ 20 ns | 50 pF | 1 kΩ | open | V_{SS} | V_{DD} |

11 Package outline



Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

| OUTLINE VERSION | REFERENCES | | | EUROPEAN | ISSUE DATE | |
|--------------------|------------|--------|-------|----------|------------|----------------------------------|
| | IEC | JEDEC | JEITA | | PROJECTION | 133UE DATE |
| SOT163-1 | 075E04 | MS-013 | | | | -99-12-27 03-02-19 |

Figure 11. Package outline SOT163-1 (SO20)

HEF40244B

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12 Abbreviations

Table 11. Abbreviations

| Acronym | Description |
|---------|-------------------|
| DUT | Device Under Test |

13 Revision history

Table 12. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------|---|-----------------------|---------------|---------------|
| HEF40244B v.4 | 20180629 | Product data sheet | - | HEF40244B v.3 |
| Modifications: | The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. | | | |
| HEF40244B v.3 | 19950101 | Product specification | - | HEF40244B v.2 |
| HEF40244B v.2 | 19950101 | Product specification | - | HEF40244B v.1 |

14 Legal information

14.1 Data sheet status

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|-----------------------------------|-------------------------------|---|
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Octal buffers with 3-state outputs

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Contents

| 1 | General description | 1 |
|------|----------------------------------|---|
| 2 | Features and benefits | |
| 3 | Ordering information | 1 |
| 4 | Functional diagram | |
| 5 | Pinning information | |
| 5.1 | Pinning | |
| 5.2 | Pin description | |
| 6 | Functional description | |
| 7 | Limiting values | |
| 3 | Recommended operating conditions | |
| 9 | Static characteristics | |
| 10 | Dynamic characteristics | |
| 10.1 | Waveforms and test circuit | |
| 11 | Package outline | |
| 12 | Abbreviations | |
| 13 | Revision history | |
| 14 | Legal information | |

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